

SREENIVASA INSTITUTE OF TECHNOLOGY AND MANAGEMENT STUDIES, CHITTOOR.



**(AUTONOMOUS)
EXAMINATION BRANCH**

R13

I.M.TECH II SEMESTER (R13) REGULAR END EXAMINATIONS-OCTOBER /NOVEMBER 2020

Dt:24-10-2020

Time Table

Time: 10.00 AM to 1.00 PM

DATE	CSE	VLSI SYSTEM DESIGN	CS
02-11-2020	Data Warehousing and Mining(13MCSE21)	Testing & Testability (13MVS D 21)	Wireless Communication and Fading (13MCOS 21)
04-11-2020	Software Quality Assurance and Testing (13MCSE22)	Low Power VLSI Design(13MVSD 22)	Digital Satellite Communication (13MCOS 22)
06-11-2020	Software Architecture and Design Patterns (13MCSE23)	Algorithms for VLSI Design Automation (13MVSD 23)	Detection and Estimation of Signals(13MCOS 23)
09-11-2020	Ad-hoc and Sensor Networks(13MCSE24)	FPGA Architecture & Application (13MVSD 24)	Optical Fiber Communication(13MCOS 24)
11-11-2020	Cryptography and Networks Security(13MCSE25)	Scripting Language for VLSI Design Automation (13MVSD 25)	\ RF Systems and Circuits(13MCOS 25)
13-11-2020	Distributed Databases(13MCSE26A)	Hardware Software Code sign (13MVSD 26C)	Image and Videos Processing (13MCOS26C)

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Controller of Examinations

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Principal