



Sreenivasa Institute of Technology and Management Studies: Chittoor (Autonomous)
Examination Branch

Dt.20-01-2023

Circular

Sub:- II B.Tech III semester (R20) -Regular/Supple -Schedule -Practical
Examinations -Reg.

It is here by informed to the Regular/supplementary students of II B.Tech - III semester (R20) that, the **Practical Examinations** will be conducted from **30.01.2023 to 08.02.2023**. Hence the HODs are requested to prepare the lab-wise schedule and conduct the practical examinations based on rubrics assigned by the department. The last date to submit the following practical examination particulars to the examination section on or before **10.02.2023**.

- Answer scripts along with students signed attendance particulars (sealed cover)
- Award list with Chief Superintendent Signature (sealed cover)
- D-form (as per the format given)
- Remuneration bill (as per the format given)


CONTROLLER OF EXAMINATIONS


PRINCIPAL

Copy to:

- 1) The Dean (Academics)
- 2) The HODs of CE, EEE, ME, ECE, CSE & AIML
- 3) Copy to the notice board
- 4) Copy to Computer Centre
- 5) Copy to the Transport I/C



Sreenivasa Institute of Technology and Management Studies: Chittoor (Autonomous)
Examination Branch
II B.Tech III Semester R20 Regular/Supplementary Practical January/February 2023 Examinations

Schedule	CIVIL	EEE	MECH	ECE	CSE	CSE (DS)	CSE (AIML)	CSE (AI)
30.01.2023 To 08.02.2023	Building Planning using AUTOCAD (SOC)	20ECE235 Electronic Circuit Analysis Lab (SOC)	--	20HSM231 Soft Skills (SOC)	20CSE234 Advanced Data Structures with C++ (SOC)	20CSD231 Advanced Python Programming (SOC)	20CSM232 Web Programming (SOC)	20CSM232 Web Programming (SOC)
	20ESC236 - Strength of Materials Lab	20ESC232 Fluid Mechanics and Machinery Lab	20ESC232 Fluid Mechanics and Machinery Lab	20ESC234 Principles of Electrical Engineering Lab	20ESC238 Digital Logic Design and Microprocessor Lab	20ESC238 Digital Logic Design and Microprocessor Lab	20ESC238 Digital Logic Design and Microprocessor Lab	20ESC238 Digital Logic Design and Microprocessor Lab
	20CIV235 - Fluid Mechanics Lab	20ECE238 Semiconductor Devices and circuits Lab	20MEC235 Material Science Lab and Manufacturing Technology Lab	20ECE234 Digital Electronics Lab	20CSE235 Design and Analysis of Algorithms Lab	20CSE235 Design and Analysis of Algorithms Lab	20CSM233 Advanced Data Structures and Algorithms Lab	20CSM233 Advanced Data Structures and Algorithms Lab
	20CIV236 - Surveying Lab	20EEE232 Network Analysis Lab	20MEC236 Mechanics of Solids Lab	20ECE235 Electronic Circuit Analysis lab	20CSE236 Programming with JAVA Lab	20CSE236 Programming with JAVA Lab	20CSE236 Programming with JAVA Lab	20CSE236 Programming with JAVA Lab

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Sreenivasa Institute of Technology and Management Studies: Chittoor (Autonomous)
Examination Branch

Dt.20-01-2023

Circular

Sub:- II B.Tech I semester (R13/R16/R18) – Supplementary - Practical
Examinations –Schedule - Reg.

It is here by informed to the Supplementary students of II B.Tech I semester (R13/R16/R18) that, the **Practical Examinations** will be conducted from **30.01.2023 to 08.02.2023**. Hence the HODs are requested to prepare the lab-wise schedule and conduct the practical examinations based on rubrics assigned by the department. The last date to submit the following practical examination particulars to the examination section on or before **10.02.2023**.

- Answer scripts along with students signed attendance particulars (sealed cover)
- Award list with Chief Superintendent Signature (sealed cover)
- D-form (as per the format given)
- Remuneration bill (as per the format given)


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