



SREENIVASA INSTITUTE OF TECHNOLOGY AND MANAGEMENT STUDIES
(Autonomous)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
B.Tech R20::HONOR COURSE STRUCTURE AND SYLLABI

1. Student shall earn 20 additional credits to be eligible for the award of B. Tech (Honors) degree (16 credits shall be earned by undergoing specified courses listed as pools, with four courses, each carrying 4 credits, the remaining 4 credits must be acquired through two MOOCs, which shall be domain specific, each with 2 credits and with a minimum duration of 8/12 weeks as recommended by the Board of studies).
2. The subjects opted for Honors should be Advanced type which are not covered in regular curriculum
3. Students has to acquire 16 credits with minimum one subject from each pool

POOL-1(IC Design)

S.No	Course Code	Course Title	Scheme of Instructions Hours per Week				Scheme of Examination Maximum Marks		
			L	T	P	C	I	E	Total
1	20ECEHR1A	IC fabrication Technology	3	1	-	4	30	70	100
2	20ECEHR1B	Analog IC design	3	1	-	4	30	70	100
3	20ECEHR1C	Low power SoC design	3	1	-	4	30	70	100
4	20ECEHR1D	Physics of VLSI devices	3	1	-	4	30	70	100

POOL-2(Signal Processing)

S.No	Course Code	Course Title	Scheme of Instructions Hours per Week				Scheme of Examination Maximum Marks		
			L	T	P	C	I	E	Total
1	20ECEHR2A	Speech and Audio Signal Processing	3	1	-	4	30	70	100
2	20ECEHR2B	Adaptive Signal Processing	3	1	-	4	30	70	100
3	20ECEHR2C	Signal Compression	3	1	-	4	30	70	100
4	20ECEHR2D	Multirate Signal Processing	3	1	-	4	30	70	100

POOL-3 (Modern Communication Systems)

S.No	Course Code	Course Title	Scheme of Instructions Hours per Week				Scheme of Examination Maximum Marks		
			L	T	P	C	I	E	Total
1	20ECEHR3A	Advanced Digital communication	3	1	-	4	30	70	100
2	20ECEHR3B	Cryptography and Network security	3	1	-	4	30	70	100
3	20ECEHR3C	4G wireless Technologies	3	1	-	4	30	70	100
4	20ECEHR3D	Cognitive Radio	3	1	-	4	30	70	100

POOL-4 (Embedded Systems)

S.No	Course Code	Course Title	Scheme of Instructions Hours per Week				Scheme of Examination Maximum Marks		
			L	T	P	C	I	E	Total
1	20ECEHR4A	Embedded system design	3	1	-	4	30	70	100
2	20ECEHR4B	Embedded Networks	3	1	-	4	30	70	100
3	20ECEHR4C	Advanced processors and its applications	3	1	-	4	30	70	100
4	20ECEHR4D	Embedded Real time operating systems	3	1	-	4	30	70	100

BOS-Chairman

Principal