



**SREENIVASA INSTITUTE OF TECHNOLOGY AND MANAGEMENT  
STUDIES (AUTONOMOUS)  
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

**BOARD OF STUDIES Academic Year:2023-2024**

14.09.2023

**Minutes of Board of Studies Meeting**

**No. SITAMS/BOS/EEE/2023**

The Board of Studies meeting of the **Department of Electrical and Electronics Engineering** is held on 14<sup>th</sup> September 2023 at 02.00 PM through Online Mode (Google meet).

**BOARD OF STUDIES MEMBERS:**

S.NO	NAME & DESIGNATION	INSTITUTION	MEMBER
1	Dr.M. Venkatachalapathi PRINCIPAL	SITAMS, CHITTOR	PRINCIPAL
2	Dr.V. MAHESWARI PROFESSOR & HOD/EEE	SITAMS, CHITTOR	BOS CHAIRMAN
3	Dr.P. SUJATHA PROFESSOR	JNTUA, ANANTHAPURUMU	UNIVERSITY NOMINEE
4	Dr.A. HEMACHANDER ASSITANT PROFESSOR	NIT, PUDUCHERRY	ACADEMICIAN - 1
5	Dr.M.S. GIRIDHAR PROFESSOR	LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING MYLAVARAM, ANDRA PRADESH.	ACADEMICIAN - 2
6	Mr.C R SARAVANAN EXECUTIVE ENGINEER	SCADA/DMS MLDC BUILDING GROUND FLOOR,144, ANNA SALAI, CHENNAI-600002	INDUSTRIAL EXPERT
7	Dr. J. JITHENDRANATH SENIOR PROJECT ENGINEER	HITACHI ENERGY, GPQS-INOPC, CHENNAI.	ALUMNI

In the beginning of the meeting Dr.V. Maheswari, Chairman-BoS, welcomed all the members and briefed the about the progress of the **Department of Electrical and Electronics Engineering**.

Dr.N.Venkatachalapathi, Principal, introduced all the board members and briefed about the importance of updated curriculum and syllabus. He requested valuable inputs from the members and handed over the session to Dr.V. Maheswari, Chairman-BOS.



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The BOS Chairman presented the R23 course structure and syllabi of I B. Tech-I Semester and I B.Tech II Semester. It is resolved to accept the same after thorough discussion and taking into consideration the suggestions and remarks given by the members.

**The members of the BOS have given the following suggestions:**

1. Dr P. Sujatha suggested adding the topics differential and Laplace transform to solve the single-phase circuit's problems of Network analysis course in I B.Tech II Semester.

*-The Suggestion is accepted and incorporated*

2. Dr A. Hemachander recommended to change the name of unit 4 from "Resonance Circuits" to Resonance and coupled circuits of Network analysis course in I B.Tech II Semester.

*-The Suggestion is accepted and incorporated*

3. Dr M.S.Giridhar suggested including that "Basic of Laplace transform" in Network analysis course in I B.Tech II Semester.

*-The Suggestion is accepted and incorporated*

4. Dr M.S. Giridhar suggested to allot more class hours for "Basic Electrical and Electronics engineering" subject in I B.Tech I Semester.

*-The Suggestion is accepted and incorporated*

5. Mr.C R Saravanan appreciated for keeping the electricity safety and billing part in Basic electrical engineering syllabus

6. Dr. J. Jithendranath appreciated the inclusion of the content framing of Basic Electrical Engineering syllabus and NSS Activity for students

7. Fundamental subjects are carefully reviewed to be consistent with GATE syllabi to reflect the important concepts emphasized in the GATE examination.

8. BOS Members have suggested Inviting industrial experts to college as well as planning

9. Industrial visits for the students to have exposure to the real-life industrial environment.

10. The BOS members insisted to monitor the academic activities which should follow Outcome Based Education.



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The board has suggested chairman of BoS-EEE to make the necessary changes and corrections in R23 B.Tech curriculum by discussing with curriculum development cell (CDC) members.

The meeting was concluded with a vote of thanks by **Dr.V. Maheswari** Chairman, Board of Studies of Electrical and Electronics Engineering Department.

*University Nominee*  
*(Dr.P. Sujatha)*

*Academician - 1*  
*(Dr A. Hemachander)*

*Academician - 2*  
*(Dr M.S.Giridhar)*

*Industrial Expert*  
*(Mr.C R Saravanan)*

*Alumni*  
*(Dr. J. Jithendranath)*

*Bos Chairman*  
*(Dr.V.Maheswari)*