

Channel Encoder & Decoder

Syllabus: Introduction, types of error control coding: Automatic Repeat Request, Forward Error Control - Linear block codes, error detection & correction capabilities of linear block codes, Binary Cyclic codes, error detection & correction capabilities in BCC, convolutional codes: time domain & frequency domain approach, state diagram, trellis diagram, convolutional decoder: Viterbi & sequential decoder.

Introduction :

- ✓ The purpose of source coding (encoding & decoding) is to convert the discrete message in to bits (0 and 1).
 - ✓ The purpose of channel coding is to detect and correct the errors.
 - * Channel coding is a combination of channel encoding and channel decoding.
 - ✓ The signal passes through some noisy channel, because of noise errors are generated in the received data. These errors can be detected and corrected using Coding techniques.

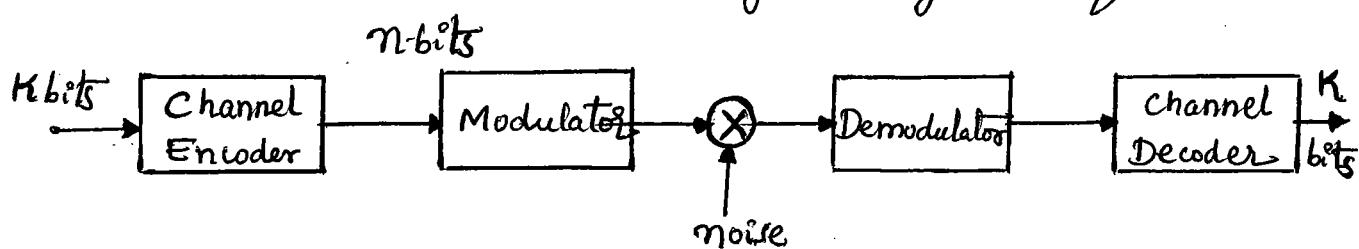


Fig: Channel Coding.

- * Channel Encoder: The channel encoder add extra bits to the message sequence, the adding of extra bits is called parity bits (or) check bits (or) Redundant bits.

* Channel decoder :

The channel decoder identifies this redundant bits and use that to detect and correct the errors in the message bit.

- ✓ The channel encoder converts k bits into n bits by adding extra bits. Such a code is called (n, k) block codes. Redundant bits is equal to $(n-k)$.

Parameters :

- ① Code Word : The encoded block of n bits is called a codeword. It contains message bits ' k ' and parity bits $(n-k)$.
- ② Block length : The no. of bits 'n' after coding is called block length of the code.
- ③ Code rate : The ratio of the message bits (k) and the encoded bits (n) is called code rate.

$$R_c = \frac{k}{n}$$

- ④ Channel data rate : If the bit rate at the input of the encoder is R_s then the channel data rate at the output of the encoder will be ..

$$R_c = \frac{n}{k} \cdot R_s$$

$$\Rightarrow R_c = \frac{R_s}{R_c}$$

where

$R_s \rightarrow$ Source data bit rate,

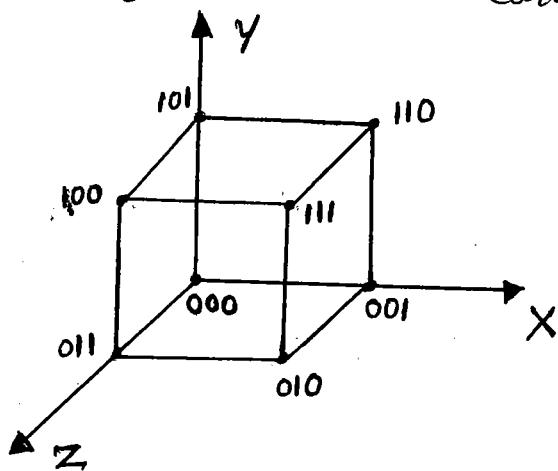
$R_c \rightarrow$ Code rate,

$R_c \rightarrow$ Channel data rate.

⑤ Code vector (or) Code book :

As n -bit code word can be visualize in an n -dimensional space as a vector whose elements are co-ordinates at the bits in the codeword.

i.e Collection of all code words called as code book.



$$K=3 \Rightarrow 2^3 = 8 \text{ code words.}$$

$$K=4 \Rightarrow 2^4 = 16 \text{ code words.}$$

⑥ Hamming distance : The hamming distance between two code words is equal to the number of elements (or) bits in which they differ. i.e The difference between two code words.

The hamming distance is denoted as ' d '.

$$\text{Ex: } \begin{matrix} x = & 1 & 0 & 0 \\ & \downarrow & \downarrow & \downarrow \\ y = & 1 & 1 & 1 \end{matrix} \quad \therefore d = 2. \quad \underline{d(x,y) = 2}.$$

⑦ Weight of the Code :

The number of non zero's (1's) elements in the transmitted code word is called weight of the code (or) vector rate.

It is denoted as $w(x)$, where x is code word.

$$\text{Ex: } x = 0\underset{1}{1}\underset{1}{1} 0\underset{1}{1} 0\underset{1}{1}.$$

Weight of the code word is $\underline{w(x) = 5}$

⑧ Minimum distance (d_{min}):

It is the smallest Hamming distance between the code words except zero length.
The following table list shows some of the requirements of error control capability of the code.

| S. No | Error detection & Correction | Distance Requirement |
|-------|---|-------------------------------------|
| 1 | Detect upto s -errors per codeword. | $d_{min} \geq s+1$ |
| 2 | Correct upto t errors per code word. | $d_{min} \geq 2t+1$ |
| ③ * | Correct upto t errors and detect upto s errors for code | $d_{min} \geq t+s+1$ ($s > t$) |

For (n, k) block code the minimum distance is given by

$$d_{min} \leq n - k + 1$$

The min-Hamming distance should be two (2) for detection of errors and at least three (3) for correction of errors.

Ex : If $d_{min} = 3$ then

- 1 \rightarrow Error can not be detected
- 2 \rightarrow Single error detection
- 3 \rightarrow Single error correction

No of detections are '2', No of Corrections are '1'
ie $d_{min} \geq s+1$ ie $d_{min} \geq 2t+1$

$$3 \geq s+1$$

$$2 \geq s$$

$$\boxed{S \leq 2}$$

4 \rightarrow Single error correction
+ double error detection

5 \rightarrow Double error correction

6 \rightarrow Double error correction
+ triple error detection

$$3 \geq 2t+1$$

$$2 \geq 2t$$

$$1 \geq t$$

$$\boxed{t \leq 1}$$

$\frac{n-1}{2}$ errors can be corrected
if n odd.

$\frac{n-2}{2}$ errors can be corrected if n even.

Error Control Coding :

- * The probability of error for a particular signalling scheme is a function of signal to noise ratio at the receiver input and the information rate.
- * In practical systems, the maximum signal power and the bandwidth of the channel are restricted to some fixed values. Also, the noise power spectral density ' $\frac{\eta}{2}$ ' is fixed for a particular operating environment.
- * With all these constraints, it is often not possible to arrive at a signalling scheme which will yield an acceptable probability of error for a given application.
- * The only practical alternative for reducing the probability of error is the use of error-control coding.

Types of Error Control Coding :

There are two methods of error control coding.

- ① ARQ [Automatic Retransmission Query (or) Automatic Repeat Request) (or) (Error detection with retransmission)
- ② FEC [Forward Error Correction]

① ARQ [Automatic Repeat Request] :

- ✓ In this method the receiver checks the input sequence if there is any error occurs then the receiver discard that part of the sequence and request the transmitter for retransmission. So, it is called automatic repeat request.
i.e. When an error is detected, the receiver send a request to the transmitter for repeat transmission, after which the transmitter retransmits.

- ✓ Consider a source which emits m messages bits and receiver receives those m -messages and finds one bit is corrupted. Hence the receiver send a request to source for retransmission.
 - ✓ The source again sends the m -messages in this method, the time taken for retransmission is more.
 - ✓ The ARQ method needs duplex arrangement as apart from the conventional transmitter to receiver signal, the request signal is to travel from receiver to transmitter.
- Advantage: It cannot be used on real-time systems.

- ① ARQ can be used to transmit messages more accurately.

Disadvantage:

- ② The probability of error is low its process is also slow.
- ③ Even though P_e is low the retransmission of entire message takes place which leads a delay / time waste in transmission.
- ④ Only detection possible and can't be correct the errors.

② FEC [Forward Error Correction] :

- ✓ In this method the errors are detected and corrected by proper coding techniques.
- ✓ In FEC method, an automatic mechanism of error correction in the form of error correction code is employed & hence retransmission of data is not necessary.
- ✓ The FEC method needs simplex arrangement (Simple hardware as compared to duplex arrangement) as the signal has to travel only from the transmitter to the receiver.
- ✓ The FEC method is more popular & used on real time systems than ARQ method.

- In FEC, the channel encoder systematically adds digits/bits to the transmitted message bits. Although these additional bits convey no new information, they make it possible for the channel decoder to detect & correct the errors in the information bearing digits/bits.
- The overall probability of error is reduced due to error detection and error correction.

Error Control Coding Techniques or Structured Sequences:

Structured Sequences are divided into 3 sub categories.

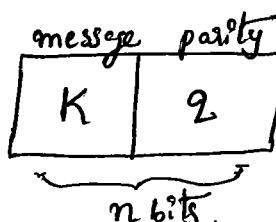
- Block Codes
- Cyclic Codes
- Convolutional Codes
- Turbo codes

Block Codes :

Block code is also known as arithmetic code or group code. There are two types of block codes.

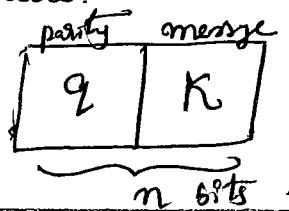
- Systematic block code.
- Non-systematic block code.

Systematic block code : In resultant 'n' bits, the first 'k' bits are message bits and remaining ' $n-k$ ' bits are check bits, called as systematic block code.



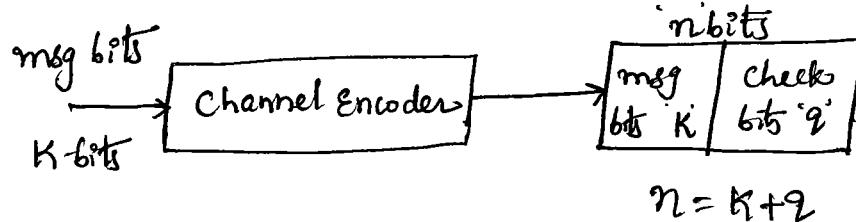
$$q = n - k$$

Non Systematic block code : In resultant 'n' bits, the first 'q' bits are check bits / parity bits and remaining bits are message bits, called as non systematic block code.



$$q = n - k$$

- Each block of K messages or message bits is encoded into a block of n -bits. ($n > K$).
- The check bits are derived from the message bits and these check bits are added to the message bits.



Parity Check Codes :

The simplest possible block code is parity check codes. In this method a no. of check bits added to the message bit is one.

There are two types of parity check codes.

- Even parity check code.
- Odd parity check code.

Even parity check code :

When the check bit may be '1' or '0' such that the total no. of '1's in the encoded of n bits code word is even then it is called Even parity check code.

| <u>Ex :</u> | <u>msg (K)</u> | <u>checkbit (q)</u> | <u>Resultant bit (n)</u> |
|-------------|----------------|---------------------|--------------------------|
| | $x = 010011$ | 1 | $\rightarrow 0100111$ |
| | $y = 101110$ | 0 | $\rightarrow 1011100$ |

Odd parity check code :

When the check bit may be '1' & '0' such that the total no. of '1's in the encoded of n bits code word is odd then it is called Odd parity check code.

| <u>Ex :</u> | <u>msg (K)</u> | <u>checkbit (q)</u> | <u>Resultant bit (n)</u> |
|-------------|----------------------------|---------------------|--------------------------|
| | $x = 010011 \rightarrow 0$ | 0 | $\rightarrow 0100110$ |
| | $y = 101110 \rightarrow 1$ | 1 | $\rightarrow 1011101$ |

Linear Block Codes :

A code is said to be linear block code if the sum (XOR) of any two code words in the codebook produced another codeword which is in the code book \rightarrow set of code words.

- ✓ The basic notation for describing linear block codes in a matrix form.
- ✓ The error control capabilities of a linear block codes are determined by its minimum distance.
- * The encoding operation in a linear block encoding scheme consists of two basic steps.
 - The information sequence is segmented / divided into message blocks, each block consists of K bits.
 - The channel encoder transform each block of K bits into large block of n -bits.

Consider the message block as a row vector $[m_1, m_2, \dots, m_K]$ each message bit can be '1' or '0'.

- ✓ There are 2^K different message blocks or 2^K different code words.
- ✓ Each message block is transformed to a code word C of length n bits. $C = [c_1, c_2, c_3, \dots, c_n]$
- ✓ The set of 2^K code words is called Codebook or Code Vector.
- ✓ The check bits are generated from the message bits.
- ✓ The output of the channel encoder is n bits which can be generated by using matrix form.

$$C = M \cdot G \quad \begin{matrix} \xrightarrow{\text{message bits}} \\ \xleftarrow{\text{Code words}} \end{matrix} \quad \begin{matrix} \xrightarrow{\text{Generator matrix}} \\ \xleftarrow{\text{[C] } 1 \times n = [M]_{1 \times K} \cdot [G]_{K \times n}} \end{matrix}$$

$$[c_1, c_2, c_3, \dots, c_n] = [m_1, m_2, m_3, \dots, m_K] \begin{bmatrix} 1 & 0 & 0 & \dots & & P_{11} & P_{12} & \dots & P_{1q} \\ 0 & 1 & 0 & \dots & & P_{21} & P_{22} & \dots & P_{2q} \\ 0 & 0 & 1 & \dots & & P_{31} & P_{32} & \dots & P_{3q} \\ \vdots & \vdots & \vdots & \ddots & & \vdots & \vdots & \ddots & \vdots \\ 0 & 0 & 0 & \dots & 1 & P_{k1} & P_{k2} & \dots & P_{kq} \end{bmatrix}$$

$$\text{where } [G]_{K \times n} = [I_{K \times K}; P_{K \times 2}]_{K \times n}.$$

where

$[G]$ $k \times n$ is called generator matrix

$[I]$ $k \times k$ is called Identity matrix.

$[P]$ $k \times q$ is called parity matrix. \leftarrow Important role.

An importance steps in the design of (n, k) block codes is the selection of a 'P' matrix so that the code generated by 'G' has some properties such as

- * Easy of implementation.
- * Ability of correct errors.
- * High rate efficiency.

① The generator matrix for a $(6, 3)$ block code is given below
Find all the codewords.

Sol

The given block code $(n, k) = (6, 3)$

$$\begin{aligned}n &= 6 \\k &= 3\end{aligned}$$

$$G = \left[\begin{array}{c|c|c} 1 & 0 & 0 & 0 & 1 & 1 \\ 0 & 1 & 0 & 1 & 0 & 1 \\ 0 & 0 & 1 & 1 & 1 & 0 \end{array} \right] \quad \underbrace{\quad}_{I_{3 \times 3}} \quad \underbrace{\quad}_{P_{3 \times 3}}$$

Length of the message block $k = 3$.

Length of the encoded codewords $n = 6$.

$$\text{Check bits } q = n - k = 6 - 3 = 3$$

There are $2^k = 2^3 = 8$ different message block, each message block consists of 3 bits.

$$C = M \cdot G$$

Consider $M_1 = [0 \ 0 \ 0]$

$$\therefore C = [0 \ 0 \ 0] \begin{bmatrix} 1 & 0 & 0 & 0 & 1 & 1 \\ 0 & 1 & 0 & 1 & 0 & 1 \\ 0 & 0 & 1 & 1 & 1 & 0 \end{bmatrix}_{3 \times 6}$$

$$C_1 = [0 \ 0 \ 0 \ 0 \ 0 \ 0]$$

i.e. Each bit in the row matrix 'M' should be multiplied by each bits in the columns of 'G' matrix.

Then all the values are added by XOR operation

XOR operation :

| | |
|----|---|
| 00 | 0 |
| 01 | 1 |
| 10 | 1 |
| 11 | 0 |

Systematic systematic
block code

$$M_2 = [001], \quad C_2 = [001] \begin{bmatrix} 100 & 011 \\ 010 & 101 \\ 001 & 110 \end{bmatrix}$$

$$= [0+0+0 \ 0+0+0 \ 0+0+1 \ 0+0+1 \ 0+0+1 \ 0+0+0]$$

$$\boxed{C_2 = [0 \ 0 \ 1 \ 1 \ 1 \ 0]}$$

$$M_3 = [010], \quad C_3 = [010] \begin{bmatrix} 100 & 011 \\ 010 & 101 \\ 001 & 110 \end{bmatrix}$$

$$= [0+0+0 \ 0+1+0 \ 0+0+0 \ 0+1+0 \ 0+0+0 \ 0+1+0]$$

$$\boxed{C_3 = [0 \ 1 \ 0 \ 1 \ 0 \ 1]}$$

$$M_4 = [011], \quad C_4 = [011] \begin{bmatrix} 100 & 011 \\ 010 & 101 \\ 001 & 110 \end{bmatrix}$$

$$= [0+0+0 \ 0+1+0 \ 0+0+1 \ 0+1+1 \ 0+0+1 \ 0+1+0]$$

$$\boxed{C_4 = [0 \ 1 \ 1 \ 0 \ 1 \ 1]}$$

$$M_5 = [100], \quad C_5 = [100] \begin{bmatrix} 100 & 011 \\ 010 & 101 \\ 001 & 110 \end{bmatrix}$$

$$= [1+0+0 \ 0+0+0 \ 0+0+0 \ 0+0+0 \ 1+0+0 \ 1+0+0]$$

$$\boxed{C_5 = [1 \ 0 \ 0 \ 0 \ 1 \ 1]}$$

$$M_6 = [101], \quad C_6 = [101] \begin{bmatrix} 100 & 011 \\ 010 & 101 \\ 001 & 110 \end{bmatrix}$$

$$= [1+0+0 \ 0+0+0 \ 0+0+1 \ 0+0+1 \ 1+0+1 \ 1+0+0]$$

$$\boxed{C_6 = [1 \ 0 \ 1 \ 1 \ 0 \ 1]}$$

$$M_7 = [110], \quad C_7 = [110] \begin{bmatrix} 100 & 011 \\ 010 & 101 \\ 001 & 110 \end{bmatrix}$$

$$= [1+0+0 \ 0+1+0 \ 0+0+0 \ 0+1+0 \ 1+0+0 \ 1+1+0]$$

$$\boxed{C_7 = [1 \ 1 \ 0 \ 1 \ 1 \ 0]}$$

$$M_8 = [111], \quad C_8 = [111] \begin{bmatrix} 100 & 011 \\ 010 & 101 \\ 001 & 110 \end{bmatrix}$$

$$= [1+0+0 \ 0+1+0 \ 0+0+1 \ 0+1+1 \ 1+0+1 \ 1+1+0]$$

$$\boxed{C_8 = [1 \ 1 \ 1 \ 0 \ 0 \ 0]}$$

| SL NO | Message | code word |
|-------|---------|-------------|
| 1 | 0 0 0 | 0 0 0 0 0 0 |
| 2 | 0 0 1 | 0 0 1 1 0 |
| 3 | 0 1 0 | 0 1 0 1 0 1 |
| 4 | 0 1 1 | 0 1 1 0 1 1 |
| 5 | 1 0 0 | 1 0 0 0 1 1 |
| 6 | 1 0 1 | 1 0 1 1 0 1 |
| 7 | 1 1 0 | 1 1 0 1 1 0 |
| 8 | 1 1 1 | 1 1 1 0 0 0 |

Alternative Method :

Consider $C = MP$.

$$G = [M_1 \ M_2 \ M_3] \begin{bmatrix} 0 & 1 & 1 \\ 1 & 0 & 1 \\ 1 & 1 & 0 \end{bmatrix}$$

$$C = [M_2 \oplus M_3 \ M_1 \oplus M_3 \ M_1 \oplus M_2]$$

$$\therefore C_1 \ C_2 \ C_3 \rightarrow M_1 \ M_2 \ M_3$$

$$C_4 = M_2 \oplus M_3 \rightarrow ①$$

$$C_5 = M_1 \oplus M_3 \rightarrow ②$$

$$C_6 = M_1 \oplus M_2 \rightarrow ③$$

The codeword can be written as $\begin{bmatrix} C_1 \ C_2 \ C_3 \ C_4 \ C_5 \ C_6 \\ M_1 \ M_2 \ M_3 \ ? \ ? \ ? \end{bmatrix}$

| S.No | Messages $M_1 \ M_2 \ M_3$ | Check bits $C_4 \ C_5 \ C_6$ | Complete Codeword | | |
|------|-------------------------------|---------------------------------|-------------------|-------------------|-------------------|
| | | | $M_1 \ M_2 \ M_3$ | $C_1 \ C_2 \ C_3$ | $C_4 \ C_5 \ C_6$ |
| 1 | 0 0 0 | 0 0 0 | 0 0 0 | 0 0 0 | 0 0 0 |
| 2 | 0 0 1 | 1 1 0 | 0 0 1 | 1 1 0 | 1 1 0 |
| 3 | 0 1 0 | 1 0 1 | 0 1 0 | 1 0 1 | 1 0 1 |
| 4 | 0 1 1 | 0 1 1 | 0 1 1 | 0 1 1 | 0 1 1 |
| 5 | 1 0 0 | 0 1 1 | 1 0 0 | 0 1 1 | 0 1 1 |
| 6 | 1 0 1 | 1 0 1 | 1 0 1 | 1 0 1 | 1 0 1 |
| 7 | 1 1 0 | 1 1 0 | 1 1 0 | 1 1 0 | 1 1 0 |
| 8 | 1 1 1 | 0 0 0 | 1 1 1 | 0 0 0 | 0 0 0 |

② The generator Matrix for a (6,3) block code is given below . Find all the code words.

$$G = \begin{bmatrix} 1 & 0 & 0 & 1 & 1 & 0 \\ 0 & 1 & 0 & 0 & 1 & 1 \\ 0 & 0 & 1 & 1 & 1 & 1 \end{bmatrix}$$

Sd The given block code $(n, k) = (6, 3)$.

$$n = 6$$

$$k = 3$$

Length of the message block $k = 3$

Length of the encoded code words $n = 6$.

$$\text{Check bits } q = n - k = 6 - 3 = 3 = 3.$$

∴ There are $2^k = 2^3 = 8$ different message block , each message block consists of 3 bits.

$$C = M \cdot [G]$$

and

$$C = [M] [P]$$

$$= [M_1 \ M_2 \ M_3] \begin{bmatrix} 1 & 1 & 0 \\ 0 & 1 & 1 \\ 1 & 1 & 1 \end{bmatrix}$$

$$C = [M_1 \oplus M_3 \ M_1 \oplus M_2 \oplus M_3 \ M_2 \oplus M_3]$$

$$\therefore C_1 \ C_2 \ C_3 = M_1 \ M_2 \ M_3$$

$$C_4 = M_1 \oplus M_3$$

$$C_5 = M_1 \oplus M_2 + M_3$$

$$C_6 = M_2 \oplus M_3.$$

| SL NO | Messages $M_1 \ M_2 \ M_3$ | Check bits $C_4 \ C_5 \ C_6$ | Complete codeword $M_1 \ M_2 \ M_3 \ C_4 \ C_5 \ C_6$ |
|-------|-------------------------------|---------------------------------|--|
| 1 | 0 0 0 | 0 0 0 | 0 0 0 0 0 0 |
| 2 | 0 0 1 | 1 1 1 | 0 0 1 1 1 1 |
| 3 | 0 1 0 | 0 1 1 | 0 1 0 0 1 1 |
| 4 | 0 1 1 | 1 0 0 | 0 1 1 1 0 0 |
| 5 | 1 0 0 | 1 1 0 | 1 0 0 1 1 0 |
| 6 | 1 0 1 | 0 0 1 | 1 0 1 0 0 1 |
| 7 | 1 1 0 | 1 0 1 | 1 1 0 1 0 1 |
| 8 | 1 1 1 | 0 1 0 | 1 1 1 0 1 0 |

$$G = \begin{bmatrix} 1 & 0 & 0 & 1 & 1 & 0 \\ 0 & 1 & 0 & 0 & 1 & 1 \\ 0 & 0 & 1 & 1 & 1 & 1 \end{bmatrix}$$

$$\underline{\underline{I_{3 \times 3}}} \quad \underline{\underline{P_{3 \times 3}}}$$

Parity Check Matrix (H) :

- ✓ The parity check matrix can be used to verify whether the code word is generated by the matrix 'G' or not.
- ✓ The code word in the (n, k) block code generated by 'G' if and only if $C H^T = 0$

$$\Rightarrow M G H^T = 0$$

where M - message matrix

G - Generator matrix, $G = [\mathbb{I}_{k \times k}, P_{k \times q}]_{k \times n}$

H - parity check matrix

$$H = [P^T_{q \times k} : \mathbb{I}_{q \times q}]_{q \times n}$$

$$H^T = \begin{bmatrix} (P^T)^T \\ \mathbb{I} \end{bmatrix} = \begin{bmatrix} P_{k \times q} \\ \mathbb{I}_{q \times q} \end{bmatrix}_{n \times q}.$$

$$P = \begin{bmatrix} P_{11} & P_{12} & \dots & P_{1q} \\ P_{21} & P_{22} & \dots & P_{2q} \\ \vdots & & & \\ P_{k1} & P_{k2} & \dots & P_{kq} \end{bmatrix}, \quad P^T = \begin{bmatrix} P_{11} & P_{21} & \dots & P_{k1} \\ P_{12} & P_{22} & \dots & P_{k2} \\ \vdots & \vdots & & \vdots \\ P_{1q} & P_{2q} & \dots & P_{kq} \end{bmatrix}$$

Parity check matrix

$$H = \begin{bmatrix} P_{11} & P_{21} & \dots & P_{k1} & 1 & 0 & 0 & \dots & 0 \\ P_{12} & P_{22} & \dots & P_{k2} & 0 & 1 & 0 & \dots & 0 \\ \vdots & \vdots & & \vdots & \vdots & \vdots & \vdots & & \vdots \\ P_{1q} & P_{2q} & \dots & P_{kq} & 0 & 0 & 0 & \dots & 1 \end{bmatrix}_{q \times n}$$

Error detection & Correction Capabilities of Linear block code :

Hamming codes are (n, k) linear block codes.

These codes satisfies the following conditions.

- ① The number of check bits $q \geq 3$
- ② Block length $n = 2^q - 1$
- ③ The number of message bits $k = n - q$

④ The minimum distance $d_{min} \geq 2$.

If $d_{min} = 2$ then single error can be detected.

If $d_{min} = 3$ then two errors can be detected and one error can be corrected.

⑤ The code rate is given as $R_c = \frac{k}{n}$

$$= \frac{n-2}{n}$$

$$= 1 - \frac{2}{n}$$

$$\boxed{R_c = 1 - \frac{2}{2^k - 1}}$$

⑥ Error detection & correction capabilities of hamming codes are.

* $d_{min} \geq s+1 \rightarrow$ Detect upto 's' error per codeword

* $d_{min} \geq 2t+1 \rightarrow$ Correct upto 't' errors per codeword.

* $d_{min} \geq s+t+1 \rightarrow$ Detect upto 's' errors and correct upto 't' errors for codeword.
($s > t$)

③

The parity check matrix of a particular (7,4) linear block code is given by

$$H = \begin{bmatrix} 1 & 1 & 1 & 0 & 1 & 0 & 0 \\ 1 & 1 & 0 & 1 & 0 & 1 & 0 \\ 1 & 0 & 1 & 1 & 0 & 0 & 1 \end{bmatrix}$$

(a) Find the generator matrix 'G'.

(b) List all the codewords.

(c) Minimum distance between the codewords.

(d) How many errors can be detected and how many error corrected.

(e) Draw the encoded diagram for given hamming code.

Sol

Linear block code $(n, k) = (7, 4)$ i.e. $n=7$, $k=4$.

The length of the message block $k=4$

The length of the encoded codeword $n=7$.

$$\text{Check bits } q = n - k = 7 - 4 = 3$$

\therefore There are $2^k = 2^4 = 16$ different message blocks, each message block consists of 4 bits.

Given parity check matrix

$$H = \begin{bmatrix} 1 & 1 & 1 & 0 & | & 1 & 0 & 0 \\ 1 & 1 & 0 & 1 & | & 0 & 1 & 0 \\ 1 & 0 & 1 & 1 & | & 0 & 0 & 1 \end{bmatrix}_{3 \times 7}$$

$$H = \left[P^T_{2 \times K} \mid \Omega_{2 \times 2} \right]_{2 \times n}$$

$$P = \begin{bmatrix} 1 & 1 & 1 \\ 1 & 1 & 0 \\ 1 & 0 & 1 \\ 0 & 1 & 1 \end{bmatrix}_{K \times 2} \text{ i.e. } 4 \times 3$$

$$\therefore P = (P^T)^T$$

(a) Generator matrix: $G_1 = \left[\Omega_{K \times K} \ P_{K \times 2} \right]_{K \times n}$

$$G_1 = \left[\begin{array}{ccc|ccc} 1 & 0 & 0 & 0 & 1 & 1 & 1 \\ 0 & 1 & 0 & 0 & 1 & 1 & 0 \\ 0 & 0 & 1 & 0 & 1 & 0 & 1 \\ 0 & 0 & 0 & 1 & 0 & 1 & 1 \end{array} \right]_{4 \times 7}$$

(b) All Codewords:

$$C = M G$$

$$G_1 = [\Omega \mid P]$$

$$C = [M] [P]$$

$$\therefore C = \left[\begin{array}{cccc} M_1 & M_2 & M_3 & M_4 \end{array} \right]_{1 \times 3} \left[\begin{array}{c} 1 & 1 & 1 \\ 1 & 1 & 0 \\ 1 & 0 & 1 \\ 0 & 1 & 1 \end{array} \right]_{4 \times 3} \quad 4 \times 3$$

$$C = [M_1 \oplus M_2 \oplus M_3 \quad M_1 \oplus M_2 \oplus M_4 \quad M_1 \oplus M_3 \oplus M_4]$$

$$\therefore [c_1, c_2, c_3, c_4] = [M_1, M_2, M_3, M_4]$$

$$c_5 = M_1 \oplus M_2 \oplus M_3 \rightarrow ①$$

$$c_6 = M_1 \oplus M_2 \oplus M_4 \rightarrow ②$$

$$c_7 = M_1 \oplus M_3 \oplus M_4 \rightarrow ③$$

$$C_5 = M_1 \oplus M_2 \oplus M_3, \quad C_6 = M_1 \oplus M_2 \oplus M_3 \oplus M_4$$

| SL No | Message M_1, M_2, M_3, M_4 | check bits c_5, c_6, c_7 | | | Complete Codeword $M_1, M_2, M_3, M_4, c_5, c_6, c_7$ | | | | Vector rate, weight |
|-------|---------------------------------|-------------------------------|-------|-------|--|-------|-------|-------|------------------------|
| | | c_5 | c_6 | c_7 | M_1 | M_2 | M_3 | M_4 | |
| 1 | 0 0 0 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 2 | 0 0 0 1 | 0 | 1 | 1 | 0 | 0 | 0 | 1 | 3 |
| 3 | 0 0 1 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 3 |
| 4 | 0 0 1 1 | 1 | 1 | 0 | 0 | 0 | 1 | 1 | 4 |
| 5 | 0 1 0 0 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 3 |
| 6 | 0 1 0 1 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 4 |
| 7 | 0 1 1 0 | 0 | 1 | 1 | 0 | 1 | 1 | 0 | 4 |
| 8 | 0 1 1 1 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 3 |
| 9 | 1 0 0 0 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 4 |
| 10 | 1 0 0 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 3 |
| 11 | 1 0 1 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 3 |
| 12 | 1 0 1 1 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 4 |
| 13 | 1 1 0 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 3 |
| 14 | 1 1 0 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 4 |
| 15 | 1 1 1 0 | 1 | 0 | 0 | 1 | 1 | 0 | 1 | 4 |
| 16 | 1 1 1 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 7 |

$$d_{\min} = 3 \\ \text{Except '0'}$$

(c)

The minimum distance of the (n, k) block code is $\leq n-k+1$

\therefore The minimum hamming distance $d_{\min} = 3$

$$d_{\min} \leq n-k+1$$

$$3 \leq (7-4+1)$$

$$3 \leq 4 \quad (\text{True}) \quad \checkmark$$

$$3 \leq 4$$

(d) No. of detections of error $\Rightarrow d_{\min} \geq s+1$

$$3 \geq s+1$$

$$2 \geq s \quad \therefore s \leq 2$$

No. of error detections $s=2$

No. of error corrections $t \Rightarrow d_{\min} \geq 2t+1$

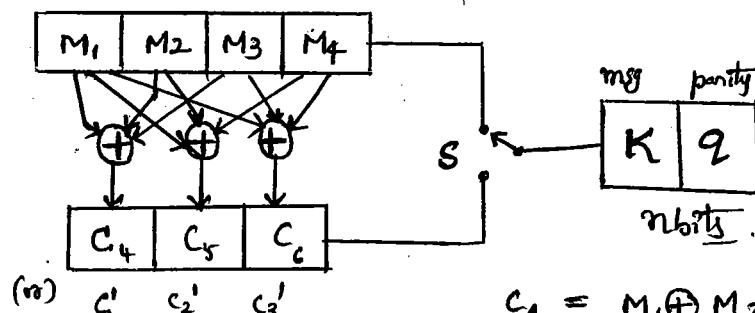
$$3 \geq 2t+1$$

$$1 \geq t \quad \therefore t \leq 1$$

No. of error corrections $t=1$

(e) Encoder diagram :

The encoder of (7,4) hamming code is implemented for the given generator matrix.



S - switch

- ✓ The check bits c_1' , c_2' , c_3' are obtained from the message bits by modulo-2 XOR operation.
- ✓ The switch 'S' is connected to the message register first then transmit all the message bits.
- ✓ Then switch 'S' is then connected to the check bit register then all check bits are transmitted.
Thus, it form a 7 bit block codes.

Syndrome Decoding :

- * The generator matrix (G) is used in the encoding operation & the parity check matrix (H) is used in the decoding operation.
- ✓ The encoder has to store the G -matrix & perform binary arithmetic operations to generate the check bits.
- ✓ The complexity of the encoder increases as the no. of check bits are increases.
- * Let transmitted code vector be x , the received code vector be y
 - If $x=y$ then there are no transmission errors.
 - If $x \neq y$ then there are errors created during transmission.

- (10)
- The decoder detects & Corrects the errors in 'y' using the stored information about the codeword.
 - A direct way of performing error detection would be comparing 'y' with every codeword in the code book.

Let 'c' be a codeword that was transmitted over a noisy channel & it is received as 'y'.

The syndrome is represented by 's' & it can be written as

Detection:
$$S = Y H^T \quad \text{where } [H]_{q \times n} - \text{parity check matrix.}$$

$$[S]_{1 \times q} = [Y]_{1 \times n} [H^T]_{n \times q}$$

Let us consider error vector 'E' & this vector represent the position of transmission errors in 'y'.

$$\text{So } Y = X \oplus E \Rightarrow X = Y \oplus E$$

Substitute 'y' in 's'

i.e.

$$\begin{aligned} S &= [X \oplus E] \cdot H^T \\ &= X H^T \oplus E H^T \quad (\because X H^T = 0) \\ S &= E H^T \\ \therefore S &= E H^T \quad \text{For Single bit error.} \end{aligned}$$

This relation shows that the syndrome depends upon error pattern only. It does not depend upon a particular message.

Syndrome vector 's' of size $1 \times q$.

2 bits of syndrome can only represent 2^{2-1} syndrome vectors.

Each syndrome vector corresponds to a particular error pattern.

Hence in syndrome decoding we can detect and correct only one error.

$$\therefore \text{The no. of errors can be corrected} \Leftrightarrow 1 - r_c \geq \left[\frac{1}{n} \log_2 \sum_{i=1}^t n_{c_i} \right]$$

This eqn also called Hamming bound.

- ④ The parity check matrix of a particular $(6,3)$ block code is given as $H = \begin{bmatrix} 1 & 0 & 1 & 1 & 0 & 0 \\ 1 & 1 & 0 & 0 & 1 & 0 \\ 0 & 1 & 1 & 0 & 0 & 1 \end{bmatrix}$.
- Determine the generator matrix G .
 - Find all the code words.
 - Find the min distance between the codeword.
 - How many errors can be detected & Corrected.
 - Draw the encoder diagram.
 - Calculate the syndrome vector for single bit error.
 - Suppose that the received codeword is '110110', decode this codeword.
 - Draw the syndrome diagram.

Sol.: Given block code $(n,k) = (6,3)$ $n=6$ $k=3$.

Length of the message blocks $k=3$

Length of the encoded codeword $n=6$.

Check bits $q=n-k=6-3=3$.

$K=3$. There are $2^k=2^3=8$ different message block, each message block consists of 3 bits.

$$H = \begin{bmatrix} 1 & 0 & 1 & 1 & 0 & 0 \\ 1 & 1 & 0 & 1 & 0 & 1 \\ 0 & 1 & 1 & 0 & 0 & 1 \end{bmatrix}_{3 \times 6}$$

$$H = \begin{bmatrix} P^T & I \end{bmatrix}_{3 \times 3 \quad 3 \times 3 \quad 3 \times 6}$$

where $P^T = \begin{bmatrix} 1 & 0 & 1 \\ 1 & 1 & 0 \\ 0 & 1 & 1 \end{bmatrix} \Rightarrow P = \begin{bmatrix} 1 & 1 & 0 \\ 0 & 1 & 1 \\ 1 & 0 & 1 \end{bmatrix}_{3 \times 3}, I = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{bmatrix}_{3 \times 3}$

(a) Generator matrix $G_1 = \begin{bmatrix} I_{K \times K} & P_{K \times 2} \end{bmatrix}_{K \times n}$

$$G_1 = \begin{bmatrix} 1 & 0 & 0 & 1 & 1 & 0 \\ 0 & 1 & 0 & 0 & 1 & 1 \\ 0 & 0 & 1 & 1 & 0 & 1 \end{bmatrix}_{3 \times 6}$$

(b) Code words $C = MG$

$$\text{Ans. } C = [M][P] = \begin{bmatrix} M_1 & M_2 & M_3 \end{bmatrix} \begin{bmatrix} 1 & 0 \\ 0 & 1 & 1 \\ 1 & 0 & 1 \end{bmatrix}$$

$$\therefore c_1 c_2 c_3 \rightarrow M_1 M_2 M_3$$

$$c_4 = M_1 \oplus M_3$$

$$c_5 = M_1 \oplus M_2$$

$$c_6 = M_2 \oplus M_3.$$

| SL NO | Messages M ₁ M ₂ M ₃ | Check bits C ₄ C ₅ C ₆ | Complete Codeword | | | Vector rate W(x) |
|-------|--|--|-------------------|----------------|----------------|------------------------|
| | | | M ₁ | M ₂ | M ₃ | |
| 1 | 0 0 0 | 0 0 0 | 0 0 0 | 0 0 0 | 0 0 0 | 0 |
| 2 | 0 0 1 | 1 0 1 | 0 0 1 | 1 0 1 | 0 0 1 | 3 |
| 3 | 0 1 0 | 0 1 1 | 0 1 0 | 0 1 1 | 0 1 1 | 3 |
| 4 | 0 1 1 | 1 1 0 | 0 1 1 | 1 1 0 | 1 1 0 | 4 |
| 5 | 1 0 0 | 1 1 0 | 1 0 0 | 1 1 0 | 1 1 0 | 3 |
| 6 | 1 0 1 | 0 1 1 | 1 0 1 | 0 1 1 | 1 0 1 | 4 |
| 7 | 1 1 0 | 1 0 1 | 1 1 0 | 1 0 1 | 1 0 1 | 4 |
| 8 | 1 1 1 | 0 0 0 | 1 1 1 | 0 0 0 | 1 1 1 | 3 |

(c) The minimum distance $d_{min} = 3$

except 0.

$$d_{min}=3.$$

(d) No. of detections of errors.

$$d_{min} \geq s+1$$

$$3 \geq s+1$$

No. of error corrections.

$$s \leq 2$$

∴ No. of error detections

$$d_{min} \geq 2t+1$$

$$3 \geq 2t+1$$

$$t \leq 1$$

$$t=1$$

For (n, k) block code the minimum distance is given by

$$d_{min} \leq n-k+1$$

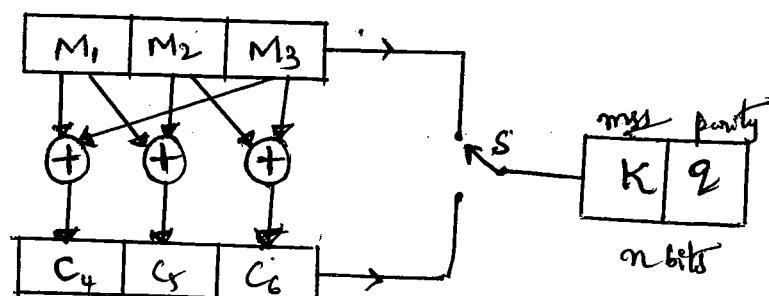
$$3 \leq 6-3+1$$

$$\begin{matrix} n=6 \\ k=3 \end{matrix}$$

$$3 \leq 4 \text{ (True)}$$

(e) The encoder diagram of $(6, 3)$ block code is implemented for the given generator matrix.

$$\begin{aligned} c_4 &= M_1 \oplus M_3 \\ c_5 &= M_1 \oplus M_2 \\ c_6 &= M_2 \oplus M_3. \end{aligned}$$



(F) Syndrome vector $S = EH^T$ for single bit error

$$H^T = \begin{bmatrix} (P^T)^T \\ Q \end{bmatrix} = \begin{bmatrix} P \\ Q \end{bmatrix}_{n \times q}$$

$$H^T = \begin{bmatrix} 110 \\ 011 \\ 101 \\ 100 \\ 010 \\ 001 \end{bmatrix}_{6 \times 3} \quad \text{where} \quad P = \begin{bmatrix} 110 \\ 011 \\ 101 \end{bmatrix}, \quad Q = \begin{bmatrix} 100 \\ 010 \\ 001 \end{bmatrix}$$

Error Vector E as

$$E = \begin{bmatrix} 100000 \\ 010000 \\ 001000 \\ 000100 \\ 000010 \\ 000001 \end{bmatrix}_{6 \times 6}$$

Syndrome vector

$$S = EH^T$$

$$\text{for } E = 100000, \quad S = [100000] \begin{bmatrix} 110 \\ 011 \\ 101 \\ 100 \\ 010 \\ 001 \end{bmatrix} = [110]$$

My.

| Sl No | Bit in error | Bit in Error vector | Syndrome Vector |
|-------|-----------------|---------------------|-----------------|
| 1 | 1 st | 1 0 0 0 0 0 | 110 |
| 2 | 2 nd | 0 1 0 0 0 0 | 011 |
| 3 | 3 rd | 0 0 1 0 0 0 | 101 |
| 4 | 4 th | 0 0 0 1 0 0 | 100 |
| 5 | 5 th | 0 0 0 0 1 0 | 010 |
| 6 | 6 th | 0 0 0 0 0 1 | 001 |

(G) For the detection of the error by using syndrome decoding is given as $S = YH^T$

Given received bits (codeword) $Y = [110110]$

$$S = [110110]_{1 \times 6} \begin{bmatrix} 110 \\ 011 \\ 101 \\ 100 \\ 010 \\ 001 \end{bmatrix}_{6 \times 3}$$

$$\Rightarrow [1 \oplus 0 \oplus 0 \oplus 1 \oplus 0 \oplus 0 \quad 1 \oplus 1 \oplus 0 \oplus 0 \oplus 1 \oplus 0 \quad 0 \oplus 1 \oplus 0 \oplus 0 \oplus 0 \oplus 0]$$

$$S = [0 \ 1 \ 1] \quad \therefore \underline{\text{2}^{\text{nd}} \text{ bit error}}$$

Error correction is possible by using

$$X = Y \oplus E$$

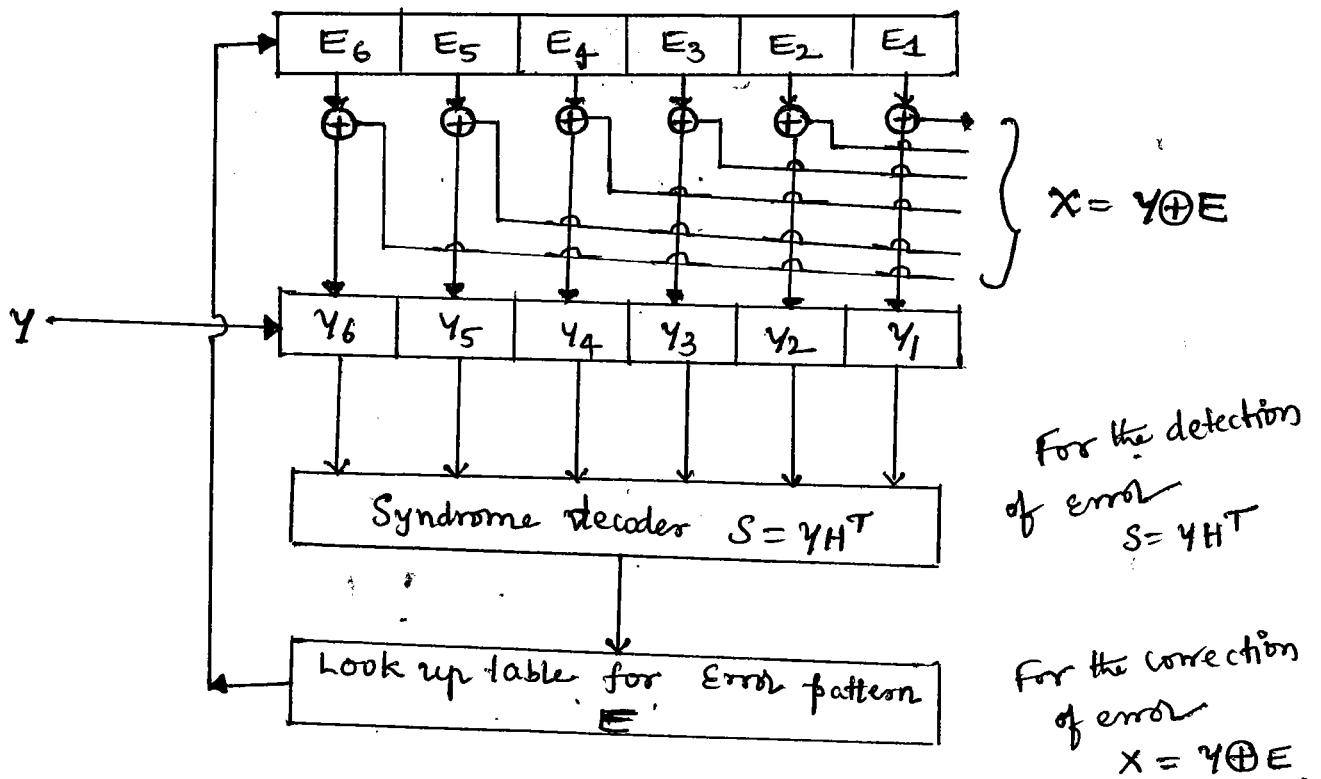
$$= [110110] \oplus [010000]$$

$$= [100110]$$

$$\therefore X = \begin{array}{c} 100 \\ \downarrow \quad \downarrow \\ \text{message bits} \quad \text{parity bits} \end{array} 110$$

(h) Syndrome diagram :

Error bits $\rightarrow n$ bits
Rxed bits $\rightarrow n$ bits.



For the detection of error $S = YH^T$

for the correction of error $X = Y \oplus E$.

5)

The parity check matrix of a particular (7,4) linear block codes is given by

$$H = \begin{bmatrix} 1 & 1 & 1 & 0 & 1 & 0 & 0 \\ 1 & 1 & 0 & 1 & 0 & 1 & 0 \\ 1 & 0 & 1 & 1 & 0 & 0 & 1 \end{bmatrix}$$

(a) Calculate Syndrome Vector for single bit error

(b) Suppose that the Rxed codeword is 0110110, decode this codeword.

(c) Draw the syndrome diagram.

So

Given linear block code $(n, k) = (7, 4)$

Length of the message block $k = 4$

Length of the Encoded Codeword $n = 7$.

check bits $q = m-k = 7-4 = 3$

$K=4, m=7, q=3$.

$$H = \begin{bmatrix} 1 & 1 & 1 & 0 & 1 & 0 & 0 \\ 1 & 1 & 0 & 1 & 0 & 1 & 0 \\ 1 & 0 & 1 & 1 & 0 & 0 & 1 \end{bmatrix}, \quad H = [P^T | I]$$

$$P^T = \begin{bmatrix} 1 & 1 & 1 & 0 \\ 1 & 1 & 0 & 1 \\ 1 & 0 & 1 & 1 \end{bmatrix}_{3 \times 4} \Rightarrow P = \begin{bmatrix} 1 & 1 & 1 \\ 1 & 1 & 0 \\ 1 & 0 & 1 \\ 0 & 1 & 1 \end{bmatrix}_{4 \times 3}$$

$$H^T = \begin{bmatrix} 1 & 1 & 1 \\ 1 & 1 & 0 \\ 1 & 0 & 1 \\ 0 & 1 & 1 \\ 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{bmatrix}_{7 \times 3} \Rightarrow H^T = [(P^T)^T] = \begin{bmatrix} P \\ I \end{bmatrix}$$

$$Q = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{bmatrix}$$

Error matrix

(a)

$$E = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}_{7 \times 7}$$

Syndrome Vector $S = E \cdot H^T$

for $E = [0 \ 1 \ 0 \ 0 \ 0 \ 0 \ 0]$

$$\begin{bmatrix} 1 & 1 & 1 \\ 1 & 1 & 0 \\ 1 & 0 & 1 \\ 0 & 1 & 1 \\ 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{bmatrix} = \begin{array}{l} 0 \oplus 1 \oplus 0 \oplus 0 \oplus 0 \oplus 0 \oplus 0 \\ 0 \oplus 1 \oplus 0 \oplus 0 \oplus 0 \oplus 0 \oplus 0 \\ 0 \oplus 0 \oplus 0 \oplus 0 \oplus 0 \oplus 0 \oplus 0 \end{array}$$

$$= [1 \ 1 \ 0]$$

My

| S.No | Bit in error | Bits in Error Vector | Syndrome vector $S \approx H^T$ |
|------|-----------------|----------------------|---------------------------------|
| 1 | 1 st | 1 0 0 0 0 0 0 | 1 1 1 |
| 2 | 2 nd | 0 1 0 0 0 0 0 | 1 1 0 |
| 3 | 3 rd | 0 0 1 0 0 0 0 | 1 0 1 |
| 4 | 4 th | 0 0 0 1 0 0 0 | 0 1 1 |
| 5 | 5 th | 0 0 0 0 1 0 0 | 1 0 0 |
| 6 | 6 th | 0 0 0 0 0 1 0 | 0 1 0 |
| 7 | 7 th | 0 0 0 0 0 0 1 | 0 0 1 |

$\approx H^T$

(b) For the detection of the error by using syndrome decoding

$$S = \mathbf{y} \mathbf{H}^T$$

Given $\gamma = \begin{bmatrix} 0 & 1 & 1 & 0 & 1 & 1 & 0 \\ \text{nsb} & & & & & & \text{LSB} \end{bmatrix}$

$$S = \begin{bmatrix} 0 & 1 & 1 & 0 & 1 & 1 & 0 \\ 1 & 0 & 1 & 1 & 0 & 1 & 1 \\ 1 & 1 & 0 & 1 & 1 & 0 & 1 \\ 1 & 0 & 1 & 1 & 1 & 0 & 0 \\ 0 & 1 & 1 & 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 1 & 0 & 1 & 0 \\ 0 & 1 & 0 & 0 & 1 & 0 & 1 \end{bmatrix}$$

$$S = \begin{bmatrix} 1 & 0 & 1 \end{bmatrix}_{1 \times 3}$$

$$S = [1 \ 0 \ 1] \text{ i.e } 3^{\text{rd}} \text{ bit Error.}$$

Error Correction can be possible by using

$$X = Y \oplus E$$

$$= [0\ 1\ 1\ 0\ 1\ 1\ 0] \oplus [0\ 0\ 1\ 0\ 0\ 0\ 0]$$

$$x = 0100110$$

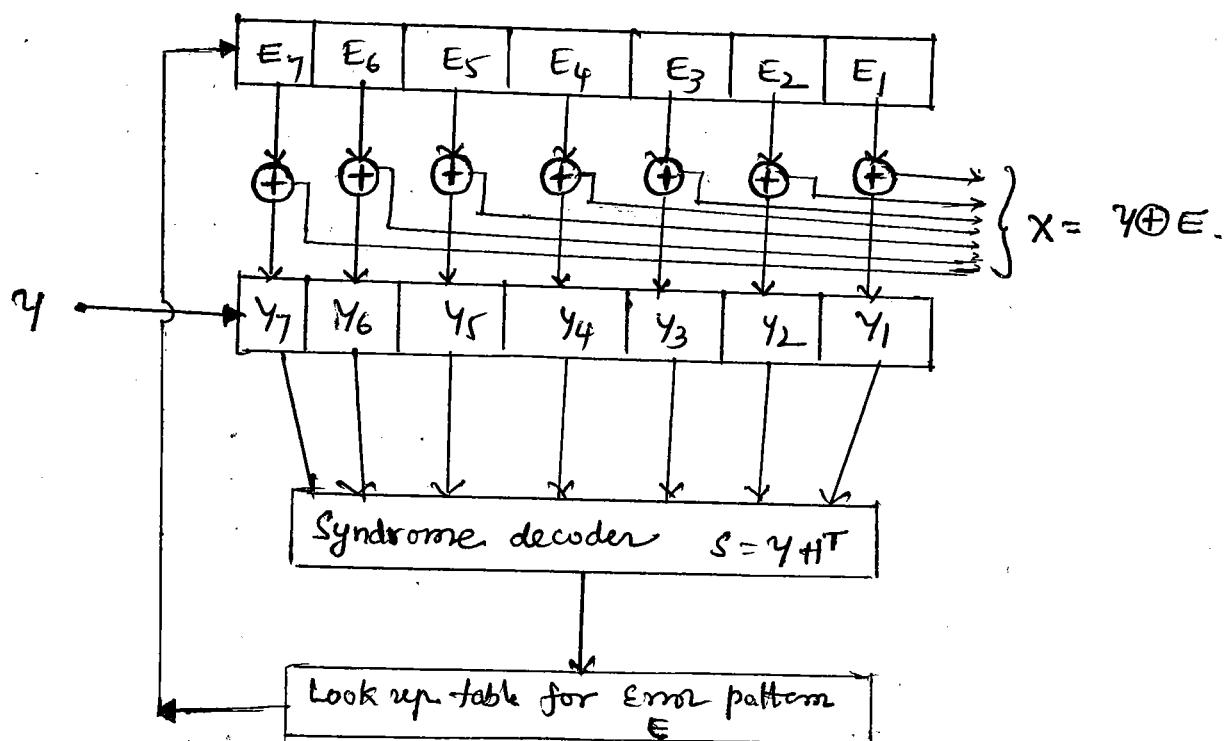
$$x = [0 \ 1 \ 0 \ 0 \ 1 \ 1 \ 0]$$

↓
message bits ↓
parity bits.

(c) Syndrome diagram:

Error bits $\rightarrow n$ bits

Received bits \rightarrow n bits.



* (6) Consider $(7,4)$ code whose generator matrix is

$$G = \begin{bmatrix} 1 & 1 & 1 & 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 1 & 0 & 0 & 0 \\ 0 & 1 & 1 & 0 & 0 & 1 & 0 \\ 1 & 1 & 0 & 0 & 0 & 1 & 1 \end{bmatrix}$$

(a) Find all the codewords of the code.

(b) Find parity check matrix H .

(c) Calculate the syndrome for the received vector '1101101'.

Sol

Given Linear block code $(n, k) = (7, 4)$

Given generator Matrix

$$G_1 = \begin{bmatrix} 1 & 1 & 1 & 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 1 & 0 & 0 & 0 \\ 0 & 1 & 1 & 0 & 0 & 1 & 0 \\ 1 & 1 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}_{4 \times 7}$$

$$\begin{aligned} n &= 7 \\ k &= 4 \\ q &= n - k = 3 \end{aligned}$$

Code word $C = MG_1$

$$\text{where } G = \begin{bmatrix} I & P \end{bmatrix}_{k \times n} \text{ or } G_1 = \begin{bmatrix} P & I \end{bmatrix}_{k \times n}$$

$$K = 4$$

$2^k = 16$ different message blocks

$$(a) C = M[P]$$

$$= [M_1 \ M_2 \ M_3 \ M_4] \begin{bmatrix} 1 & 1 & 1 \\ 1 & 0 & 1 \\ 0 & 1 & 1 \\ 1 & 1 & 0 \end{bmatrix}_{4 \times 4}$$

$$I = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix}_{4 \times 4}$$

$$P = \begin{bmatrix} 1 & 1 & 1 \\ 1 & 0 & 1 \\ 0 & 1 & 1 \\ 1 & 1 & 0 \end{bmatrix}_{4 \times 3}$$

$$\Rightarrow C_1 \ C_2 \ C_3 \ C_4 \rightarrow M_1 \ M_2 \ M_3 \ M_4 \Rightarrow \begin{aligned} C_4 &= M_1 + M_2 + M_4 \\ C_5 &= M_1 + M_3 + M_4 \\ C_6 &= M_1 + M_2 + M_3. \end{aligned}$$

| SL No | Message bits $M_1 \ M_2 \ M_3 \ M_4$ | Check bits | | | Complete code word | | | Weight |
|-------|---|------------|-------|-------|---|---------|---------|--------|
| | | c_5 | c_6 | c_7 | $M_1 \ M_2 \ M_3 \ M_4 \ c_5 \ c_6 \ c_7$ | | | |
| 1 | 0 0 0 0 | 0 | 0 | 0 | 0 0 0 0 0 0 0 | 0 0 0 0 | 0 0 0 0 | 0 |
| 2 | 0 0 0 1 | 1 | 1 | 0 | 0 0 0 1 1 1 0 | 0 0 0 1 | 1 1 1 0 | 3 |
| 3 | 0 0 1 0 | 0 | 1 | 1 | 0 0 1 0 0 1 1 | 0 0 1 0 | 0 1 1 1 | 3 |
| 4 | 0 0 1 1 | 1 | 0 | 1 | 0 0 1 1 1 0 1 | 0 0 1 1 | 1 0 1 1 | 4 |
| 5 | 0 1 0 0 | 1 | 0 | 1 | 0 1 0 0 1 0 1 | 0 1 0 0 | 1 0 1 1 | 3 |
| 6 | 0 1 0 1 | 0 | 1 | 1 | 0 1 0 1 0 1 1 | 0 1 0 1 | 0 1 1 1 | 4 |
| 7 | 0 1 1 0 | 1 | 1 | 0 | 0 1 1 0 1 1 0 | 0 1 1 0 | 1 1 1 0 | 4 |
| 8 | 0 1 1 1 | 0 | 0 | 0 | 0 1 1 1 0 0 0 | 0 1 1 1 | 0 0 0 0 | 3 |
| 9 | 1 0 0 0 | 1 | 1 | 1 | 1 0 0 0 1 1 1 | 1 0 0 0 | 1 1 1 1 | 4 |
| 10 | 1 0 0 1 | 0 | 0 | 1 | 1 0 0 1 0 0 1 | 1 0 0 1 | 0 0 1 1 | 3 |
| 11 | 1 0 1 0 | 1 | 0 | 0 | 1 0 1 0 1 0 0 | 1 0 1 0 | 1 0 0 0 | 3 |
| 12 | 1 0 1 1 | 0 | 1 | 0 | 1 0 1 1 0 1 0 | 1 0 1 1 | 0 1 0 0 | 4 |
| 13 | 1 1 0 0 | 0 | 1 | 0 | 1 1 0 0 0 1 0 | 1 1 0 0 | 0 1 0 0 | 3 |
| 14 | 1 1 0 1 | 1 | 0 | 0 | 1 1 0 1 1 0 0 | 1 1 0 1 | 1 0 0 0 | 4 |
| 15 | 1 1 1 0 | 0 | 0 | 1 | 1 1 1 0 0 0 1 | 1 1 1 0 | 0 0 1 1 | 4 |
| 16 | 1 1 1 1 | 1 | 1 | 1 | 1 1 1 1 1 1 1 | 1 1 1 1 | 1 1 1 1 | 7 |

The minimum distance $d_{min} = 3$ except '0'.

$$d_{min} \geq s+1$$

$$s \geq s+1$$

$$s \leq 2$$

No. of detection of errors $s=2$

$$d_{min} \geq 2t+1$$

$$3 \geq 2t+1$$

$$t \leq 2$$

No. of correction of errors $t=1$

(b) Parity check matrix 'H'.

$$P = \begin{bmatrix} 1 & 1 & 1 \\ 1 & 0 & 1 \\ 0 & 1 & 1 \\ 1 & 1 & 0 \end{bmatrix} \Rightarrow P^T = \begin{bmatrix} 1 & 1 & 0 & 1 \\ 1 & 0 & 1 & 1 \\ 1 & 1 & 1 & 0 \end{bmatrix}_{3 \times 4}, I = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{bmatrix}_{3 \times 3}$$

$$H = \begin{bmatrix} 1 & 1 & 0 & 1 & 1 & 0 & 0 \\ 1 & 0 & 1 & 1 & 0 & 1 & 0 \\ 1 & 1 & 1 & 0 & 1 & 0 & 0 \end{bmatrix} \Rightarrow H^T = \begin{bmatrix} P \\ I \end{bmatrix} = \begin{bmatrix} 1 & 1 & 1 \\ 1 & 0 & 1 \\ 0 & 1 & 1 \\ 1 & 1 & 0 \\ 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{bmatrix}_{7 \times 3}$$

Syndrome vector

$$S = E H^T$$

where

$$\text{Error Vector } E = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$$

$$S = [E]_{1 \times 7} [H^T]_{7 \times 3}$$

$$S = 1 \times 3.$$

| SNo | Bit in Error | Bit in Error Vector E | Syndrome Vector $S \approx H^T$ |
|-----|-----------------|-------------------------|---------------------------------|
| 1 | 1 st | 1 0 0 0 0 0 0 | 1 1 1 |
| 2 | 2 nd | 0 1 0 0 0 0 0 | 1 0 1 |
| 3 | 3 rd | 0 0 1 0 0 0 0 | 0 1 1 |
| 4 | 4 th | 0 0 0 1 0 0 0 | 1 1 0 |
| 5 | 5 th | 0 0 0 0 1 0 0 | 1 0 0 |
| 6 | 6 th | 0 0 0 0 0 1 0 | 0 1 0 |
| 7 | 7 th | 0 0 0 0 0 0 1 | 0 0 1 |

For the detection of error by using syndrome decoding is

$$S = y H^T$$

Given . Received vector $y = [1 \ 1 \ 0 \ 1 \ 1 \ 0 \ 1]$

$$S = [1 \ 1 \ 0 \ 1 \ 1 \ 0 \ 1] \begin{bmatrix} 1 & 1 & 1 \\ 1 & 0 & 1 \\ 0 & 1 & 1 \\ 1 & 1 & 0 \\ 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{bmatrix} \Rightarrow S = [0 \ 0 \ 1]$$

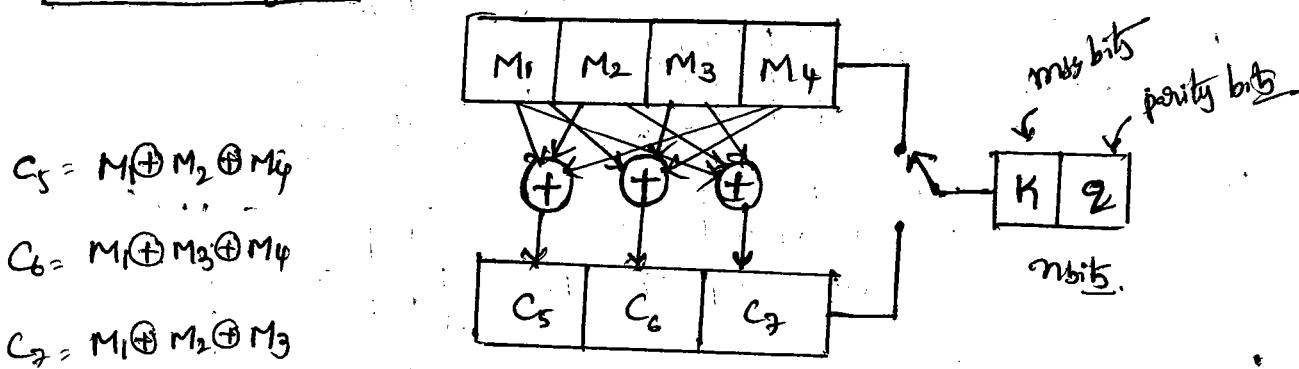
$\therefore S = [0 \ 0 \ 1]$ ie 7th bit error

Error correction is possible by using. $x = y \oplus E$

$$x = [1101101] \oplus [0000001]$$

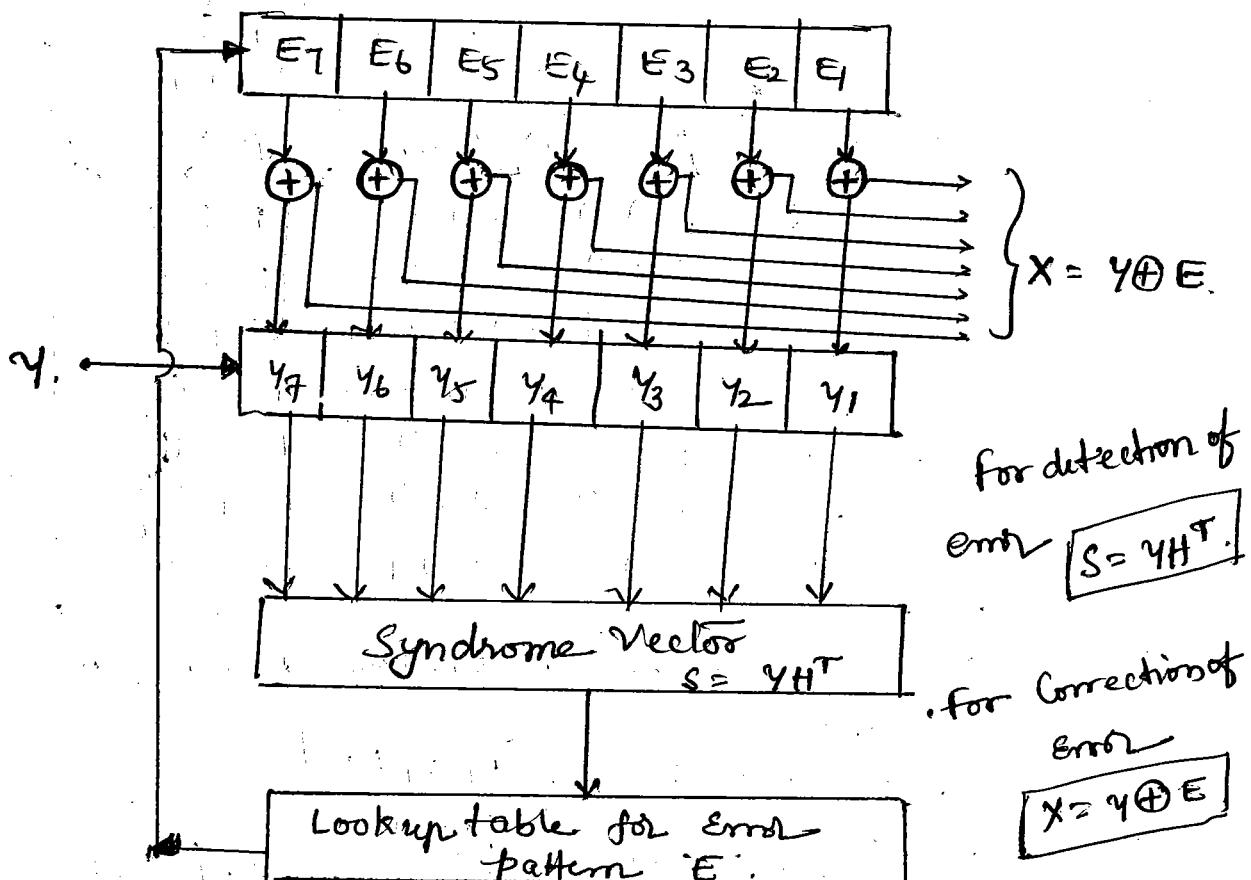
$$x = \underbrace{[1101]}_{\text{msg bits}} \underbrace{[100]}_{\text{parity bits}}$$

Encoder diagram:



Syndrome diagram: Error bits - n bits.

Received bits - $n+2$ bits.



Cyclic Codes: (Polynomial form)

In linear block codes the design of hardware requirements are complex, so the alternative method to generate code vector is "cyclic codes".

- * A linear code is called Cyclic code, if every cyclic shift of the code vector is produced another code vector.

Ex: Consider an n -bit code vector

$$[x] = [x_{n-1} \underset{\text{MSB}}{x_{n-2}} \dots x_1 \underset{\text{LSB}}{x_0}]$$

Shift the above code vector cyclically produced another code vector.

$$[x'] = [x_{n-2} \underset{\text{MSB}}{x_{n-3}} \dots x_1 x_0 \underset{\text{LSB}}{x_{n-1}}]$$

Actually x_{n-1} is MSB position but after cyclic shift it becomes LSB position.

A binary code is said to be a Cyclic Codes if exhibits two fundamental properties.

① Linear property: The sum of two code words is also a code word.

② Cyclic property: Any cyclic shift of a codeword is also a codeword.

Ex:

$$\begin{array}{r} 1 0 1 1 \\ \downarrow \quad \downarrow \quad \downarrow \quad \downarrow \\ 1 1 0 1 \\ \downarrow \quad \downarrow \quad \downarrow \quad \downarrow \\ 1 1 1 0 \\ \downarrow \quad \downarrow \quad \downarrow \quad \downarrow \\ 0 1 1 1 \\ \hline 1 0 1 1 \end{array}$$

Obtained by a cyclic shift of
n-tuple (tuple) $n=4$

Cyclic codes are important for two reasons.

- ① Encoding & syndrome calculations can be easily implemented by using simple shift registers with feedback connection.
- ② The mathematical structure of these codes is such that it is possible to design codes having useful error-correcting properties.

Analysis: The code vector is represented in mathematical form of polynomials.

$$X(p) = [x_{n-1} p^{n-1} + x_{n-2} p^{n-2} + \dots + x_1 p^1 + x_0] \xrightarrow{\text{MSB LSB}} \quad \text{①}$$

where p - arbitrary value

power of p represents the position of the code word bit.

- Multiply eqn ① with p we get

$$p \cdot X(p) = x_{n-1} p^n + x_{n-2} p^{n-1} + \dots + x_1 p^2 + x_0 \cdot p \xrightarrow{\text{②}}$$

- Cyclic shift of eqn ② produced another code vector.

$$X'(p) = x_{n-2} p^{n-1} + x_{n-3} p^{n-2} + \dots + x_1 p^2 + x_0 p^1 + x_{n-1} p^0 \xrightarrow{\text{③}}$$

- Add eqn ② & ③ by using modulo 2 operation ie XOR operation.

$$\begin{aligned} p \cdot X(p) \oplus X'(p) &= x_{n-1} p^n \oplus x_{n-1} + x_{n-2} p^{n-1} \oplus x_{n-2} p^{n-1} \\ &\quad + \dots + x_1 p^2 \oplus x_1 p^2 + x_0 p^1 \oplus x_0 p^1 \\ &= x_{n-1} p^n \oplus x_{n-1} + [x_{n-2} \oplus x_{n-2}] p^{n-1} \\ &\quad + [x_{n-3} \oplus x_{n-3}] p^{n-2} + \dots + p^2 (x_1 \oplus x_1) + p^1 (x_0 \oplus x_0) \\ &= x_{n-1} [p^n \oplus 1] + 0 + 0 + \dots + 0 + 0 \end{aligned}$$

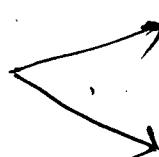
$$p \cdot X(p) \oplus X'(p) = x_{n-1} [p^n \oplus 1]$$

$$\Rightarrow X'(p) = p \cdot [X(p)] \oplus x_{n-1} [p^n \oplus 1]$$

$$\boxed{X'(p) = p \cdot X(p) \oplus x_{n-1} [p^n \oplus 1]}$$

The polynomial ' $p^n \oplus 1$ ' and its factors plays major role in cyclic codes.

Cyclic Codes



non systematic Cyclic codes:

Systematic Cyclic codes

(a) Non Systematic:

$$\text{Cyclic Code } X(P) = M(P) \cdot G(P) \rightarrow ①$$

where $M(P)$ - Message signal polynomial of degree ' k '.

$$M(P) = M_{k-1}P^{k-1} + M_{k-2}P^{k-2} + \dots + M_2P^2 + M_1P + M_0$$

$G(P)$ - Generating polynomial of degree ' q '.

$$G(P) = P^q + g_{q-1}P^{q-1} + \dots + g_2P^2 + g_1P + 1$$

$C(P)$ - Cyclic code polynomial

$$C(P) = C_{q-1}P^{q-1} + C_{q-2}P^{q-2} + \dots + C_2P^2 + C_1P + C_0$$

(b) Systematic Cyclic Code:

cyclic code

$$X(P) = P^q \cdot M(P) + C(P) \rightarrow ②$$

equating eqn ① & ②

$$M(P) \cdot G(P) = P^q M(P) + C(P)$$

Divide above eqn by $G(P)$.

$$\therefore M(P) = \frac{P^q M(P)}{G(P)} + \frac{C(P)}{G(P)}$$

Systematic
Cyclic

$$\frac{P^q M(P)}{G(P)} = M(P) \oplus \frac{C(P)}{G(P)}$$

↑
xor

This equation has the form of $\frac{N}{D} = Q + \frac{R}{D}$

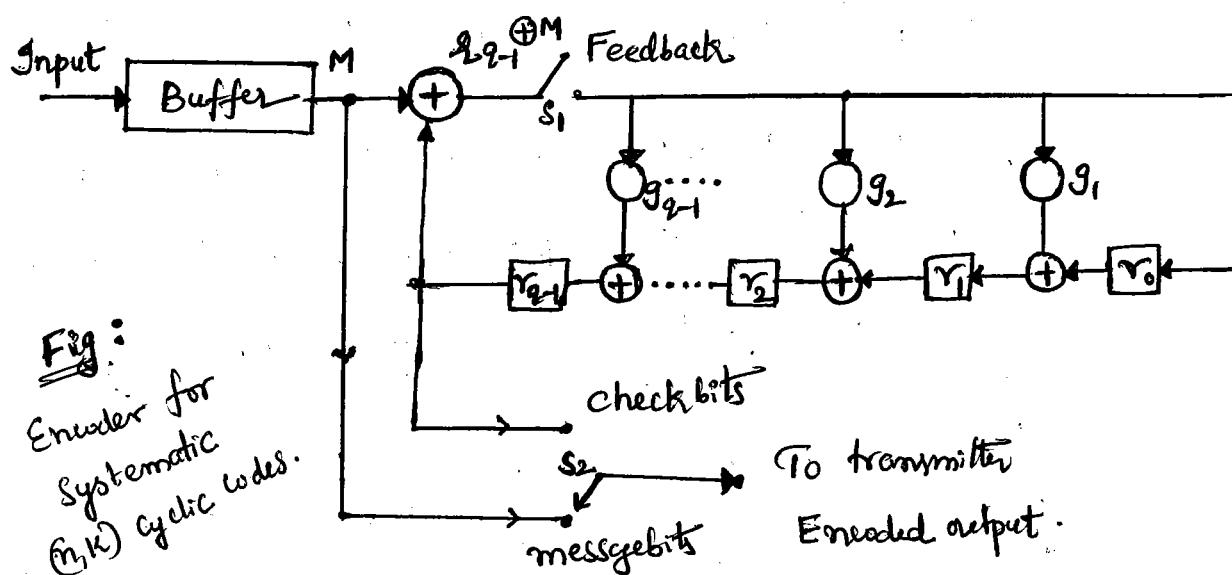
where
 N - numerator
 D - Denominator
 Q - Quotient
 R - Remainder

Thus the check bit polynomial is obtained as

remainder R is dividing $P^q \frac{M(P)}{G(P)}$.

$$\frac{N}{D} = Q + \frac{R}{D}$$

Encoder (or) Encoder diagram for Cyclic Codes :



$r_0, r_1, r_2, \dots, r_{k-1}$ represents flip flops (or) registers
they are connected in sequential order to make a shift register.

g_1, g_2, \dots, g_{k-1} represents path establishment.

- ✓ Encoding starts with the feedback switch S_1 closed, the output switch is connected to the message bits S_2 .
- ✓ All the shift registers are initiated to all zero state.
- ✓ K message bits are shifted to the transmitter as well as shifted into the registers.
- ✓ After the shift of K message bits, the register contains q check bits.
- ✓ The feedback switch S_1 is now opened and output switch is connected to check bit position.

Thus we can obtain all codewords as cyclic code.

Syndrome decoding for cyclic codes:

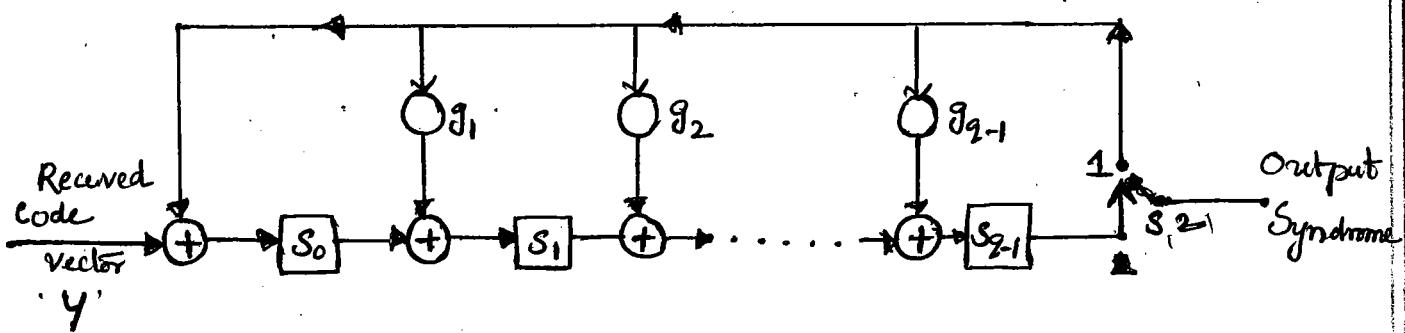


Fig: Syndrome decoding for cyclic codes.

- ✓ Initially all the shift registers are zeros and switch is connected to position ①.
- ✓ The received vector 'y' is shifted bit by bit into the shift registers.
- ✓ After all the bits of 'y' are shifted, q flip flops of shift registers contains 'q' bit syndrome vector.
- ✓ The switch is then closed to position ② & clock (For reset) are applied to the shift registers.
- ✓ The output ~~is~~ is a syndrome vector.

$$S = (S_{q-1}, S_{q-2}, \dots, S_1, S_0)$$

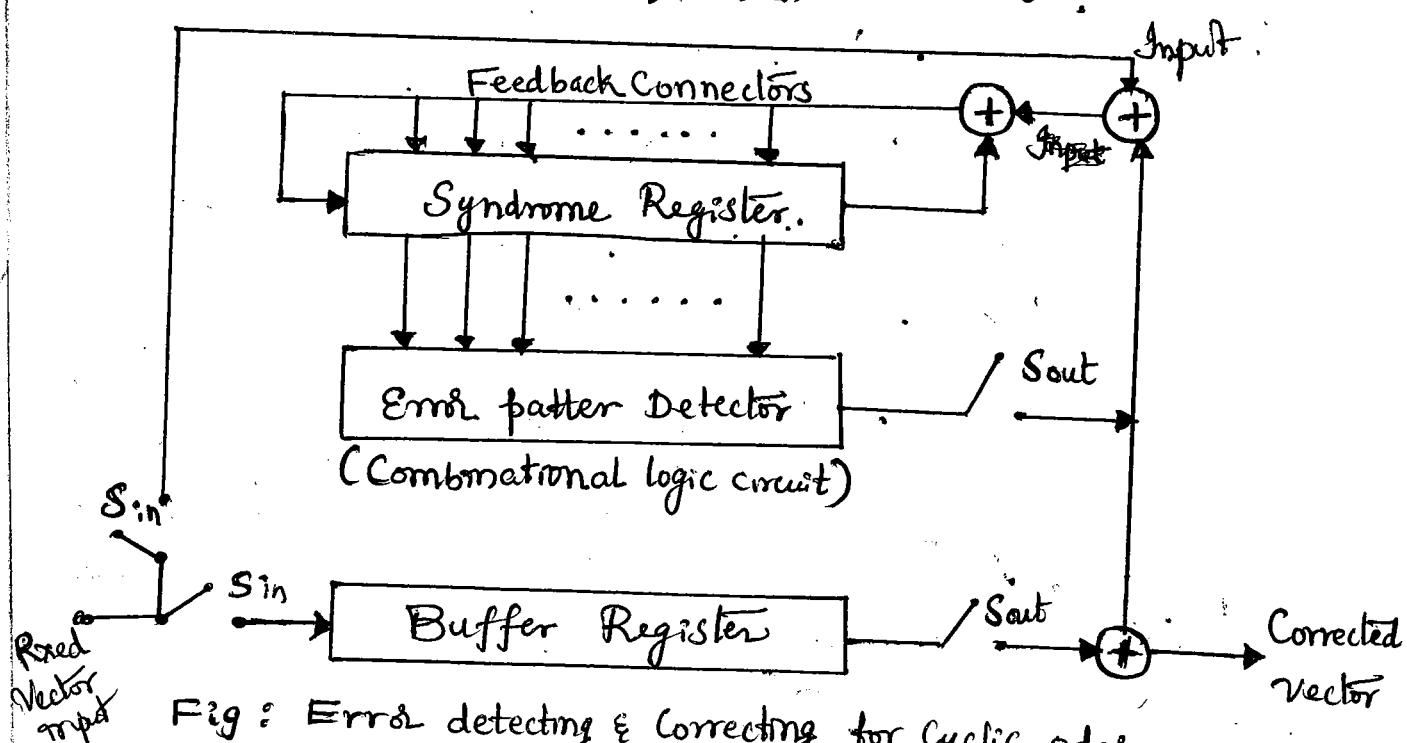


Fig: Error detecting & Correcting for cyclic codes.

- Once the syndrome is calculated, then the error pattern is detected for that particular syndrome vector.
- When this error vector is added to the received vector 'y' then it gives corrected code vector at the output.
- The switches S_{in} is closed then the bits of the received vector 'y' are shifted into the buffer register as well as they are shifted into the syndrome register.
- The output of the syndrome register is given to the error pattern detector.
- A particular syndrome detects a specific error pattern.
- The switches S_{in} is opened, S_{out} is closed. The error pattern is then added bit by bit with the received vector.

* Syndrome decoding can be used to correct the errors. Let 'y' represents the received code vector.

The error can be detected as $y = x + e$.

The error can be corrected as $x = y \oplus e$.

In the polynomial form $y(p) = x(p) + e(p)$

$$\text{We know } x(p) = M(p) \cdot G(p)$$

$$y(p) = M(p) \cdot G(p) + e(p)$$

Divide with $G(p)$ thus .

$$\frac{y(p)}{G(p)} = M(p) + \frac{e(p)}{G(p)}$$

$$\text{numerator } \frac{N}{D} = Q + \frac{R}{D} \text{ remainder}$$

The syndrome vector is obtained by dividing the received vector $y(p)$ with $G(p)$.

\therefore Syndrome polynomial

$$S(p) = \text{Rem. } \left(\frac{y(p)}{G(p)} \right)$$

Advantages of Cyclic Codes:

- ✓ The error correcting and detecting methods of cyclic codes are simple and easy to implement.
- ✓ It is very simple compared to linear block codes.

Disadvantage of cyclic code:

- ✓ Error detection in cyclic codes is simple but the error correction is complicated in cyclic codes.

Problem:

(1)

A (7,4) cyclic code generated by $G(p) = p^3 + p + 1$.

- Find all the code vectors for the code in non-systematic code.
- Find all the code vectors for the code in systematic code.
- Design the encoder for given cyclic code and verify its operation for any message vector.
- Design a syndrome calculator for a given cyclic code and calculate the syndrome for $y = 1001101$.
- Find out the generator matrix by using of cyclic codes.
- Find the generator polynomial for the given cyclic code.

Solution:

Sol.

Given $G(p) = p^3 + p + 1$

(7,4) Cyclic code $\therefore n=7$
 $k=4$

Parity bits $q = n - k = 7 - 4 = 3$

Since $k=4$, there will be $2^k = 2^4 = 16$ different message code vectors.

Each message vector consists of 4 bits.

For $\begin{matrix} 0 & 0 & 0 & 1 \\ M_3 & M_2 & M_1 & M_0 \end{matrix} \Rightarrow M(p) = M_3 p^3 + M_2 p^2 + M_1 p + M_0 p^0$

$$\begin{aligned} &= 0 \cdot p^3 + 0 \cdot p^2 + 0 \cdot p^1 + 1 \cdot 1 \\ &= 1 \\ &\therefore M(p) = 1 \end{aligned}$$

By $0010 \rightarrow P, 1001 \rightarrow p^3 + 1$ ie.

| SL No | Message bits M ₃ M ₂ M ₁ M ₀ | Message Polynomial M(p) |
|-------|---|-------------------------------------|
| 1 | 0 0 0 0 | 0 |
| 2 | 0 0 0 1 | 1 |
| 3 | 0 0 1 0 | p |
| 4 | 0 0 1 1 | p+1 |
| 5 | 0 1 0 0 | p ² |
| 6 | 0 1 0 1 | p ² +1 |
| 7 | 0 1 1 0 | p ² +p |
| 8 | 0 1 1 1 | p ² +p+1 |
| 9 | 1 0 0 0 | p ³ |
| 10 | 1 0 0 1 | p ³ +1 |
| 11 | 1 0 1 0 | p ³ +p |
| 12 | 1 0 1 1 | p ³ +p+1 |
| 13 | 1 1 0 0 | p ³ +p ² |
| 14 | 1 1 0 1 | p ³ +p ² +1 |
| 15 | 1 1 1 0 | p ³ +p ² +p |
| 16 | 1 1 1 1 | p ³ +p ² +p+1 |

(a)

Non-S systematic Code:

$$x(p) = M(p) \cdot G(p)$$

$$G(p) = p^3 + p + 1$$

$$\text{For } ①(0000) \rightarrow M(p) = 0 \quad \therefore x(p) = 0 \cdot (p^3 + p + 1) = \underline{\underline{0000000}}$$

$$\text{for } ②(0001) \rightarrow M(p) = 1 \quad \therefore x(p) = 1 \cdot (p^3 + p + 1) = p^3 + p + 1$$

$$\text{for } ③(0010) \rightarrow M(p) = p \quad x(p) = p(p^3 + p + 1) = p^4 + p^2 + p. \quad \therefore x_2(p) = \underline{\underline{0001011}}$$

$$= (0010110)$$

$$\text{for } ④(0011) \quad M(p) = p+1 \quad x(p) = (p+1)(p^3 + p + 1)$$

$$= p^4 + p^2 + p + p^3 + p + 1$$

$$= p^4 + p^3 + p^2 + p \oplus p + 1$$

$$\text{for } ⑤(0100) \quad M(p) = p^2 \quad x(p) = p^2(p^3 + p + 1) \quad = (0011101)$$

$$\text{for } ⑥(0101) \quad M(p) = p^2 + 1 \quad x(p) = (p^2 + 1)(p^3 + p + 1) \quad = p^5 + p^3 + p^2 = \underline{\underline{0101100}}$$

$$= p^5 + p^3 + p^2 + p^3 + p + 1$$

$$= p^5 + p^3 \oplus p^3 + p^2 + p + 1$$

$$= p^5 + p^2 + p + 1$$

$$= \underline{\underline{0100111}}$$

$$\text{For } \textcircled{7} (0110) \rightarrow M(p) = p^2 + p \quad \therefore X(p) = (p^2 + p)(p^3 + p + 1) \\ = p^5 + p^3 + p^2 + p^4 + p^2 + p \\ = p^5 + p^4 + p^3 + p \\ = \underline{(0111010)}$$

$\rightarrow \underline{0111}$ at last.

$$\text{for } \textcircled{8} (1000) \rightarrow M(p) = p^3$$

$$\therefore X(p) = p^3(p^3 + p + 1) \\ = p^6 + p^4 + p^3 = \underline{(1011000)}$$

$$\text{for } \textcircled{9} (1001) \rightarrow M(p) = p^3 + 1$$

$$X(p) = (p^3 + 1)(p^3 + p + 1)$$

$$= p^6 + p^4 + p^3 + p^3 + p + 1$$

$$= p^6 + p^4 + p + 1 = \underline{(1010011)}$$

$$\text{for } \textcircled{10} (1010) \rightarrow M(p) = p^3 + p$$

$$X(p) = (p^3 + p)(p^3 + p + 1)$$

$$= p^6 + p^4 + p^3 + p^4 + p^2 + p$$

$$= p^6 + p^3 + p^2 + p = \underline{(1001110)}$$

$$\text{for } \textcircled{11} (1011) \rightarrow M(p) = p^3 + p + 1$$

$$\therefore X(p) = (p^3 + p + 1)(p^3 + p + 1)$$

$$= p^6 + p^4 + p^3 + p^4 + p^2 + p + p^3 + p + 1$$

$$= p^6 + p^2 + 1 = \underline{(1000101)}$$

$$\text{for } \textcircled{12} (1100) \rightarrow M(p) = p^3 + p^2$$

$$\therefore X(p) = (p^3 + p^2)(p^3 + p + 1)$$

$$= p^6 + p^4 + p^3 + p^5 + p^3 + p^2$$

$$= (p^6 + p^5 + p^4 + p^2) = \underline{(1110100)}$$

$$\text{for } \textcircled{13} (1101) \quad M(p) = p^3 + p^2 + 1$$

$$\therefore X(p) = (p^3 + p^2 + 1)(p^3 + p + 1)$$

$$= p^6 + p^4 + p^3 + p^5 + p^3 + p^2 + p^3 + p + 1$$

$$= p^6 + p^5 + p^4 + p^3 + p^2 + p + 1$$

$$= \underline{(1111111)}$$

$$\text{for } \textcircled{14} (1110) \quad M(p) = p^3 + p^2 + p$$

$$\therefore X(p) = (p^3 + p^2 + p)(p^3 + p + 1)$$

$$= p^6 + p^4 + p^3 + p^5 + p^3 + p^2 + p^4 + p^2 + p$$

$$= p^6 + p^5 + p = \underline{(1100010)}$$

$$\text{for } \textcircled{15} (1111) \quad M(p) = p^3 + p^2 + p + 1$$

$$\therefore X(p) = (p^3 + p^2 + p + 1)(p^3 + p + 1)$$

$$= p^6 + p^4 + p^3 + p^5 + p^3 + p^2 + p^4 + p^2 + p^3 + p + 1$$

$$= p^6 + p^5 + p^3 + 1 = \underline{(1101001)}$$

$$\rightarrow \textcircled{16} (0111) \quad M(p) = p^3 + p + 1$$

$$\therefore X(p) = (p^3 + p + 1)(p^3 + p + 1)$$

$$= p^6 + p^4 + p^3 + p^4 + p^2 + p + p^3 + p + 1$$

$$= (p^5 + p^4 + 1) = \underline{(0110001)}$$

| SL NO | Message bits M ₃ M ₂ M ₁ M ₀ | <u>Non Systematic Cyclic code Vectors</u> | <u>Systematic Cyclic code vectors.</u> |
|-------|---|---|--|
| 1 | 0 0 0 0 | 0 0 0 0 0 0 0 | 0 0 0 0 0 0 0 |
| 2 | 0 0 0 1 | 0 0 0 1 0 1 1 | 0 0 0 1 0 1 1 |
| 3 | 0 0 1 0 | 0 0 1 0 1 1 0 | 0 0 1 0 1 1 0 |
| 4 | 0 0 1 1 | 0 0 1 1 1 0 1 | 0 0 1 1 1 0 1 |
| 5 | 0 1 0 0 | 0 1 0 1 1 0 0 | 0 1 0 0 1 1 1 |
| 6 | 0 1 0 1 | 0 1 0 0 1 1 1 | 0 1 0 1 1 0 0 ✓ |
| 7 | 0 1 1 0 | 0 1 1 1 0 1 0 | 0 1 1 0 0 0 1 |
| 8 | 0 1 1 1 | 0 1 1 0 0 0 1 | 0 1 1 1 0 1 0 |
| 9 | 1 0 0 0 | 1 0 1 1 0 0 0 | 1 0 0 0 1 0 1 |
| 10 | 1 0 0 1 | 1 0 1 0 0 1 1 | 1 0 0 1 1 1 0 |
| 11 | 1 0 1 0 | 1 0 0 1 1 1 0 | 1 0 1 0 0 1 1 |
| 12 | 1 0 1 1 | 1 0 0 0 1 0 1 | 1 0 1 1 0 0 0 |
| 13 | 1 1 0 0 | 1 1 1 0 1 0 0 | 1 1 0 0 0 1 0 |
| 14 | 1 1 0 1 | 1 1 1 1 1 1 1 | 1 1 0 1 0 0 1 |
| 15 | 1 1 1 0 | 1 1 0 0 0 1 0 | 1 1 1 0 1 0 0 |
| 16 | 1 1 1 1 | 1 1 0 1 0 0 1 | 1 1 1 1 1 1 1 |

(b)

Systematic Code : , message $\begin{matrix} K \\ 2 \end{matrix}$ parity. ↓

The check bits can be generated by using

$$C(p) = \text{Rem} \left[\frac{p^2 M(p)}{G(p)} \right], \quad q = n - k = 3.$$

For (0 0 0 0) $\rightarrow M(p) = 0 \xrightarrow{p^3} p^3(0) = 0, \frac{p^2(M(p))}{G(p)} \Rightarrow C(p) = (0, 0, 0)$

For (0 0 0 1) $\rightarrow M(p) = 1, p^3(M(p)) = p^3 \quad \therefore X = (M_3 M_2 M_1 M_0 C_2 C_1 C_0)$

$$C(p) = \frac{p^3 M(p)}{G(p)} = \frac{p^3}{p^3 + p + 1} = 011$$

$$\frac{p^3 + p + 1}{p^3} \overline{\frac{p^3}{p^3 + p + 1}} \quad X = (0 0 0 0 0 0)$$

$$\therefore X(p) = (0 0 0 1 0 1 1)$$

$$\frac{p^3 + p + 1}{p^3} \overline{\frac{p^3}{p^3 + p + 1}} \quad P+1 \Rightarrow 011$$

For (0 0 1 0) $\rightarrow M(p) = p \Rightarrow p^3(M(p)) = p^4.$

$$\frac{p^3 M(p)}{G(p)} = \frac{p^4}{p^3 + p + 1} = 110$$

$$\frac{p^3 + p + 1}{p^3} \overline{\frac{p^4}{p^4 + p^2 + p}} \quad P^2 + P \Rightarrow (1 1 0)$$

$$\therefore X(p) = (0 0 1 0 1 1 0)$$

for (0011), $M(p) = p+1$, $p^3 M(p) = p^3(p+1) = p^4 + p^3$

$$\frac{p^3 M(p)}{G(p)} = \frac{p^4 + p^3}{p^3 + p + 1} = (101)$$

$$\frac{p^3 + p + 1}{p^4 + p^3 + p} = \frac{p+1}{p^3 + p + 1}$$

$$\therefore X(p) = (0011101)$$

for (0100), $M(p) = p^2$, $p^3 M(p) = p^3 \cdot p^2 = p^5$

$$\frac{p^3 M(p)}{G(p)} = \frac{p^5}{p^3 + p + 1} = (111)$$

$$\therefore X(p) = (0100111)$$

$$(101) \leftarrow \underline{\underline{p^2 + 1}}$$

$$\frac{p^2 + 1}{p^5 / p^3 + p^2} = \frac{p^2 + 1}{p^3 + p + 1}$$

$$(111) \leftarrow \underline{\underline{p^2 + p + 1}}$$

for (0101), $M(p) = p^2 + 1$, $p^3 M(p) = p^3(p^2 + 1) = p^5 + p^3$

$$\frac{p^3 M(p)}{G(p)} = \frac{p^5 + p^3}{p^3 + p + 1} = (100)$$

$$\frac{p^2}{p^5 / p^3 + p^2} = \frac{p^2}{p^3 + p + 1}$$

$$\therefore X(p) = (0101100)$$

$$(100) \leftarrow \underline{\underline{p^2}}$$

for (0110), $M(p) = p^2 + p$, $p^3 M(p) = p^3(p^2 + p)$

$$\frac{p^3 M(p)}{G(p)} = \frac{p^5 + p^4}{p^3 + p + 1} = p^5 + p^4$$

$$\frac{p^2 + p + 1}{p^5 / p^4 + p^3} = \frac{p^2 + p + 1}{p^4 + p^3 + p^2}$$

$$\therefore X(p) = (0110001)$$

$$\frac{p^4 + p^3 + p^2}{p^4 + p^2 + p} = \frac{p^4 + p^3 + p^2}{p^4 + p^2 + p}$$

for (0111), $M(p) = p^2 + p + 1$, $p^3 M(p) = p^3(p^2 + p + 1)$

$$\frac{p^3 M(p)}{G(p)} = \frac{p^5 + p^4 + p^3}{p^3 + p + 1} = p^5 + p^4 + p^3$$

$$(001) \leftarrow \underline{\underline{1}}$$

$$\frac{p^2 + p}{p^5 / p^4 + p^3} = \frac{p^2 + p}{p^4 + p^3 + p^2}$$

$$\frac{p^5 + p^4 + p^3 + p^2}{p^5 / p^4 + p^3} = \frac{p^5 + p^4 + p^3 + p^2}{p^5 + p^4 + p^3}$$

$$\therefore X(p) = (0111010)$$

for (1000), $M(p) = p^3$, $p^3 M(p) = p^3 \cdot p^3 = p^6$

$$\frac{p^3 M(p)}{G(p)} = \frac{p^6}{p^3 + p + 1} = (101)$$

$$(010) \leftarrow \underline{\underline{p}}$$

$$\frac{p^3 + p + 1}{p^6 / p^5 + p^4} = \frac{p^3 + p + 1}{p^5 + p^4 + p^3}$$

$$\frac{p^6}{p^6 + p^5 + p^4} = \frac{p^6}{p^6 + p^5 + p^4}$$

$$\therefore X(p) = (1000101)$$

for (1001), $M(p) = p^3 + 1$, $p^3 M(p) = p^3(p^3 + 1)$

$$\frac{p^3 M(p)}{G(p)} = \frac{p^6 + p^3}{p^3 + p + 1} = p^6 + p^3$$

$$(101) \leftarrow \underline{\underline{p^2 + 1}}$$

$$\frac{p^3 + p + 1}{p^6 / p^5 + p^4} = \frac{p^3 + p + 1}{p^5 + p^4 + p^3}$$

$$\frac{p^6 + p^5 + p^4}{p^6 + p^5 + p^4} = \frac{p^6 + p^5 + p^4}{p^6 + p^5 + p^4}$$

$$\therefore X(p) = (1001110)$$

$$(110) \leftarrow \underline{\underline{p^2 + p}}$$

- (11) For (1010), $M(p) = p^3 + p$, $\therefore p^3 M(p) = p^3(p^3 + p) = p^6 + p^4$
- $$\frac{p^3 M(p)}{G(p)} = \frac{p^6 + p^4}{p^3 + p + 1} = (011)$$
- $$\therefore X(p) = (1010011)$$
- (12) For (1011), $M(p) = p^3 + p + 1$, $p^3 M(p) = p^3(p^3 + p + 1) = p^6 + p^4 + p^3$
- $$\frac{p^3 M(p)}{G(p)} = \frac{p^6 + p^4 + p^3}{p^3 + p + 1} = (000)$$
- $$\therefore X(p) = (1011000)$$
- (13) For (1100), $M(p) = p^3 + p^2$, $p^3 M(p) = p^3(p^3 + p^2) = p^6 + p^5$
- $$\frac{p^3 M(p)}{G(p)} = \frac{p^6 + p^5}{p^3 + p + 1} = (010)$$
- $$\therefore X(p) = (1100010)$$
- (14) For (1101), $M(p) = p^3 + p^2 + 1$, $p^3 M(p) = p^3(p^3 + p^2 + 1) = p^6 + p^5 + p^3$
- $$\frac{p^3 M(p)}{G(p)} = \frac{p^6 + p^5 + p^3}{p^3 + p + 1} = (001)$$
- $$\therefore X(p) = (1101001)$$
- (15) For (1110), $M(p) = p^3 + p^2 + p$, $p^3 M(p) = p^3(p^3 + p^2 + p) = p^6 + p^5 + p^4$
- $$\frac{p^3 M(p)}{G(p)} = \frac{p^6 + p^5 + p^4}{p^3 + p + 1} = (100)$$
- $$\therefore X(p) = (1110100)$$
- (16) For (1111), $M(p) = p^3 + p^2 + p + 1$, $p^3 M(p) = p^3(p^3 + p^2 + p + 1) = p^6 + p^5 + p^4 + p^3$
- $$\frac{p^3 M(p)}{G(p)} = \frac{p^6 + p^5 + p^4 + p^3}{p^3 + p + 1} = (111)$$
- $$\therefore X(p) = (1111111)$$

(C) The given generator polynomial is $G_1(p) = p^3 + p + 1$

$$\therefore G_1(p) = p^3 + g_{2-1}p^{2-1} + g_{2-2}p^{2-2} + \dots + g_2p^2 + g_1p^1 + 1.$$

$$\underline{q=3}. \quad \therefore G_1(p) = p^3 + g_2p^2 + g_1p^1 + 1.$$

Compare with $G_1(p) = p^3 + p + 1$ (given)

$$\therefore g_2 = 0 \text{ (Not existing path).}$$

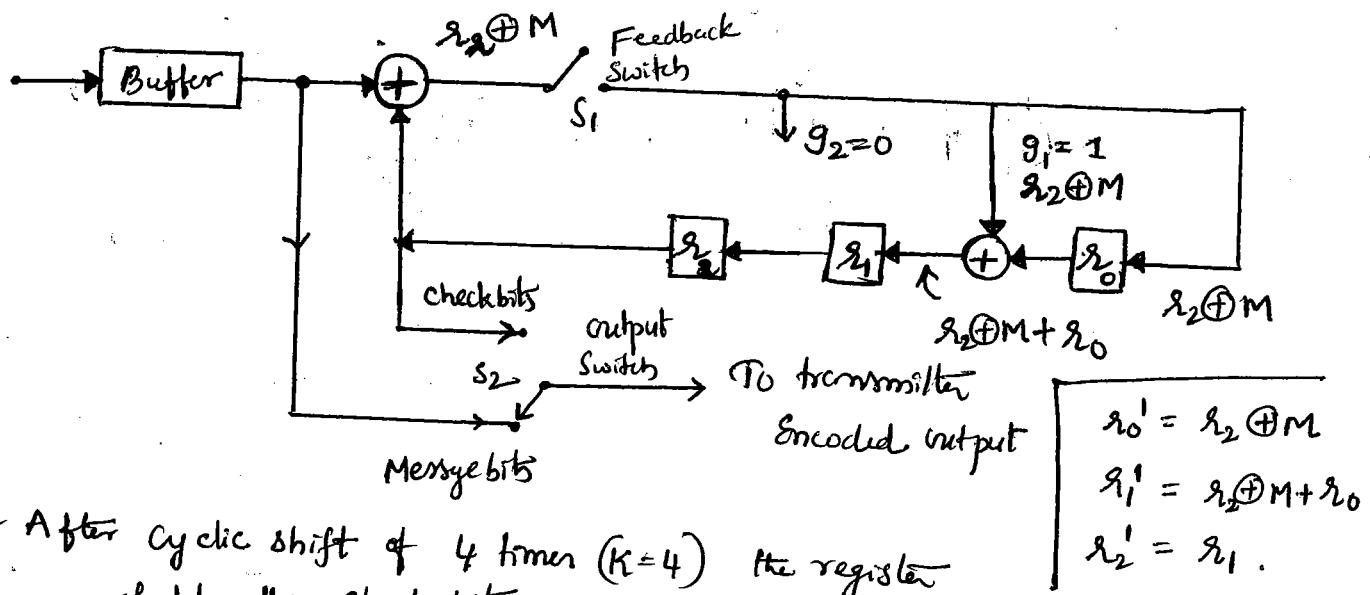
$$g_1 = 1 \text{ (Existing path).}$$

Since $q=3$, there are 3 flip flops in shift registers.

$$\begin{aligned} q &= n-k \\ &= 7-4 \\ &= 3 \end{aligned}$$

Since $g_2 = 0$ its link is not connected and
 $g_1 = 1$ its link is connected.

Encoder diagram :



- After cyclic shift of 4 times ($k=4$) the register hold the check bits.

$$\begin{cases} r_0' = r_2 \oplus M \\ r_1' = r_2 \oplus M + r_0 \\ r_2' = r_1 \end{cases}$$

For Message
(0101)

| (a) Input Message bits | (b) Registers bit input before shift | | | (c) Registers bit output after shift | | |
|---------------------------------|--|----------------|----------------|--|-----------------------------------|--|
| | M | r ₂ | r ₁ | r ₀ | r ₂ ' = r ₁ | r ₁ ' = r ₀ ⊕ r ₂ ⊕ M |
| - | 0 | 0 | 0 | 0 | 0 | 0 |
| 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 1 | 0 | 0 | 0 | 0 | 1 | 1 |
| 0 | 0 | 1 | 1 | 1 | 1 | 0 |
| 1 | 1 | 1 | 0 | 1 | 0 | 0 |

$$\therefore X = [M_3 \ M_2 \ M_1 \ M_0 \ C_2 \ C_1 \ C_0] \Rightarrow X = [0 \ 1 \ 0 \ 1 \ 1 \ 0 \ 0]$$

Hence Verified

msg bits ↴ check bits

(d) Syndrome decoder: For the given code $n=7$, $k=4$, $q=n-k=3$.

Method ①: The given polynomial of the generator: $G(p)=p^3+p+1$

$$G(p) = p^3 + g_{2-1}p^{2-1} + g_{2-2}p^{2-2} + \dots + g_1p + 1.$$

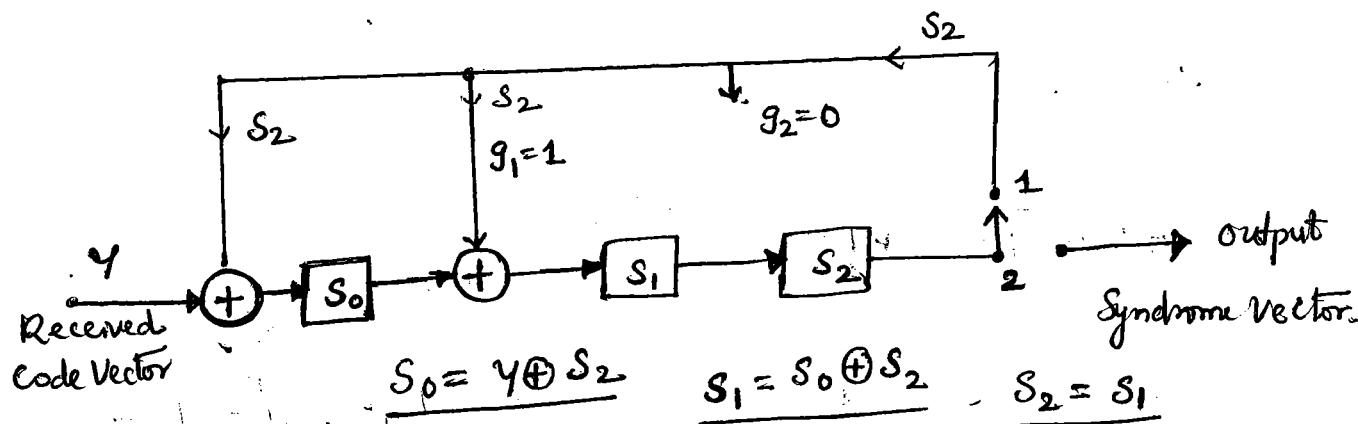
$$q=3 \quad G(p) = p^3 + g_2p^2 + g_1p + 1.$$

Compare with $g_1(p) = p^3 + p + 1$

$\therefore g_2 = 0$ (No parity)

$g_1 = 1$ (Parity established).

$$y = [1 \ 0 \ 0 \ 1 \ 1 \ 0 \ 1]$$



| Shift | Received Vector k bits in 'y' | Content of flip flop in shift registers | | |
|-------|----------------------------------|---|-------------------|-------------|
| | | $S_0 = y + S_2$ | $S_1 = S_0 + S_2$ | $S_2 = S_1$ |
| - | - | 0 | 0 | 0 |
| 1 | 1 | 1 | 0 | 0 |
| 2 | 0 | 0 | 1 | 0 |
| 3 | 0 | 0 | 0 | 1 |
| 4 | 1 | 0 | 1 | 0 |
| 5 | 1 | 1 | 0 | 1 |
| 6 | 0 | 1 | 0 | 0 |
| 7 | 1 | 1 | 1 | 0 |

(previous bits operate)

The end of the last shift registers contents are. $S_0S_1S_2 = 110$

Hence the calculated Syndrome $S = (S_2, S_1, S_0) = 011$

Method ②: Syndrome Vector is obtained by dividing received vector $y(p)$ by $G(p)$.

$$\therefore S(p) = \text{Rem} \left[\frac{y(p)}{G(p)} \right]$$

Given:
Received Vector $y = [1 \ 0 \ 0 \ 1 \ 1 \ 0 \ 1]$

$$y(p) = 1 \cdot p^6 + 0 \cdot p^5 + 0 \cdot p^4 + 1 \cdot p^3 + 1 \cdot p^2 + 0 \cdot p^1 + 1$$

$$\therefore y(p) = p^6 + p^3 + p^2 + 1$$

Generator polynomial $G(p) = p^3 + p + 1$

$$\begin{array}{r} \frac{y(p)}{G(p)} \Rightarrow \frac{p^6 + p^3 + p^2 + 1}{p^3 + p + 1} \\ \qquad\qquad\qquad \therefore \frac{p^3 + p}{p^3 + p + 1} \overline{) p^6 + p^3 + p^2 + 1} \\ \qquad\qquad\qquad \underline{p^6 + p^4 + p^3} \\ \qquad\qquad\qquad p^4 + p^2 + 1 \\ \qquad\qquad\qquad \underline{p^4 + p^2 + p} \\ \text{Syndrome Vector} \\ \boxed{S = 011} \\ \qquad\qquad\qquad S_2 \ S_1 \ S_0. \\ \qquad\qquad\qquad \text{Rem: } 011 \leftarrow \underline{p+1} \end{array}$$

(e)

Generator Matrix by using Cyclic Codes:

Given $n=7, k=4, q=n-k=7-4 \Rightarrow q=3$.

$$G(p) = p^3 + p + 1 \Rightarrow 1 \cdot p^3 + 0 \cdot p^2 + 1 \cdot p + 1$$

Multiply with ' p^i ' on both sides i.e.

$$\begin{aligned} p^i \cdot G(p) &= p^i \cdot p^3 + p^i \cdot p + p^i \cdot 1 \\ &= p^{3+i} + p^{1+i} + p^i \end{aligned}$$

Since $k=4$, the no. of rows of generated matrix is given as

$$k-1=3, i=3, 2, 1, 0.$$

For row 1, $i=3, p^3 \cdot G(p) = p^6 + p^4 + p^3 \Rightarrow 1 \ 0 \ 1 \ 1 \ 0 \ 0 \ 0$

For row 2, $i=2, p^2 \cdot G(p) = p^5 + p^3 + p^2 \Rightarrow 0 \ 1 \ 0 \ 1 \ 1 \ 0 \ 0$

For row 3, $i=1, p^1 \cdot G(p) = p^4 + p^2 + p \Rightarrow 0 \ 0 \ 1 \ 0 \ 1 \ 1 \ 0$

For row 4, $i=0, p^0 \cdot G(p) = p^3 + p + 1 \Rightarrow 0 \ 0 \ 0 \ 1 \ 0 \ 1 \ 1$

∴ Generator Matrix

$$G_1 = \begin{bmatrix} p^6 & p^5 & p^4 & p^3 & p^2 & p^1 & p^0 \\ \vdots & \vdots & \vdots & \vdots & \vdots & \vdots & \vdots \\ 1 & 0 & 1 & 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 1 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 & 1 & 1 & 0 \\ 0 & 0 & 0 & 1 & 0 & 1 & 1 \end{bmatrix}_{K \times n} \quad 4 \times 7$$

Note : Since the generator matrix is non systematic form
Hence parity check matrix can not be obtained using
direct method ($H = [P^T : I]$) .

(f) For (7,4) Cyclic codes.

$$n=7, K=4, q=n-k=7-4=3 \Rightarrow q=3.$$

The generator polynomial is a factor of $[p^n + 1]$

$$\therefore p^7 + 1 = [p+1] [p^3 + p^2 + 1] [p^3 + p + 1]$$

$$\therefore [p^7 + 1 = (p+1) (p^3 + p^2 + 1) (p^3 + p + 1)]$$

The valid generating polynomial is given by $G_i(p)$ as

$$G_1(p) = p^2 + g_{q-1} p^{q-1} + \dots + g_2 p^2 + g_1 p^1 + 1$$

$$\underline{q=3} \quad \therefore G(p) = p^3 + g_2 p^2 + g_1 p^1 + 1$$

The degree of generating polynomial should be 'q'

For this problem $q=3$

∴ The valid generator polynomials for $p^7 + 1$ will be
 $(p^3 + p^2 + 1)$ and $(p^3 + p + 1)$

' $p+1$ ' will not be a generated polynomial since its degree
is not q ($q=3$) .

∴ The generator polynomials for given (7,4) cyclic codes are

$$G_1(p) = p^3 + p^2 + 1$$

$$G_2(p) = p^3 + p + 1$$

23

The characteristics of five important classes of Cyclic Codes :

① Cyclic Redundancy Check codes (CRC Codes) :

- ✓ Cyclic Codes are extremely well suited for error detection.
ie "A cyclic code used for error detection is referred to as cyclic redundancy check (CRC) code".
- ✓ A CRC error burst of length B in an n -bit received word as a contiguous sequence of B bits in which the first & last bits of any no. of intermediate bits are received in error.
- * Binary (n, k) CRC codes are capable of detecting the following error patterns.
 - (i) All CRC error bursts of length ' $n-k$ ' or less.
 - (ii) A fraction of CRC error bursts of length equal to ' $n-k+1$ ', the fraction equals $1 - 2^{-(n-k-1)}$.
 - (iii) A fraction of CRC error bursts of lengths greater than ' $n-k+1$ ', the fraction equals $1 - 2^{-(n-k)}$.
 - (iv) All combinations of $d_{\min} - 1$ (or fewer) errors.
 - (v) All error patterns with an odd no. of errors if the generator polynomial $g(x)$ for the code has an even no. of non zero coefficients.

Ex: CRC-12 Code : $1 + p + p^2 + p^3 + p^{11} + p^{12}$

CRC-16 code : $1 + p + p^{15} + p^{16}$, $n-k=12$
 , $n-k=16$.

② Maximum length codes :

For positive integer $k \geq 3$, there exists a maximum length code with the parameters as

$$\text{Block length } n = 2^k - 1$$

$$\text{Min. distance } d_{\min} = 2^{k-1}$$

The max. length codes are generated by polynomials of the form

$$g(p) = \frac{1 + p^n}{h(p)}$$

where $h(p) = \text{any primitive polynomial of degree } k$.

i.e The maximum length codes are the dual of Hamming Codes.

- ✓ The maximum length codes are also referred to as Pseudo Noise (PN) codes.
- ✗ The name "Pseudo Noise" is derived from the fact that these codes have correlation & spectral characteristics that resemble those of a white noise sequence.

③ Golay Codes : $(n, k) \rightarrow (23, 12)$

- ✓ A Golay code is a very special binary code that is capable of correcting any combinations of three or fewer random error in a block of 23 bits.
- ✓ The code has minimum distance of '7'.
- ✗ Indeed the $(23, 12)$ Golay code is the only known three correcting binary perfect cyclic code.

Ex: The $(23, 12)$ Golay code is generated either by the polynomial

$$(or) \quad g_1(p) = 1 + p^2 + p^4 + p^6 + p^{10} + p^{11}.$$

$$g_2(p) = 1 + p + p^5 + p^6 + p^7 + p^9 + p^{11}.$$

Both $g_1(p)$ & $g_2(p)$ are factors of ' $1 + D^{23}$ '.

$$\therefore 1 + D^{23} = (1 + p)[g_1(p) \cdot g_2(p)]$$

∴ The Golay code does not generalize to other combinations of parameters n & k .

④ BCH (Bose-Chaudhuri-Hocquenghem) Codes:

- ✓ One of the most important & powerful classes of linear block codes are BCH codes.
- ✓ Specifically, for any positive integers k (equal to or > 3) & t ($\leq (2^k - 1)/2$) there exists a binary BCH code with parameters

Block length $n = 2^k - 1$

No. of msg bits $K \leq n$

Minimum distance $d_{\min} \geq 2t + 1$.

- ✓ Each BCH code is a t -error correcting code
 - ie It can detect & correct upto ' t ' random errors per codeword.
 - The hamming single error correcting codes can be described ~~BCH~~ codes.
- ✓ BCH codes offer flexibility in the choice of code parameters
 - ie Code rate and block length.
- ✓ Generator polynomials for binary block BCH codes of length upto $2^5 - 1$.

(5) Reed-Solomon (RS) Codes:

- ✓ The Reed Solomon codes are an important subclass of nonbinary BCH codes.
- ✓ The encoder for an RS code differs from a binary encoder in that it operates on multiple bits rather than individual bits.
- * The encoder for an RS (n, k) code on m -bit symbols groups the incoming binary data stream into blocks, each k_m bits long. Each block is treated as K -symbols, with each symbol having m -bits.
- ✓ The m -bit symbols are called bytes, m is integer power of two.
 - 8 bit RS codes are extremely powerful.

A t -error Correcting RS code has

- ✓ Block length : $n = 2^k - 1$ symbols
- ✓ Message size : K -symbols.
- ✓ Parity check size : $n - K = 2t$ symbols
- ✓ Minimum distance : $d_{\min} = 2t + 1$ symbols.
- ✓ Every RS code is a maximum distance separable code.
- * RS-Codes \rightarrow Highly efficient use of redundancy, & block length & symbol sizes can be adjusted readily & efficient decoding.

Convolutional Codes :

~~~~x~~~~x~~~~

- ✓ Convolutional codes were first introduced by Elias (1995) as an alternative to block codes.
- \* In block coding, the encoder accepts a  $K$ -bit message blocks and generates an  $n$ -bit codeword. Thus codewords are produced on a block-by-block basis.

However, where the message bits come in serially rather than in large blocks, in which case the use of a buffer may be undesirable. In such situations, use convolutional Coding.

- \* The main differences between block Codes and Convolutional Codes as follows

- ① In a block codes, a block of  $n$ -bits generated by the encoder in a particular time unit depends only on the block of  $K$ -input message bits within that time during.
- ② In a convolutional code, the block of  $n$ -Code bits generated by the encoder in the time during depends on not only the block of  $K$ -message bits but also on the previous  $(N-1)$  block of messages  $(N \geq 1)$ .
- ③ Like block codes, convolutional codes can be designed both for detecting and correcting of the errors.
- ④ Block Codes are better suitable for error detection and Convolutional Codes are mainly used for error correction.
- ⑤ A Convolutional encoder operates on the incoming message sequence continuously in a serial manner.
- ⑥ Convolutional Codes are well suited for mobile communications and satellite communications etc.
- ⑦ The use of non systematic codes is ordinarily preferred over systematic codes in Convolutional Codes.

## Encoder for Convolutional Codes :

$\text{m m m} \times \text{m m m} \times \text{m m m} \times \text{m m m m m}$

- ✓ A convolutional code can be generated by using of shift register and X-OR operation.

- ✓ The general representation of convolutional code is

$$(n, k, M)$$

where

$n \rightarrow$  No. of X-OR adders.

$k \rightarrow$  At a time no. of bits given to the register

$M \rightarrow$  No. of Shift Registers.

let

$L \rightarrow$  No. of bits present in the message sequence.

$L+M \rightarrow$  Total no. of shifts required to reset the register.

$n(L+M) \rightarrow$  Total no. of bits in the Encoder output sequence.

- ✓ The code rate for the convolutional code is given by

$$R_C = \frac{L}{n(L+M)} \quad (\because R_C = \frac{K}{n} \text{ for block codes})$$

If  $L \gg M$  then

$$R_C = \frac{L}{n \cdot L} = \frac{1}{n}$$

Code rate:  $R_C = \frac{1}{n}$  bits/symbol

The Constraint length ( $N$ ) of convolutional codes is defined as the no. of shift over which a single message bit can influence the encoder output.

In an encoder with an  $M$ -stage shift registers the memory of the encoder equals,  $M$  message bits and  $N=M+1$  shifts are required for a message bit to enter the shift register and finally come out.

Hence the constraint length of the encoder is

$$N = M + 1$$

Example:  $(2, 1, 3)$  Convolutional Codes. Find the output of the encoder for an input data stream of 1011.

Sol: Given  $(n, k, M)$  convolutional codes as  $(2, 1, 3)$

$n = 2 \rightarrow$  No. of X-OR adders.

$k = 1 \rightarrow$  At a time no. of bits given to the register

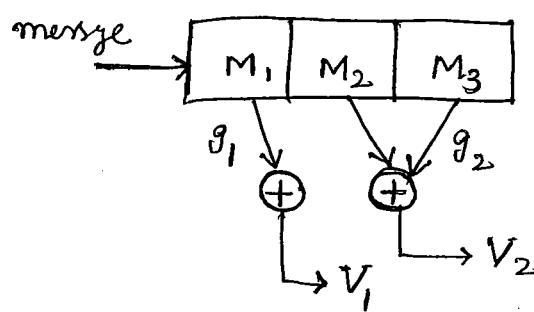
$M = 3 \rightarrow$  No. of shift registers.

Given message 1011.

$\therefore L = 4 \rightarrow$  No. of bits present in the message sequence.

$L+M = 3+4 = 7 \rightarrow$  Shifts are required to reset the registers.

$n(L+M) = 2(7) = 14 \rightarrow$  The total no. of bits in the encoder output sequence.



(let  $g_1 = 100$   
 $g_2 = 011$ )

$$V_1 = M_1$$

$$V_2 = M_2 \oplus M_3$$

Assume initially all shift registers are reset position

Now  $M = \underbrace{1011}_{\text{MSB}}$  is entered in the shift register from MSB bit

First bit of the input data stream is enter into  $M_1$ , the bit is initially present in  $M_1$  is shifted to  $M_2$  in next state ... this process continues until the last bit of the message has been entered and comes out.

| Shift | Input             | Registers |       |       | Outputs     |                        |
|-------|-------------------|-----------|-------|-------|-------------|------------------------|
|       |                   | $M_1$     | $M_2$ | $M_3$ | $V_1 = M_1$ | $V_2 = M_2 \oplus M_3$ |
| -     | -                 | 0         | 0     | 0     | 0           | 0                      |
| 1     | $1 \rightarrow 1$ | 0         | 0     | 0     | 1           | 0                      |
| 2     | $0 \rightarrow 0$ | 0         | 1     | 0     | 0           | 1                      |
| 3     | 1                 | 1         | 0     | 1     | 1           | 1                      |
| 4     | 1                 | 1         | 1     | 0     | 1           | 1                      |
| 5     | 0                 | 0         | 1     | 1     | 0           | 0                      |
| 6     | 0                 | 0         | 0     | 1     | 0           | 1                      |
| 7     | 0                 | 0         | 0     | 0     | 0           | 0                      |

After 7 shifts.

Where  
 $V_1$  - First code symbol  
 $V_2$  - Second code symbol  
in Encoded output.

The Encoder output Sequence  $x = (1001111000100)$

## Encoding methods of Convolutional Codes :

xxxxxx \* xxxx \* xxxx \* xxxx

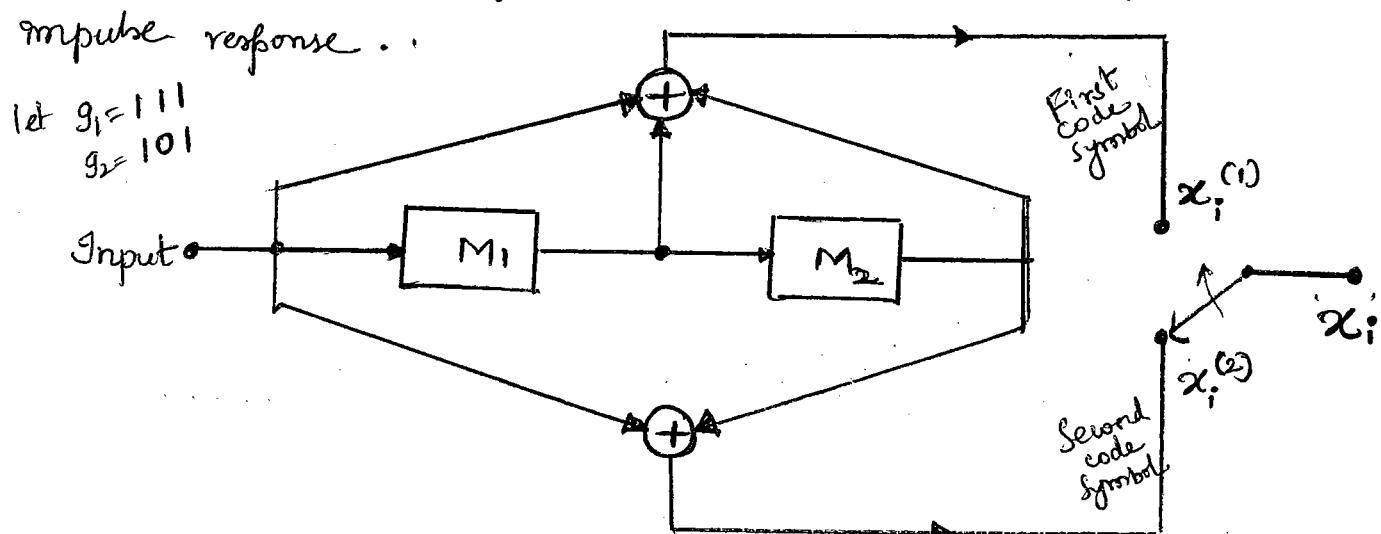
There are five methods for encoding of convolutional codes.

- ① Time domain approach (or) Impulse approach  
(or) Connection diagram
- ② Frequency domain approach (or) Transfer domain approach  
(or) Connection polynomial  
(or) Connection representation.
- ③ Tree diagram
- ④ State diagram
- ⑤ Trellis diagram.

### ① Time domain approach :

xxxx xx xxxxxxxx

The time domain behaviour of convolutional codes with a code rate of  $\frac{1}{n}$  may be defined in terms of a set of  $n$  impulse response ..



- ✓ This consists of two modulo-2 adders and the code rate is  $\frac{1}{2}$
- ✓ This encoder are non systematic codes.
- ✓ for a given encoder two impulse responses are needed to characterised its behaviour in time domain

Ex:

$$g_1 = (111)$$

up

$$g_2 = (101)$$

down → connection does not exist.

- \* Let the sequence  $[g_0^{(1)}, g_1^{(1)}, g_2^{(1)} \dots g_M^{(1)}]$  denotes the impulse response of the input top-adder output path of the encoder.

$[g_0^{(2)}, g_1^{(2)}, g_2^{(2)} \dots g_M^{(2)}]$  denotes the impulse response of the input bottom-adder output path of the encoder.

- \* The impulse response is so defined as the generated sequence of the code.

- \* The top output sequence is defined by the convolutional sum as

$$x_i^{(1)} = \sum_{l=0}^M g_l^{(1)} \cdot M_{i-l}, \quad \text{where } i = 0, 1, 2, \dots$$

- \* The bottom output sequence is defined as

$$x_i^{(2)} = \sum_{l=0}^M g_l^{(2)} \cdot M_{i-l}, \quad \text{where } i = 0, 1, 2, \dots$$

The two output sequences  $x_i^{(1)}$  and  $x_i^{(2)}$  are combined by the multiplexer to produce the encoder output sequence ( $x_i$ )

$$\therefore \{x_i\} = (x_0^{(1)} x_0^{(2)} \quad x_1^{(1)} x_1^{(2)} \quad x_2^{(1)} x_2^{(2)} \dots \dots)$$

Problem

(1)

A convolutional encoder has a single shift register with two stages (ie constraint length 3), two X-OR adders and an output ~~seg~~ multiplexer. The generated sequences of the encoder as

$$g_1 = \begin{pmatrix} 1 & 1 & 1 \\ g_0 & g_1 & g_2 \end{pmatrix} \quad \& \quad g_2 = \begin{pmatrix} 1 & 0 & 1 \\ g_0 & g_1 & g_2 \end{pmatrix}.$$

Draw the block diagram of encoder and find the output of the encoder for an input data stream of  $M_0 M_1 M_2 M_3 M_4$  by using time domain approach.

Sol

Given generated Sequences

$$g_1 = \begin{pmatrix} 1 & 1 & 1 \\ g_0 & g_1 & g_2 \end{pmatrix} \quad ; \quad g_2 = \begin{pmatrix} 1 & 0 & 1 \\ g_0 & g_1 & g_2 \end{pmatrix}$$

Constraint length  $N = M+1 = 3$

$$\boxed{M=2}$$

Given      So, no. of shift registers are '2'       $M_0, M_1$ .  
 two XOR adders.       $\boxed{M=2}$        $\boxed{n=2}$

At a time no. of bits given to the register

i.e Single shift register |  $K=1 \Rightarrow \boxed{K=1}$ .

$(n, K, M) = (2, 1, 2)$  Convolutional Codes.

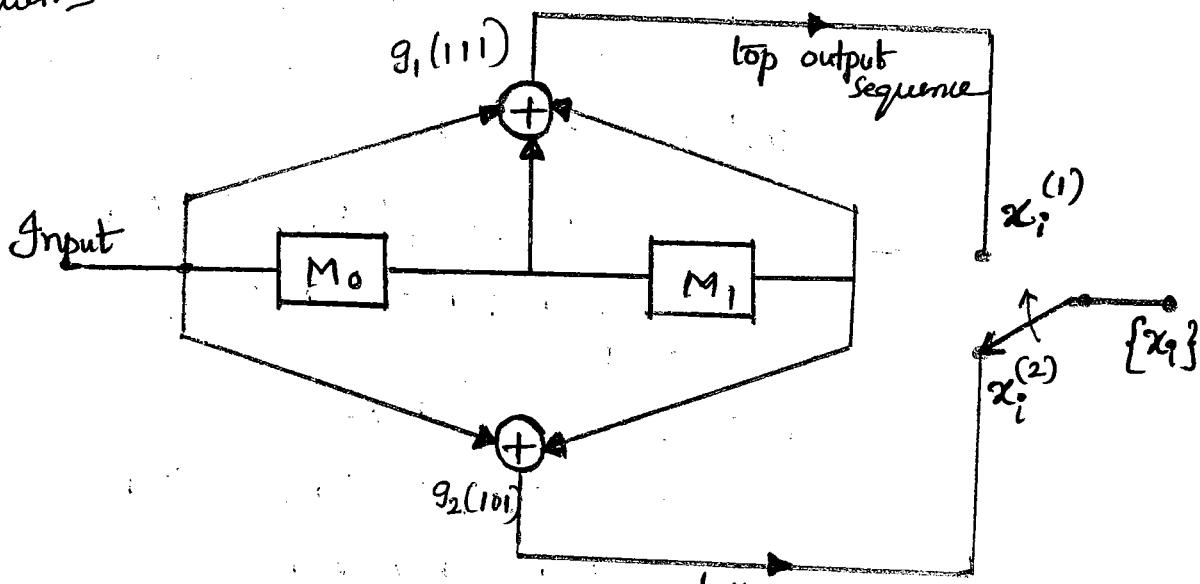
Given Input data stream    1 0 0 1 1

$\boxed{L=5}$  i.e no. of bits in message.

$$L+M = 5+2 = 7 \Rightarrow \boxed{L+M=7}$$

Total no. of bits  
in encoded O/P sequence  
 $n(L+M) = 2(7) = 14 \Rightarrow \boxed{n(L+M)=14}$ .

Thus.



\* The top output sequence can be generated by using  $\sum_{l=0}^{L-1} g_1^{(1)} M_{i-l}$ , when  $i = 0, 1, 2, 3, 4, 5, 6$ .

\* The bottom output sequence can be generated by using  $\sum_{l=0}^{L-1} g_2^{(2)} M_{i-l}$ ,  $i = 0, 1, 2, 3, 4, 5, 6$ .

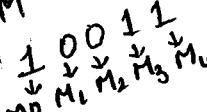
$$x_i^{(2)} = \sum_{l=0}^{L-1} g_2^{(2)} M_{i-l}, \quad i = 0, 1, 2, 3, 4, 5, 6.$$

✓ The top output sequence

$$x_i^{(1)} = \sum_{l=0}^2 g_l^{(1)} M_{i-l}, \quad i=0, 1, 2, 3, 4, 5, 6.$$

For  $i=0$ ,  $x_0^{(1)} = \sum_{l=0}^2 g_l^{(1)} \cdot M_{-l}$

$$\begin{aligned} &= g_0^{(1)} M_0 + g_1^{(1)} M_{-1} + g_2^{(1)} M_{-2} \\ &= 1(1) + 1(0) + 1(0) \\ &= 1 \quad \Rightarrow \boxed{x_0^{(1)} = 1} \end{aligned}$$

$M$   


$$g_1 = \frac{1}{g_0} \frac{1}{g_1} \frac{1}{g_2}$$

For  $i=1$ ,  $x_1^{(1)} = \sum_{l=0}^2 g_l^{(1)} M_{1-l}$

$$\begin{aligned} &= g_0^{(1)} M_1 + g_1^{(1)} M_0 + g_2^{(1)} M_{-1} \\ &= 1(0) + 1(1) + 1(0) \\ &= 1 \quad \therefore \boxed{x_1^{(1)} = 1} \end{aligned}$$

For  $i=2$ ,  $x_2^{(1)} = \sum_{l=0}^2 g_l^{(1)} \cdot M_{2-l}$

$$\begin{aligned} &= g_0^{(1)} M_2 + g_1^{(1)} M_1 + g_2^{(1)} M_0 \\ &= 1(0) + 1(0) + 1(1) \\ &= 1 \quad \therefore \boxed{x_2^{(1)} = 1} \end{aligned}$$

For  $i=3$ ,  $x_3^{(1)} = \sum_{l=0}^2 g_l^{(1)} \cdot M_{3-l}$

$$\begin{aligned} &= g_0^{(1)} M_3 + g_1^{(1)} M_2 + g_2^{(1)} M_1 \\ &= 1(1) + 1(0) + 1(0) \\ &= 1 \quad \therefore \boxed{x_3^{(1)} = 1} \end{aligned}$$

For  $i=4$ ,  $x_4^{(1)} = \sum_{l=0}^2 g_l^{(1)} \cdot M_{4-l}$

$$\begin{aligned} &= g_0^{(1)} M_4 + g_1^{(1)} M_3 + g_2^{(1)} M_2 \\ &= 1(1) + 1(1) + 1(0) \\ &= 1 \oplus 1 \\ &= 0 \quad \therefore \boxed{x_4^{(1)} = 0} \end{aligned}$$

For  $i=5$ ,  $x_5^{(1)} = \sum_{l=0}^2 g_l^{(1)} M_{5-l}$

$$\begin{aligned} &= g_0^{(1)} M_5 + g_1^{(1)} M_4 + g_2^{(1)} M_3 \\ &= 1(0) + 1(1) + 1(1) \\ &= 1 \oplus 1 \\ &= 0 \quad \therefore \boxed{x_5^{(1)} = 0} \end{aligned}$$

$$\begin{aligned}
 \text{For } i=6, \quad x_6^{(1)} &= \sum_{l=0}^2 g_l^{(1)} M_{6-l} \\
 &= g_0^{(1)} M_6 + g_1^{(1)} M_5 + g_2^{(1)} M_4 \\
 &= 1(0) + 1(0) + 1(1) \\
 &= 1 \quad \therefore \boxed{x_6^{(1)} = 1}
 \end{aligned}$$

$\therefore$  The top output sequence

$$\boxed{x_i^{(1)} = (1111001)}$$

$\checkmark$  The bottom output sequence

$$x_i^{(2)} = \sum_{l=0}^2 g_l^{(2)} M_{i-l}, \quad i=0,1,2,3,4,5,6.$$

$$\begin{aligned}
 \text{For } i=0, \quad x_0^{(2)} &= \sum_{l=0}^2 g_l^{(2)} M_{-l} \\
 &= g_0^{(2)} M_0 + g_1^{(2)} M_{-1} + g_2^{(2)} M_{-2} \\
 &= 1(1) 1(1) + 0(0) + 1(0) \\
 &= 1 \quad \therefore \boxed{x_0^{(2)} = 1}.
 \end{aligned}$$

$$\begin{array}{ccccccc}
 & 1 & 0 & 0 & 1 & 1 \\
 & \downarrow & \downarrow & \downarrow & \downarrow & \downarrow \\
 M_0 & M_1 & M_2 & M_3 & M_4 \\
 g_0 & g_1 & g_2
 \end{array}$$

$$\begin{aligned}
 \text{For } i=1, \quad x_1^{(2)} &= \sum_{l=0}^2 g_l^{(2)} M_{1-l} \\
 &= g_0^{(2)} M_1 + g_1^{(2)} M_0 + g_2^{(2)} M_{-1} \\
 &= 1(0) + 0(1) + 1(0) \\
 &= 0 \quad \therefore \boxed{x_1^{(2)} = 0}
 \end{aligned}$$

$$\begin{aligned}
 \text{For } i=2, \quad x_2^{(2)} &= \sum_{l=0}^2 g_l^{(2)} M_{2-l} \\
 &= g_0^{(2)} M_2 + g_1^{(2)} M_1 + g_2^{(2)} M_0 \\
 &= 1(0) + 0(0) + 1(1) \\
 &= 1 \quad \therefore \boxed{x_2^{(2)} = 1}.
 \end{aligned}$$

$$\begin{aligned}
 \text{For } i=3, \quad x_3^{(2)} &= \sum_{l=0}^2 g_l^{(2)} M_{3-l} \\
 &= g_0^{(2)} M_3 + g_1^{(2)} M_2 + g_2^{(2)} M_1 \\
 &= 1(1) + 0(0) + 1(0) \\
 &= 1 \quad \therefore \boxed{x_3^{(2)} = 1}.
 \end{aligned}$$

$$\begin{aligned}
 \text{For } i=4, \quad x_4^{(2)} &= \sum_{l=0}^2 g_l^{(2)} M_{4-l} \\
 &= g_0^{(2)} M_4 + g_1^{(2)} M_3 + g_2^{(2)} M_2 \\
 &= 1(1) + 0(1) + 1(0) \\
 &= 1 \quad \therefore \boxed{x_4^{(2)} = 1}
 \end{aligned}$$

$$\begin{aligned}
 \text{for } i=5, \quad x_5^{(2)} &= \sum_{l=0}^2 g_l^{(2)} M_{5-l} \\
 &= g_0^{(2)} M_5 + g_1^{(2)} M_4 + g_2^{(2)} M_3 \\
 &= 1(0) + 0(1) + 1(1) \\
 &= 1 \quad \therefore \boxed{x_5^{(2)} = 1}
 \end{aligned}$$

$$\begin{aligned}
 \text{for } i=6, \quad x_6^{(2)} &= \sum_{l=0}^2 g_l^{(2)} M_{6-l} \\
 &= g_0^{(2)} M_6 + g_1^{(2)} M_5 + g_2^{(2)} M_4 \\
 &= 1(0) + 0(0) + 1(1) \\
 &= 1 \quad \therefore \boxed{x_6^{(2)} = 1}
 \end{aligned}$$

$\therefore$  The bottom output sequence

$$\boxed{x_i^{(2)} = (1 \ 0 \ 1 \ 1 \ 1 \ 1 \ 1)}$$

$\therefore$  The Encoded output sequence is given by.

$$\{x_i\} = (x_0^{(1)} \ x_0^{(2)} \ x_1^{(1)} \ x_1^{(2)} \ \dots \ x_6^{(1)} \ x_6^{(2)})$$

$$x_i^{(1)} = (1 \ 1 \ 1 \ 1 \ 0 \ 0 \ 1)$$

$$x_i^{(2)} = \begin{matrix} \downarrow & \downarrow & & \downarrow \\ (1 & 0 & 1 & 1 & 1 & 1 & 1) \end{matrix}$$

$$x_i = (1 \ 1 \ 1 \ 0 \ 1 \ 1 \ 1 \ 1 \ 0 \ 1 \ 0 \ 1 \ 1 \ 1)$$

$\therefore$  The Encoded output sequence

$$\boxed{x_i = (1 \ 1 \ 1 \ 0 \ 1 \ 1 \ 1 \ 1 \ 0 \ 1 \ 0 \ 1 \ 1 \ 1)}$$

## (2) Frequency domain approach :

- \* Transfer domain response is represented in the form of polynomial.
- \* The input top adder-output path of the encoder

$$g^{(1)}(D) = g_0^{(1)} + g_1^{(1)}D + g_2^{(1)}D^2 + \dots + g_M^{(1)}D^M.$$

- \* The input-bottom adder-output path of the encoder

$$g^{(2)}(D) = g_0^{(2)} + g_1^{(2)}D + g_2^{(2)}D^2 + \dots + g_M^{(2)}D^M.$$

where  $g_0^{(1)}, g_1^{(1)}, \dots, g_M^{(1)}$  and  $g_0^{(2)}, g_1^{(2)}, \dots, g_M^{(2)}$  are the elements of the transfer domain response of the path.

The message polynomial is given as

$$M(D) = M_0 + M_1D + M_2D^2 + \dots + M_{L-1}D^{L-1}.$$

where L - length of the message sequence.

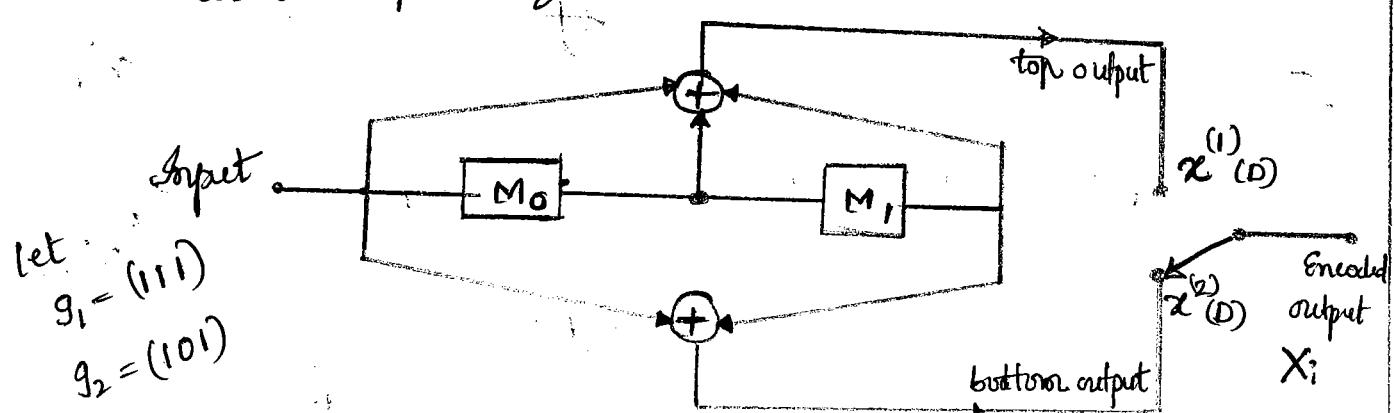
∴ The top-output polynomial is given as

$$X^{(1)}(D) = g^{(1)}(D) \cdot M(D)$$

The bottom output polynomial is given as

$$X^{(2)}(D) = g^{(2)}(D) \cdot M(D)$$

∴ The Encoded output Sequence  $x_i = [x^{(1)}(D), x^{(2)}(D), \dots]$



(2) A convolutional encoder has a single shift register with two stages (ie Constraint length 3), two XOR adders and an output multiplexer. The generated polynomials of the encoder are as follows

$$g_{(1)}^{(1)}(D) = 1 + D + D^2 \quad \text{and} \quad g_{(2)}^{(2)}(D) = 1 + D^2.$$

Draw the block diagram of encoder and find the output of the encoder for an input data stream of (10011) by using frequency / transfer domain approach.

Sol

Given data  $g_{(1)}^{(1)}(D) = 1 + D + D^2$

$$g_{(2)}^{(2)}(D) = 1 + D^2$$

Input data stream or message as  $10011$   
 $M_0 M_1 M_2 M_3 M_4$

Given single shift register  $k=1$

Constraint length  $N=M+1=3 \Rightarrow M=2$

No. of shift register 2

No. of XOR adders 2;  $n=2$

$$L=5, L+M=5+2=7 \Rightarrow L+M=7$$

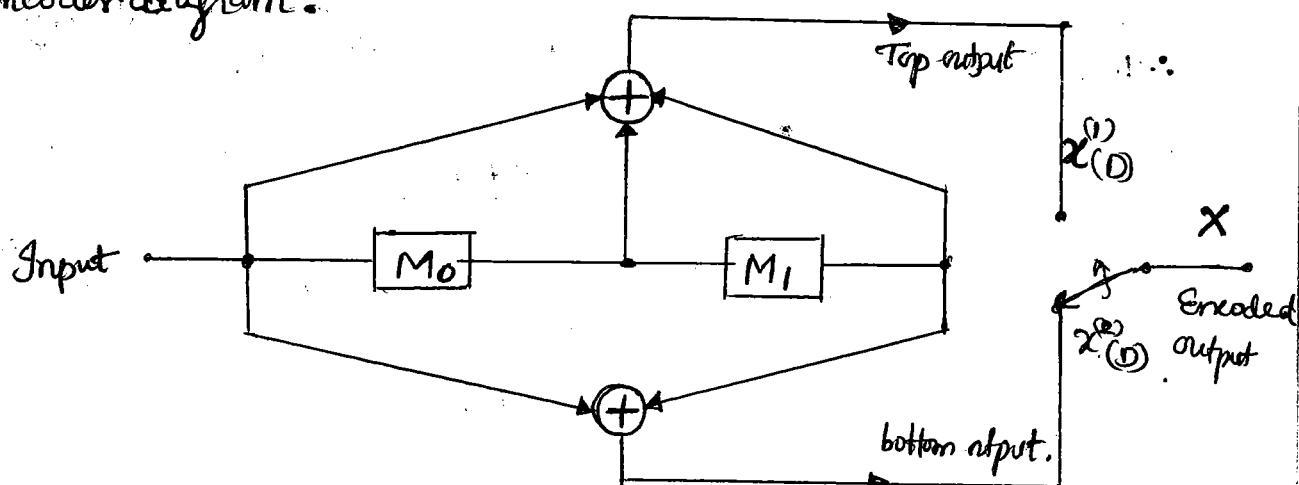
$$n(L+M)=2(7)=14 \Rightarrow n(L+M)=14$$

Message polynomial as

$$M(D) = 1 \cdot D^0 + 0 \cdot D^1 + 0 \cdot D^2 + 1 \cdot D^3 + 1 \cdot D^4$$

$$\therefore M(D) = 1 + D^3 + D^4.$$

Encoder diagram:



- The top output polynomial is given as

$$\begin{aligned}
 x^{(1)}(D) &= g^{(1)}(D) \cdot M(D) \\
 &= (1+D+D^2)(1+D^3+D^4) \\
 &= 1+D^3+D^4+D+D^4+D^5+D^2+D^5+D^6 \\
 &= 1+D+D^2+D^3+D^6.
 \end{aligned}$$

$$\therefore \boxed{x^{(1)}(D) = (1 \ 1 \ 1 \ 1 \ 0 \ 0 \ 1)}$$

( XOR  
operator  
1+1=0  
0+0=0  
0+1=1  
1+0=1 )

- The bottom output polynomial is given as

$$\begin{aligned}
 x^{(2)}(D) &= g^{(2)}(D) \cdot M(D) \\
 &= (1+D^2)(1+D^3+D^4) \\
 &= 1+D^3+D^4+D^2+D^5+D^6 \\
 &= 1+D^2+D^3+D^4+D^5+D^6
 \end{aligned}$$

$$\therefore \boxed{x^{(2)}(D) = (1 \ 0 \ 1 \ 1 \ 1 \ 1 \ 1)}$$

- ∴ The Encoded output polynomial in bits.

$$\begin{aligned}
 x^{(1)}(D) &= (1 \ 1 \ 1 \ 1 \ 0 \ 0 \ 1) \\
 x^{(2)}(D) &= (1 \ 0 \ 1 \ 1 \ 1 \ 1 \ 1)
 \end{aligned}$$

$$\therefore \boxed{x_i = (1 \ 1 \ 1 \ 0 \ 1 \ 1 \ 1 \ 1 \ 0 \ 1 \ 0 \ 1 \ 1 \ 1)}.$$

HW

(3)

A convolutional encoder has a single shift register with two stages, 3 modulo-2 adders & an output multiplexer.

The generated sequence of the encoder as follows.

$$g_1 = (101), g_2 = (110) \text{ & } g_3 = (111)$$

Draw the block diagram of encoder & the output of the encoder using time and frequency domain approach with message data (10011).

Sol Given data Input message (10011).

$L = 5 \rightarrow$  No. of bits in the input data

$K = 1 \rightarrow$  Single shift register.

$M = 2 \rightarrow$  No. of shift registers.

$L+M = 5+2 = 7 \rightarrow$  total no. of shift required to reset the register.

$n = 3 \rightarrow$  no. of modulo-2 adders.

$n(L+M) = 3(7) = 21 \rightarrow$  Total no. of bits in the encoder output sequence.  
Transfer / Frequency domain approach:

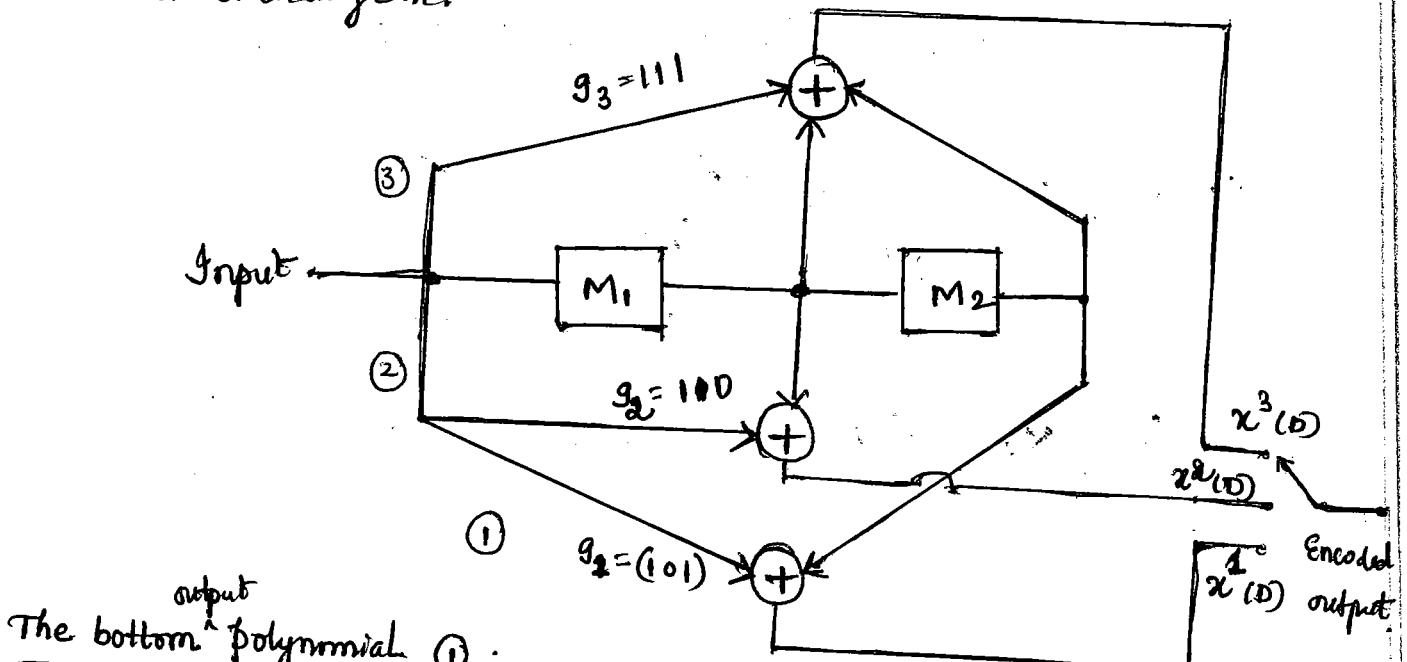
Message polynomial  $M(D) = 1 + D^3 + D^4$

Generated polynomial  $g_1^{(1)}(D) = (101) = 1 + D^2$

$$g_2^{(2)}(D) = (110) = 1 + D$$

$$g_3^{(3)}(D) = (111) = 1 + D + D^2$$

Encoder block diagram:



The bottom polynomial ① :

$$\begin{aligned} x'(D) &= g_1^{(1)}(D) \cdot M(D) \\ &= (1 + D^2)(1 + D^3 + D^4) \\ &= 1 + D^3 + D^4 + D^2 + D^5 + D^6 \\ &= 1 + D^2 + D^3 + D^4 + D^5 + D^6 \end{aligned}$$

$$x'(D) = (1 \ 0 \ 1 \ 1 \ 1 \ 1 \ 1)$$

The bottom <sup>output</sup> polynomial ② :

$$\begin{aligned}
 x^{(2)}(D) &= g^{(2)}(D) \cdot M(D) \\
 &= (1+D)(1+D^3+D^4) \\
 &= 1+D^3+D^4+D+D^4+D^5 \\
 &= 1+D+D^3+D^5
 \end{aligned}$$

$$x^{(2)}(D) = 1 \ 1 \ 0 \ 1 \ 0, 1 \ 0$$

The top output polynomial ③ :

$$\begin{aligned}
 x^{(3)}(D) &= g^{(3)}(D) \cdot M(D) \\
 &= (1+D+D^2)(1+D^3+D^4) \\
 &= 1+D^3+D^4+D+D^4+D^5+D^2+D^5+D^6 \\
 &= 1+D+D^2+D^3+D^6
 \end{aligned}$$

$$x^{(3)}(D) = (1 \ 1 \ 1 \ 1 \ 0 \ 0 \ 1)$$

∴ The Encoder output polynomial is given by  $\{x\}$ .

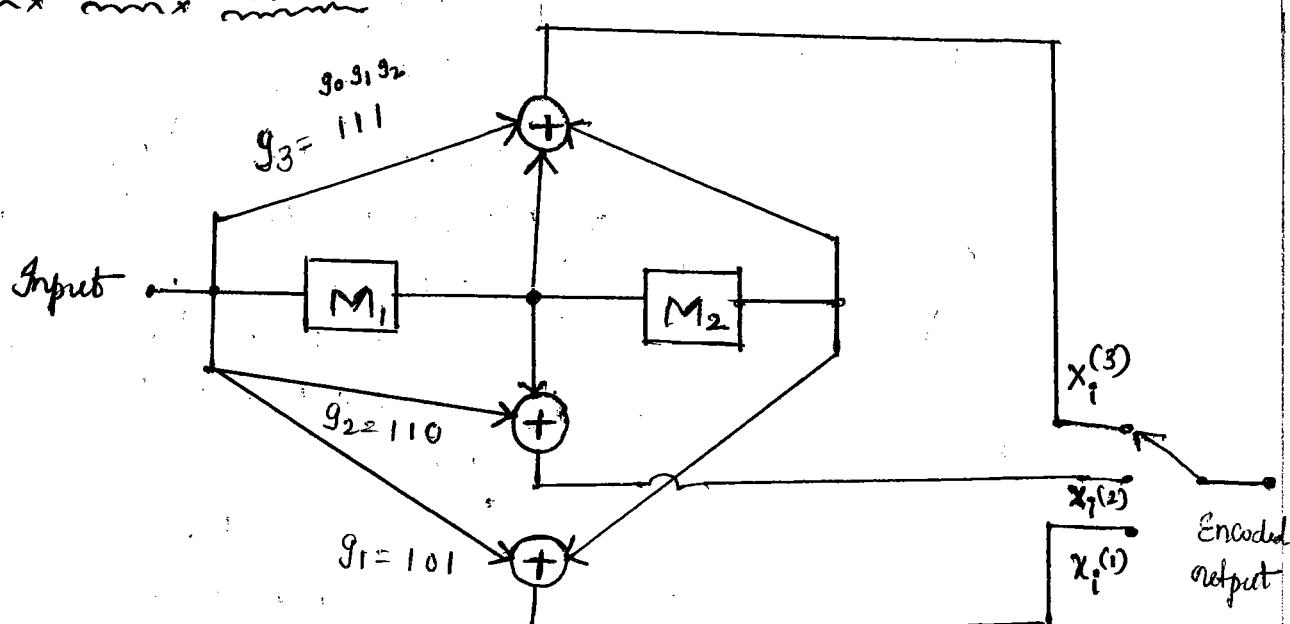
$$x^1 = (1 \ 0 \ 1 \ 1 \ 1 \ 1)$$

$$x^2 = (1 \ 1 \ 0 \ 1 \ 0 \ 1 \ 0)$$

$$x^3 = (1 \ 1 \ 1 \ 1 \ 0 \ 0 \ 1)$$

$$\therefore \{x\} = (1 \ 1 \ 1 \ 0 \ 1 \ 1 \ 1 \ 0 \ 1 \ 1 \ 1 \ 1 \ 0 \ 0 \ 1 \ 1 \ 0 \ 1 \ 0 \ 1)$$

Time domain approach:



✓ Message bits  $(1 \ 0 \ 0 \ 1 \ 1)$

$M_0 \ M_1 \ M_2 \ M_3 \ M_4$

~ Given generated sequences

$$g_1 = (1 \ 0 \ 1) \quad g_2 = (1 \ 1 \ 0) \quad , \quad g_3 = (1 \ 1 \ 1)$$

$g_0 \ g_1 \ g_2$        $g_0 \ g_1 \ g_2$        $g_0 \ g_1 \ g_2$

✓ The bottom output sequence ① can be generated by using

$$x_i^{(1)} = \sum_{l=0}^M g_l^{(1)} M_{i-l} ; \quad l = 0, 1, 2, 3, 4, 5, 6.$$

$$\text{For } i=0, \quad x_0^{(1)} = \sum_{l=0}^2 g_l^{(1)} M_{i-l}$$

$$= g_0^{(1)} M_0 + g_1^{(1)} M_1 + g_2^{(1)} M_2$$

$$= 1(1) + 0(0) + 1(0)$$

$$= 1$$

$$\boxed{x_0^{(1)} = 1}$$

$$M = \begin{pmatrix} M_0 & M_1 & M_2 & M_3 & M_4 \\ 1 & 0 & 0 & 1 & 1 \end{pmatrix}$$

$$g_1 = \begin{pmatrix} 1 & 0 & 1 \\ g_0 & g_1 & g_2 \end{pmatrix}$$

$$\text{for } i=1, \quad x_1^{(1)} = \sum_{l=0}^2 g_l^{(1)} M_{i-l}$$

$$= g_0^{(1)} M_1 + g_1^{(1)} M_0 + g_2^{(1)} M_{-1}$$

$$= 1(0) + 0(1) + 1(0) = 0$$

$$\boxed{x_1^{(1)} = 0}$$

$$\text{for } i=2, \quad x_2^{(1)} = \sum_{l=0}^2 g_l^{(1)} M_{i-l}$$

$$= g_0^{(1)} M_2 + g_1^{(1)} M_1 + g_2^{(1)} M_0$$

$$= 1(0) + 0(0) + 1(1) = 1$$

$$\boxed{x_2^{(1)} = 1}$$

$$\text{for } i=3, \quad x_3^{(1)} = \sum_{l=0}^2 g_l^{(1)} M_{i-l}$$

$$= g_0^{(1)} M_3 + g_1^{(1)} M_2 + g_2^{(1)} M_1$$

$$= 1(1) + 0(0) + 1(0) = 1$$

$$\boxed{x_3^{(1)} = 1}$$

$$\text{for } i=4, \quad x_4^{(1)} = \sum_{l=0}^2 g_l^{(1)} M_{i-l}$$

$$= g_0^{(1)} M_4 + g_1^{(1)} M_3 + g_2^{(1)} M_2$$

$$= 1(1) + 0(1) + 1(0) = 1$$

$$\boxed{x_4^{(1)} = 1}$$

$$\text{for } i=5, \quad x_5^{(1)} = \sum_{l=0}^2 g_l^{(1)} M_{i-l}$$

$$= g_0^{(1)} M_5 + g_1^{(1)} M_4 + g_2^{(1)} M_3$$

$$= 1(0) + 0(1) + 1(1) = 1$$

$$\boxed{x_5^{(1)} = 1}$$

$$\text{for } i=6, \quad x_6^{(1)} = \sum_{l=0}^2 g_l^{(1)} M_{i-l}$$

$$= g_0^{(1)} M_6 + g_1^{(1)} M_5 + g_2^{(1)} M_4$$

$$= 1(0) + 0(0) + 1(1) = 1$$

$$\boxed{x_6^{(1)} = 1}$$

The bottom output sequence ② can be generated by using

$$x_i^{(2)} = \sum_{l=0}^2 g_l^{(2)} M_{i-l}, \quad i=0, 1, 2, 3, 4, 5, 6.$$

For  $i=0$      $x_0^{(2)} = \sum_{l=0}^2 g_l^{(2)} M_{i-l}$   
 $= g_0^{(2)} M_0 + g_1^{(2)} M_{-1} + g_2^{(2)} M_{-2}$   
 $= 1(1) + 1(0) + 0(0) = 1 \quad \therefore x_0^{(2)} = 1$

M =  $\begin{matrix} m_0 & m_1 & m_2 & m_3 & m_4 \\ 1 & 0 & 0 & 1 & 1 \end{matrix}$   
 $g_2 = \begin{matrix} g_0 & g_1 & 0 \\ 1 & 1 & 0 \end{matrix}$

For  $i=1$      $x_1^{(2)} = \sum_{l=0}^2 g_l^{(2)} M_{i-l}$   
 $= g_0^{(2)} M_1 + g_1^{(2)} M_0 + g_2^{(2)} M_{-1}$   
 $= 1(0) + 1(1) + 0(0) = 1 \quad \therefore x_1^{(2)} = 1$

For  $i=2$ ,     $x_2^{(2)} = \sum_{l=0}^2 g_l^{(2)} M_{i-l}$   
 $= g_0^{(2)} M_2 + g_1^{(2)} M_1 + g_2^{(2)} M_0$   
 $= 1(0) + 1(0) + 0(1) = 0 \quad \therefore x_2^{(2)} = 0$

For  $i=3$ ,     $x_3^{(2)} = \sum_{l=0}^2 g_l^{(2)} M_{i-l}$   
 $= g_0^{(2)} M_3 + g_1^{(2)} M_2 + g_2^{(2)} M_1$   
 $= 1(1) + 1(0) + 0(0) = 1 \quad \therefore x_3^{(2)} = 1$

For  $i=4$ ,     $x_4^{(2)} = \sum_{l=0}^2 g_l^{(2)} M_{i-l}$   
 $= g_0^{(2)} M_4 + g_1^{(2)} M_3 + g_2^{(2)} M_2$   
 $= 1(1) + 1(1) + 0(0) = 0 \quad \therefore x_4^{(2)} = 0$

For  $i=5$ ,     $x_5^{(2)} = \sum_{l=0}^2 g_l^{(2)} M_{i-l}$   
 $= g_0^{(2)} M_5 + g_1^{(2)} M_4 + g_2^{(2)} M_3$   
 $= 1(0) + 1(1) + 0(1) = 1 \quad \therefore x_5^{(2)} = 1$

For  $i=6$ ,     $x_6^{(2)} = \sum_{l=0}^2 g_l^{(2)} M_{i-l}$   
 $= g_0^{(2)} M_6 + g_1^{(2)} M_5 + g_2^{(2)} M_4$   
 $= 1(0) + 1(0) + 0(1) = 0 \quad \therefore x_6^{(2)} = 0$

∴ The bottom output sequences ① & ②

$$x_i^{(1)} = (1 \ 0 \ 1 \ 1 \ 1 \ 1)$$

$$x_i^{(2)} = (1 \ 1 \ 0 \ 1 \ 0 \ 1 \ 0)$$

∴ The top output sequence ③ can be generated by using

$$x_i^{(3)} = \sum_{l=0}^M g_l^{(3)} M_{i-l}, \quad i=0, 1, 2, 3, 4, 5, 6,$$

$$M = \begin{pmatrix} M_0 & M_1 & M_2 & M_3 & M_4 \\ 1 & 0 & 0 & 1 & 1 \end{pmatrix}$$

$$g_3 = \begin{pmatrix} 1 & 1 \\ g_0 & g_1 & g_2 \end{pmatrix}$$

For  $i=0$ ,  $x_0^{(3)} = \sum_{l=0}^2 g_l^{(3)} M_{-l}$

$$\begin{aligned} &= g_0^{(3)} M_0 + g_1^{(3)} M_{-1} + g_2^{(3)} M_{-2} \\ &= 1(1) + 1(0) + 1(0) = 1 \end{aligned} \quad \therefore \boxed{x_0^{(3)} = 1}$$

For  $i=1$ ,  $x_1^{(3)} = \sum_{l=0}^2 g_l^{(3)} M_{1-l}$

$$\begin{aligned} &= g_0^{(3)} M_1 + g_1^{(3)} M_0 + g_2^{(3)} M_{-1} \\ &= 1(0) + 1(1) + 1(0) = 1 \end{aligned} \quad \therefore \boxed{x_1^{(3)} = 1}$$

For  $i=2$ ,  $x_2^{(3)} = \sum_{l=0}^2 g_l^{(3)} M_{2-l}$

$$\begin{aligned} &= g_0^{(3)} M_2 + g_1^{(3)} M_1 + g_2^{(3)} M_0 \\ &= 1(0) + 1(0) + 1(1) = 1 \end{aligned} \quad \therefore \boxed{x_2^{(3)} = 1}$$

For  $i=3$ ,  $x_3^{(3)} = \sum_{l=0}^2 g_l^{(3)} M_{3-l}$

$$\begin{aligned} &= g_0^{(3)} M_3 + g_1^{(3)} M_2 + g_2^{(3)} M_1 \\ &= 1(1) + 1(0) + 1(0) = 1 \end{aligned} \quad \therefore \boxed{x_3^{(3)} = 1}$$

For  $i=4$ ,  $x_4^{(3)} = \sum_{l=0}^2 g_l^{(3)} M_{4-l}$

$$\begin{aligned} &= g_0^{(3)} M_4 + g_1^{(3)} M_3 + g_2^{(3)} M_2 \\ &= 1(1) + 1(1) + 1(0) = 0 \end{aligned} \quad \therefore \boxed{x_4^{(3)} = 0}$$

For  $i=5$ ,  $x_5^{(3)} = \sum_{l=0}^2 g_l^{(3)} M_{5-l}$

$$\begin{aligned} &= g_0^{(3)} M_5 + g_1^{(3)} M_4 + g_2^{(3)} M_3 \\ &= 1(0) + 1(1) + 1(1) = 0 \end{aligned} \quad \therefore \boxed{x_5^{(3)} = 0}$$

For  $i=6$ ,  $x_6^{(3)} = \sum_{l=0}^2 g_l^{(3)} M_{6-l}$

$$\begin{aligned} &= g_0^{(3)} M_6 + g_1^{(3)} M_5 + g_2^{(3)} M_4 \\ &= 1(0) + 1(0) + 1(1) = 1 \end{aligned} \quad \therefore \boxed{x_6^{(3)} = 1}$$

$\therefore$  The top output sequence (3) is

$$\boxed{x_i^{(3)} = (1 \ 1 \ 1 \ 1 \ 0 \ 0 \ 1)}$$

$$x_i^{(1)} = (1 \ 0 \ 1 \ 1 \ 1 \ 1 \ 1)$$

$$x_i^{(2)} = (1 \ 1 \ 0 \ 1 \ 0 \ 1 \ 0)$$

$$x_i^{(3)} = (1 \ 1 \ 1 \ 1 \ 0 \ 0 \ 1)$$

$\therefore$  The encoder output sequence can be generated by using

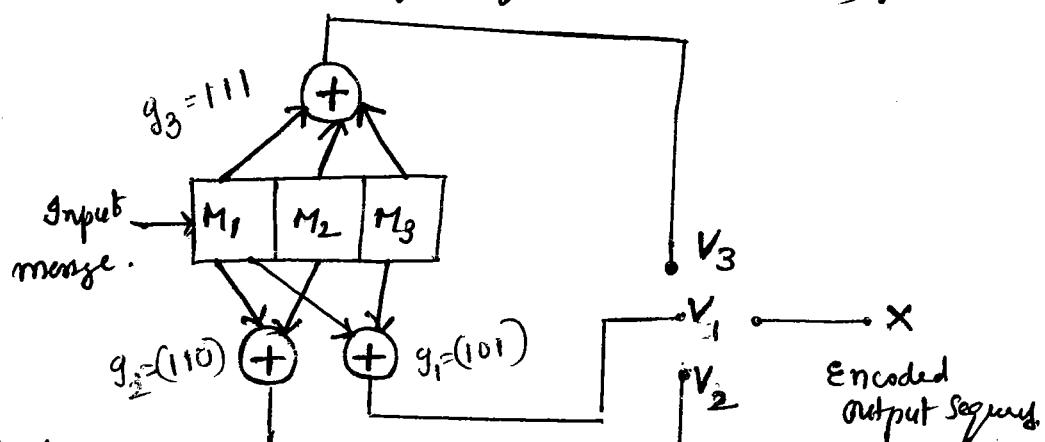
$$X = \{x_0^{(1)} x_0^{(2)} x_0^{(3)}, x_1^{(1)} x_1^{(2)} x_1^{(3)}, \dots, x_6^{(1)} x_6^{(2)} x_6^{(3)}\}$$

$$\therefore \boxed{X = (1 \ 1 \ 1 \ 0 \ 1 \ 1 \ 1 \ 0 \ 1 \ 1 \ 1 \ 1 \ 0 \ 0 \ 1 \ 1 \ 0 \ 1 \ 1 \ 0 \ 1)}$$

## II Method:

$$g_1 = (101), g_2 = (110), g_3 = (111).$$

Merge Sequence (10011).



$$V_1 = M_1 \oplus M_3$$

$$V_2 = M_2 \oplus M_3$$

$$V_3 = M_1 \oplus M_2 \oplus M_3.$$

| shift<br>no | input<br>m | shift registers<br>M <sub>1</sub> M <sub>2</sub> M <sub>3</sub> | Output sequences                                 |                                                  |                                                                   |
|-------------|------------|-----------------------------------------------------------------|--------------------------------------------------|--------------------------------------------------|-------------------------------------------------------------------|
|             |            |                                                                 | V <sub>1</sub> = M <sub>1</sub> ⊕ M <sub>3</sub> | V <sub>2</sub> = M <sub>2</sub> ⊕ M <sub>3</sub> | V <sub>3</sub> = M <sub>1</sub> ⊕ M <sub>2</sub> ⊕ M <sub>3</sub> |
| -           | -          | 0 0 0                                                           | 0                                                | 0                                                | 0                                                                 |
| 1           | 1 →        | 1 0 0                                                           | 1                                                | 1                                                | 1                                                                 |
| 2           | 0          | 0 1 0                                                           | 0                                                | 1                                                | 1                                                                 |
| 3           | 0          | 0 0 1                                                           | 1                                                | 0                                                | 1                                                                 |
| 4           | 1          | 1 0 0                                                           | 1                                                | 1                                                | 1                                                                 |
| 5           | 1          | 1 1 0                                                           | 1                                                | 0                                                | 0                                                                 |
| 6           | 0          | 0 1 1                                                           | 1                                                | 1                                                | 0                                                                 |
| 7           | 0          | 0 0 1                                                           | 1                                                | 0                                                | 1                                                                 |

$$v_1 = (1011111)$$

$$v_2 = (1101010)$$

$$v_3 = (1111001)$$

The Encoded output

$$X = (11101110111100110101)$$

HW:

- ④ Consider the code rate is  $\frac{1}{2}$ , constraint length '2'.

Find encoded output sequence for the given input data

(10111) &  $g_1 = (11), g_2 = (00)$ , using time

& transfer domain approach methods.  $x_1 = (111001)$

$$\therefore X = (101010000010)$$

$$x_2 = (000000)$$

- 5 A  $(2, 1, 3)$  Convolutional Code is described by  
 $g_1 = (1 \ 1 \ 1) \ \& \ g_2 = (1 \ 0 \ 1)$
- Draw the Encoder diagram for this code.
  - Find the encoded output sequence for an input message sequence  $1 \ 1 \ 0 \ 1 \ 1$ .
  - Draw the Code tree diagram for this code and find the encoded output sequence for the given message sequence.
  - Draw the State diagram & find encoded output sequence for the given message sequence.
  - Draw the Trellis diagram & find encoder output sequence for the given message sequence.

Sol.

Given  $(n, k, M)$  Convolutional Code  $\rightarrow (2, 1, 3)$ .

$\therefore n = 2 \rightarrow$  no. of X-OR adders.

$k = 1 \rightarrow$  At a time no. of bits given to the register.

$M = 3 \rightarrow$  no. of shift registers.

Given message sequence  $(1 \ 1 \ 0 \ 1 \ 1)$ .

$\therefore L = 5 \rightarrow$  no. of bits present in message sequence.

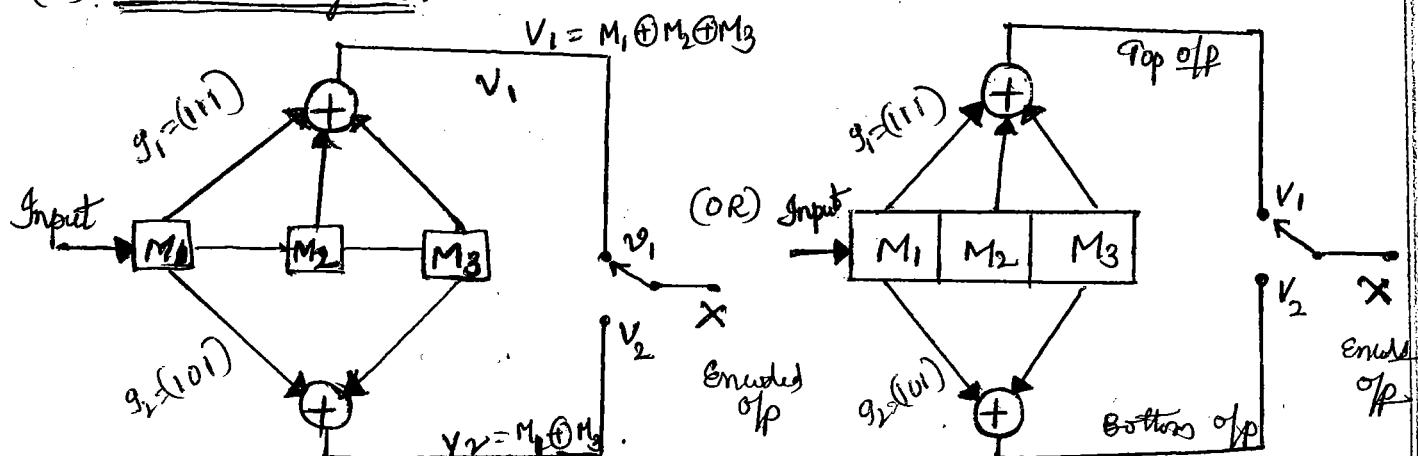
$L+M = 5+3 = 8 \rightarrow$  No. of shift required to reset the registers.

$n(L+M) = 2(8) = 16 \rightarrow$  Total no. of bits in the encoded output sequence.

Given

$$g_1 = (1 \ 1 \ 1) \ \& \ g_2 = (1 \ 0 \ 1)$$

(a). Encoder diagram:



(b) Encoded Output Sequence:

Given message '11011'.

| Shift | Input | Registers      |                |                | top & bottom output sequences                                     |                                                  |
|-------|-------|----------------|----------------|----------------|-------------------------------------------------------------------|--------------------------------------------------|
|       |       | M <sub>1</sub> | M <sub>2</sub> | M <sub>3</sub> | V <sub>1</sub> = M <sub>1</sub> ⊕ M <sub>2</sub> + M <sub>3</sub> | V <sub>2</sub> = M <sub>1</sub> ⊕ M <sub>3</sub> |
| -     | -     | 0              | 0              | 0              | 0                                                                 | 0                                                |
| 1     | 1     | 1              | 0              | 0              | 1                                                                 | 1                                                |
| 2     | 1     | 1              | 1              | 0              | 0                                                                 | 1                                                |
| 3     | 0     | 0              | 1              | 1              | 0                                                                 | 1                                                |
| 4     | 1     | 1              | 0              | 1              | 0                                                                 | 0                                                |
| 5     | 1     | 1              | 1              | 0              | 0                                                                 | 1                                                |
| 6     | 0     | 0              | 1              | 1              | 0                                                                 | 1                                                |
| 7     | 0     | 0              | 0              | 1              | 1                                                                 | 1                                                |
| 8     | 0     | 0              | 0              | 0              | 0                                                                 | 0                                                |

The top output sequence  $v_1 = (1 \underset{\uparrow}{0} \underset{\downarrow}{0} \underset{\uparrow}{0} \underset{\downarrow}{0} \underset{\uparrow}{0} \underset{\downarrow}{0} 1 0)$   
 The bottom output sequence  $v_2 = (1 \underset{\uparrow}{1} \underset{\downarrow}{1} 0 \underset{\uparrow}{1} \underset{\downarrow}{1} 1 0)$

The Encoded output sequence is given by

$$X = (1101010001011100)$$

(OR) frequency domain approach:

$$\text{Message } 11011 \quad M(D) = 1 + D + D^3 + D^4$$

$$g_1(D) = 1 + D \quad g_1^{(1)}(D) = 1 + D + D^2$$

$$g_2(D) = 1 + D^2 \quad g_2^{(2)}(D) = 1 + D^2$$

∴ Top o/p sequence is

$$\begin{aligned} X^{(1)}(D) &= g^{(1)}(D) \cdot M(D) \\ &= (1 + D + D^2)(1 + D + D^3 + D^4) \\ &= 1 + D + D^3 + D^4 + D^5 + D^6 + D^7 + D^8 + D^9 + D^{10} \end{aligned}$$

$$= 1 + D^6$$

$$X^{(1)}(D) = (10000010)$$

Bottom o/p sequence

$$\begin{aligned} X^{(2)}(D) &= g^{(2)}(D) \cdot M(D) \\ &= (1 + D^2)(1 + D + D^3 + D^4) \\ &= 1 + D + D^2 + D^4 + D^5 + D^6 + D^7 + D^8 + D^9 + D^{10} \\ &= 1 + D + D^2 + D^4 + D^5 + D^6 \end{aligned}$$

$$X^{(2)}(D) = (11101110)$$

∴ O/P Encoded sequence

$$X = (1101010001011100)$$

(C) Code tree diagram:

The number of states is given by  $2^{K(M-1)}$

$$(n, K, M) \\ = (2, 1, 3)$$

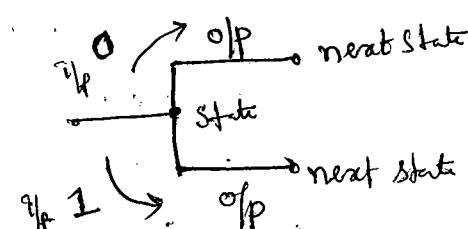
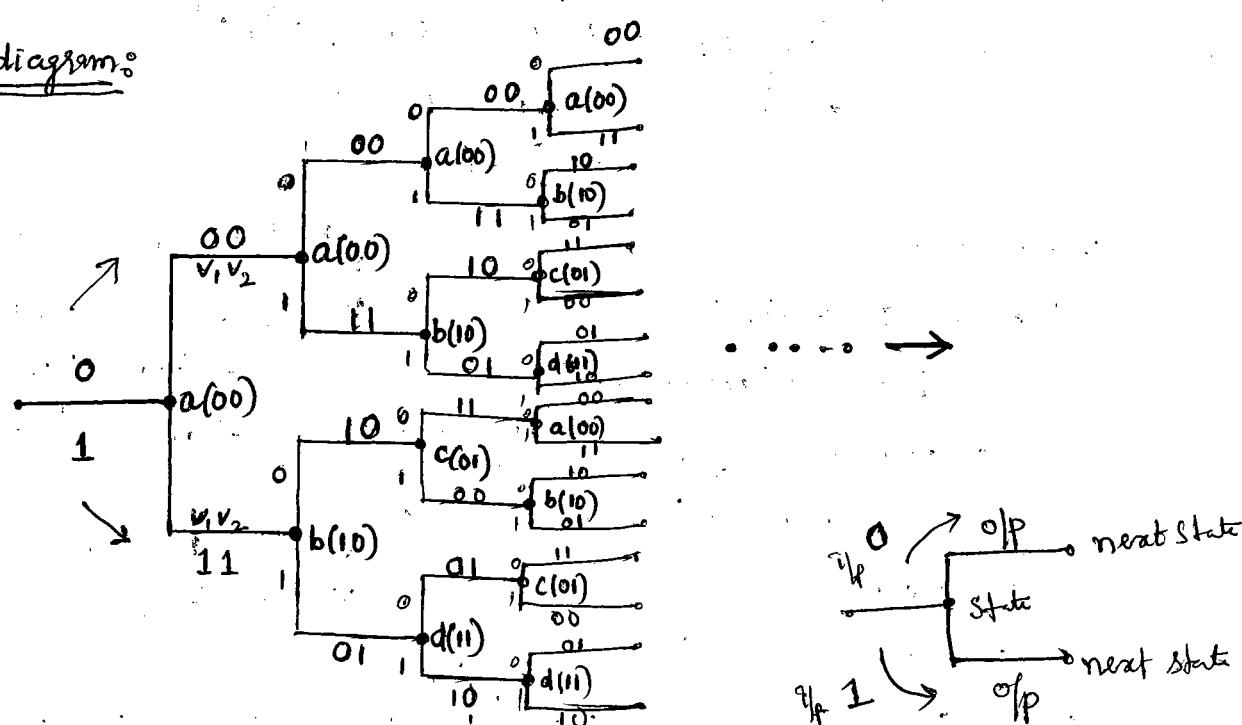
where  
 K - At a time no. of bits given to the register  
 M - no. of shift registers.

No. of states  $2^{1(3-1)} = 2^2 = 4$  i.e.  $\begin{matrix} 00 \\ 01 \\ 10 \\ 11 \end{matrix}$   
 i.e. two states,  $s_1$  and  $s_2$ .

Let  $a=00, b=10, c=01, d=11$ .  
 ↑              ↑

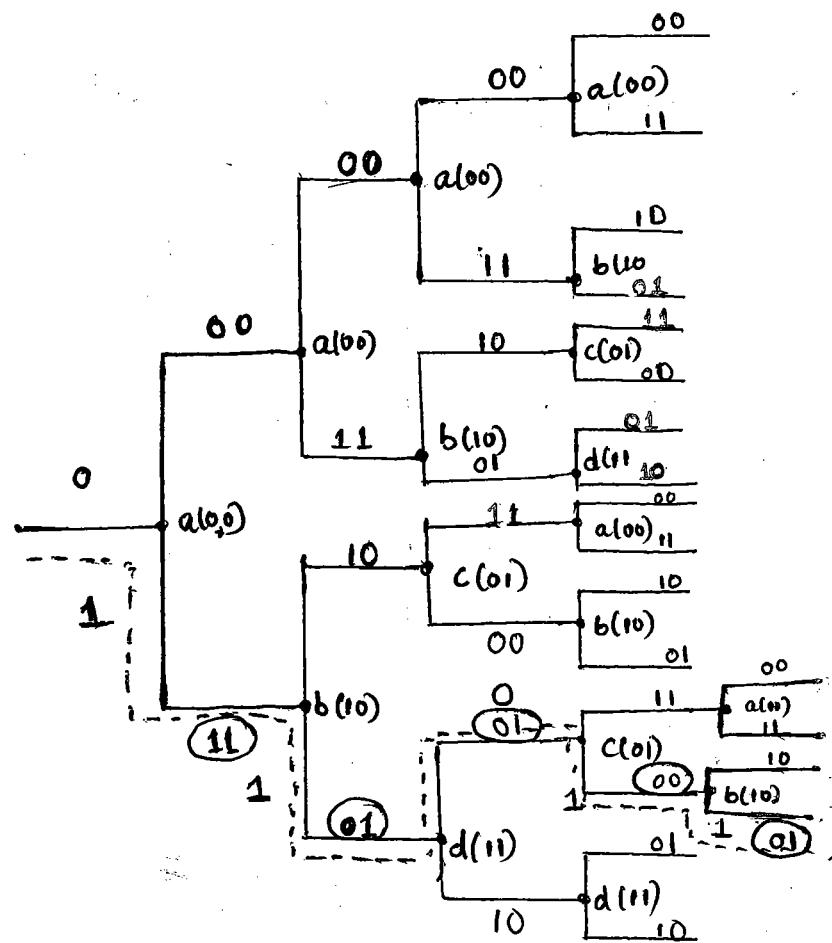
| Input<br>M <sub>1</sub> | Present States                   |                                  |                                                                   |                                                  | Outputs                          |                                  | Next States |   |   |
|-------------------------|----------------------------------|----------------------------------|-------------------------------------------------------------------|--------------------------------------------------|----------------------------------|----------------------------------|-------------|---|---|
|                         | S <sub>1</sub><br>M <sub>2</sub> | S <sub>2</sub><br>M <sub>3</sub> | V <sub>1</sub> = M <sub>1</sub> ⊕ M <sub>2</sub> ⊕ M <sub>3</sub> | V <sub>2</sub> = M <sub>1</sub> ⊕ M <sub>3</sub> | S <sub>1</sub><br>M <sub>1</sub> | S <sub>2</sub><br>M <sub>2</sub> |             |   |   |
| 0                       | 0                                | 0                                | a                                                                 | 0                                                | 0                                | 0                                | 00          | 0 | a |
| 1                       | 0                                | 0                                | a                                                                 | 1                                                | 1                                | 1                                | 10          | 0 | b |
| 0                       | 0                                | 1                                | c                                                                 | 1                                                | 1                                | 1                                | 00          | 0 | a |
| 1                       | 0                                | 1                                | c                                                                 | 0                                                | 0                                | 0                                | 10          | 0 | b |
| 0                       | 1                                | 0                                | b                                                                 | 1                                                | 0                                | 0                                | 01          | 1 | c |
| 1                       | 1                                | 0                                | b                                                                 | 0                                                | 1                                | 1                                | 11          | 1 | d |
| 0                       | 1                                | 1                                | d                                                                 | 0                                                | 1                                | 0                                | 01          | 1 | c |
| 1                       | 1                                | 1                                | d                                                                 | 1                                                | 0                                | 0                                | 11          | 1 | d |

Tree diagram:



The given message sequence 11011.

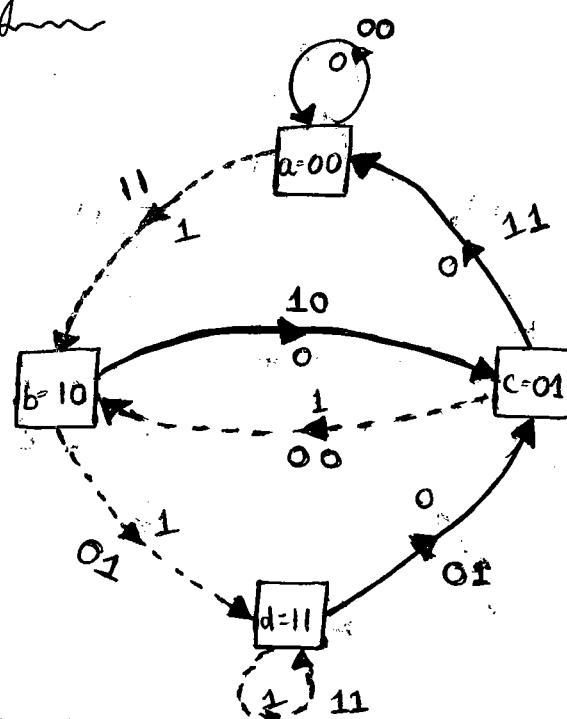
The tree diagram.



The Encoded output for the given message 11011.

(1101010001)

(d) State diagram:



Solid line → for input 0

Dashed line

→ for input 1

→ outputs V1V2

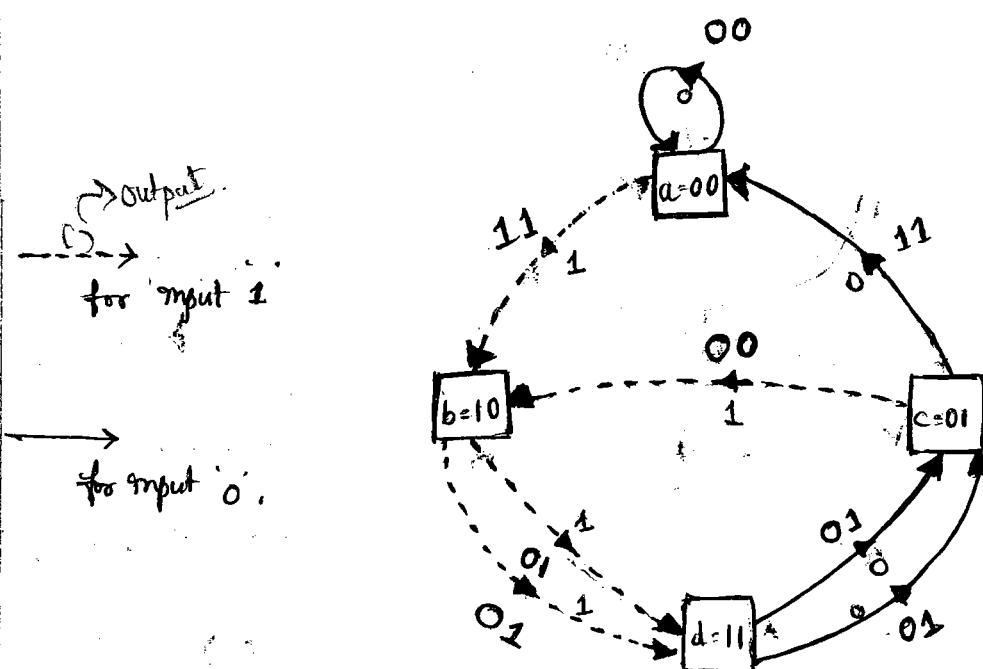
For the given merge sequence 11011, the state table is given by.

| Input | Registers<br>$M_1, M_2, M_3$ | Present<br>States<br>$M_1, M_2, M_3$ | Future/next<br>state<br>$M_1, M_2$ |       | Outputs<br>$V_1 = M_2 M_3, V_2 = M_1 + M_3$ |
|-------|------------------------------|--------------------------------------|------------------------------------|-------|---------------------------------------------|
|       |                              |                                      | $M_1$                              | $M_2$ |                                             |
| -     | 0 0 0                        | 0 0                                  | 0 0                                | 0 0   | 0 0                                         |
| 1     | 1 0 0                        | 0 0 a                                | 1 0 b                              | 1 1   | 1 1                                         |
| 1     | 1 1 0                        | 1 0 b                                | 1 1 d                              | 0 1   | 1                                           |
| 0     | 0 1 1                        | 1 1 d                                | 0 1 c                              | 0 1   | 1                                           |
| 1     | 1 0 1                        | 0 1 c                                | 1 0 b                              | 0 0   | 0                                           |
| 1     | 1 1 0                        | 1 0 b                                | 1 1 d                              | 0 1   | 1                                           |
| 0     | 0 1 1                        | 1 1 d                                | 0 1 c                              | 0 1   | 1                                           |
| 0     | 0 0 1                        | 0 1 c                                | 0 0 a                              | 1 1   | 1                                           |
| 0     | 0 0 0                        | 0 0 a                                | 0 0 a                              | 0 0   | 0                                           |

The Encoded output for the given merge sequence is given by (1101010001011100).

State diagram for given merge sequence.

from above state stable.



(e) Trellis diagram:

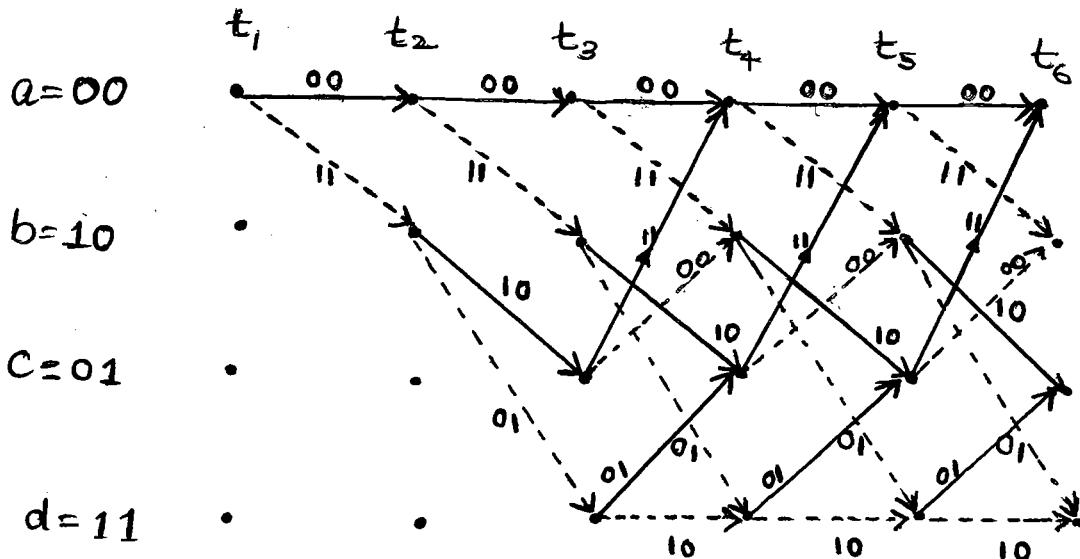
The number of time axis length is given as

$$t_1, t_2, t_3, \dots, t_{L+1}$$

where L - no. of bits present in the message sequence.

Trellis diagram can be obtained from state diagram.

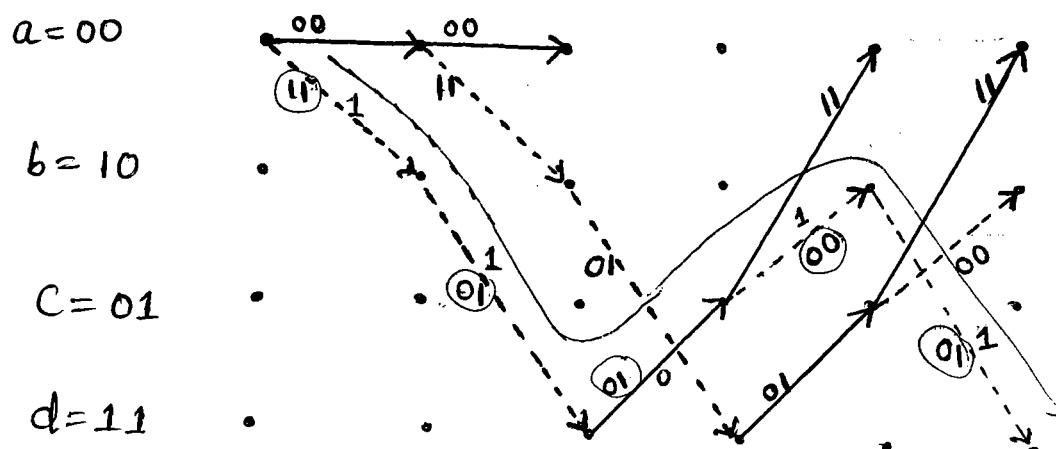
$$2^{K(M-1)} = 2^{1(3-1)} = 2^2 = 4 \text{ stages.}$$



Solid line : → The output generated by an input bit '0'

Dashed line : ..... → The output generated by an input bit '1'.

For the given message sequence 110111, the trellis diagram using state diagram for the message sequence.



The Encoded output sequence : (11 01 01 10 00 01)

## Decoding Methods of Convolutional Codes :

There are two methods for decoding of convolutional codes

1. Viterbi decoding.
2. Sequential decoding.

### ① Viterbi Decoding :

~~~~~x~~~~~x~~~~~x~~~~~x~~~~~

- ✓ The viterbi algorithm are currently used in about one million cellphones, which is probably the largest numbers in any application.
- ✓ The largest current consumer of viterbi algorithm processor cycle is probably digital video broadcasting.
- ✓ Now being decoded by the viterbi algorithm in digital TV sets around the world every second of every day.
- ✓ Now a days they are also used in bluetooth implementations.
- ✓ Viterbi algorithm essentially performs max-likelihood decoding.
- * The algorithm involves calculating a measure of similarity or distance between the received signal at time t and all the trellis paths entering each state at time t .
- ✓ When two paths enter the same state, the one having the best metric is chosen. This path is called the "Serving Path".
- * The hamming distance between two code vectors is called "Metric".
- * The smallest path from transmitter to receiver is called "Metric path" (or) "Serving path".

① A $(2,1,3)$ convolutional code is described by $g_1 = (111)$ & $g_2 = (101)$. The output of the detector is '1101011001' using the viterbi algorithm. Find the transmitted data.

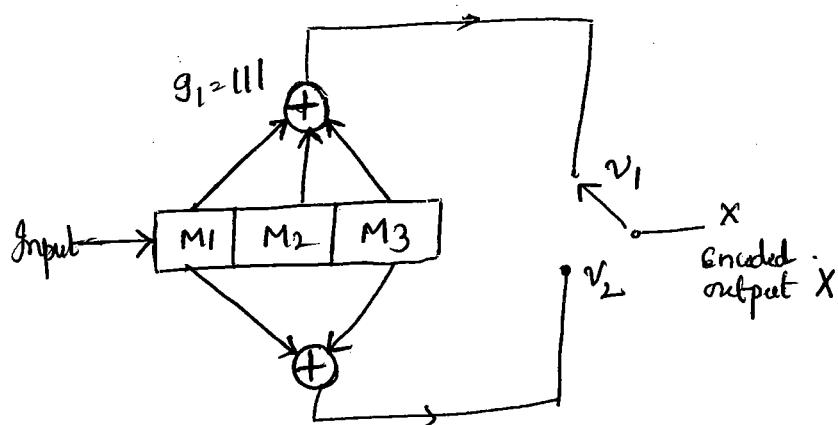
Sol

Given $(2,1,3) = (n, k, M)$ Convolutional Code.

$$n=2, k=1, M=3.$$

$$\text{Given } g_1 = (111), g_2 = (101)$$

The encoder diagram as



$$v_1 = M_1 \oplus M_2 \oplus M_3$$

$$v_2 = M_1 \oplus M_3$$

The state stable is given by.

$$\begin{aligned} \text{The no. of states} &= 2^{K(M-1)} \\ &= 2^2 = 4 \text{ states} \end{aligned}$$

$$\text{let } a=00, b=10, c=01, d=11.$$

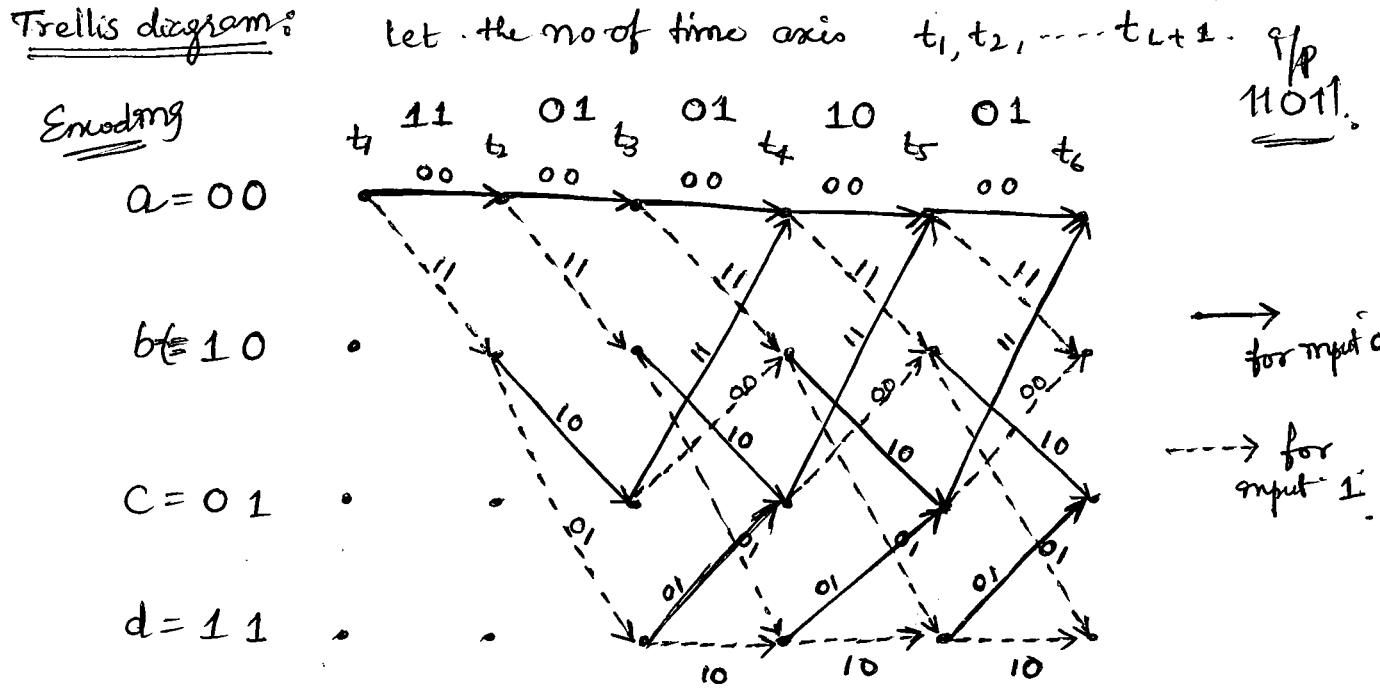
In general.

| Input M ₁ | Present States | | Next States | | Outputs | |
|-------------------------|----------------|-------------------------------|---------------------------------|---|--|--|
| | M ₁ | M ₂ M ₃ | M ₁ , M ₂ | v ₁ = M ₁ ⊕ M ₂ ⊕ M ₃ | v ₂ = M ₁ ⊕ M ₃ | |
| 0 | 0 | 0 0 a | 0 0 a | 0 | 0 | |
| 1 | 0 | 0 0 a | 1 0 b | 1 | 1 | |
| 0 | 0 | 0 1 c | 0 0 a | 1 | 1 | |
| 1 | 0 | 0 1 c | 1 0 b | 0 | 0 | |
| 0 | 1 | 0 b | 0 1 c | 1 | 0 | |
| 1 | 1 | 0 b | 1 1 d | 0 | 1 | |
| 0 | 1 | 1 d | 0 1 c | 0 | 1 | |
| 1 | 1 | 1 d | 1 1 d | 1 | 0 | |

(P7D)

Viterbi decoding can be obtained by using trellis diagram.

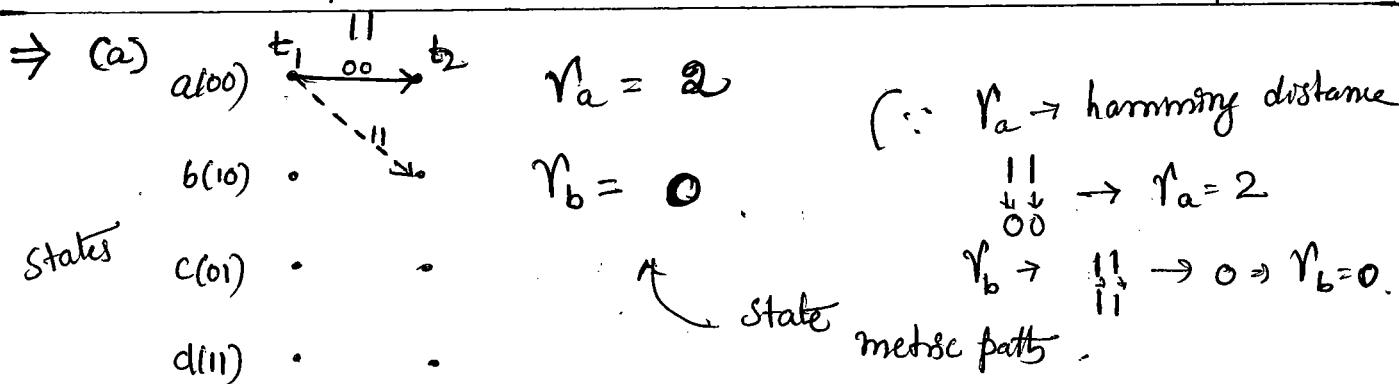
Trellis diagram:



Viterbi decoding:

The output of the decoder is 1101011001

The smallest path from transmitter to receiver is metric path.



Fig(a) Survivors at t_2

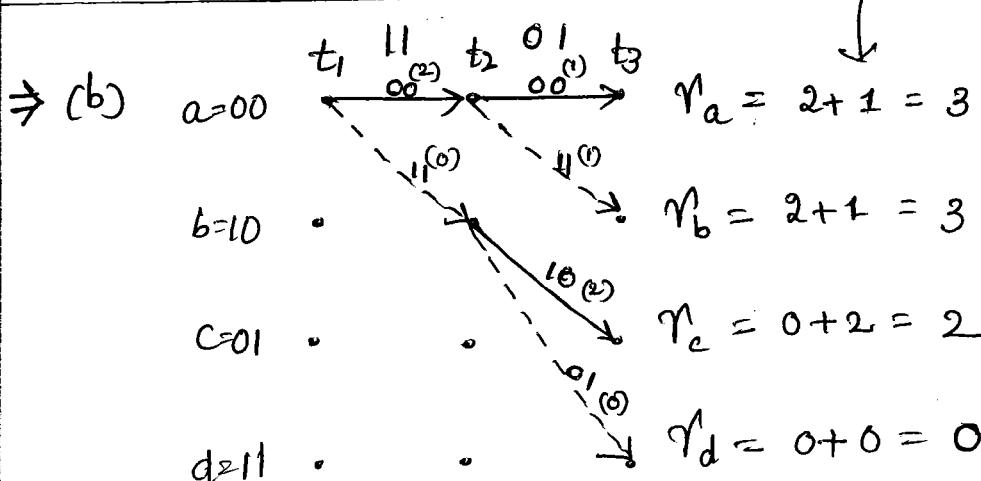


Fig: Survivors at t_3 .

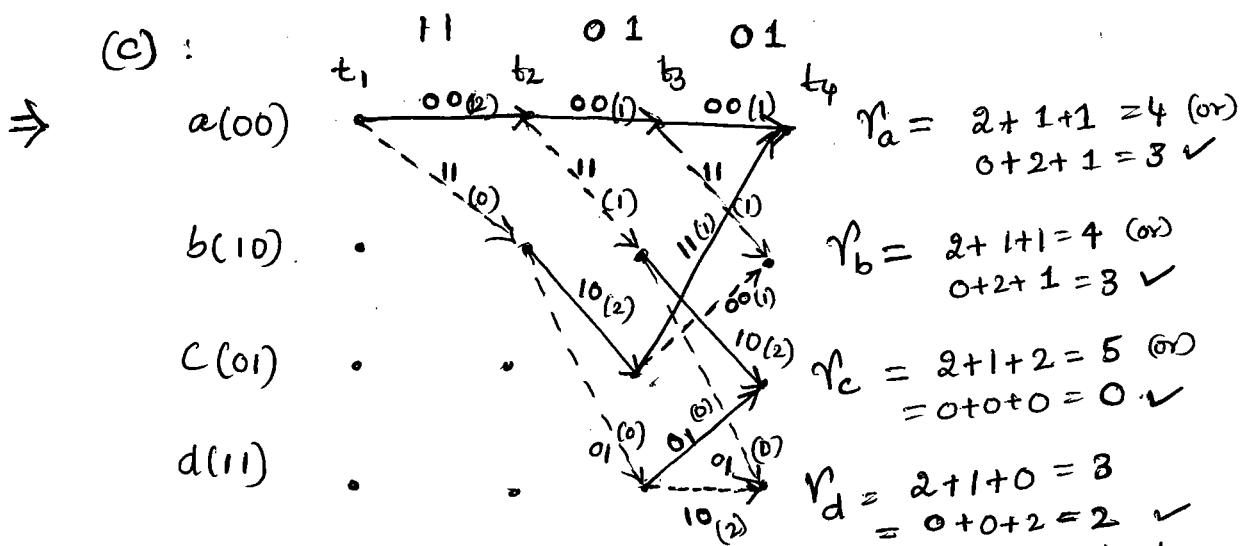


Fig: Metric components / compression at t_4 .

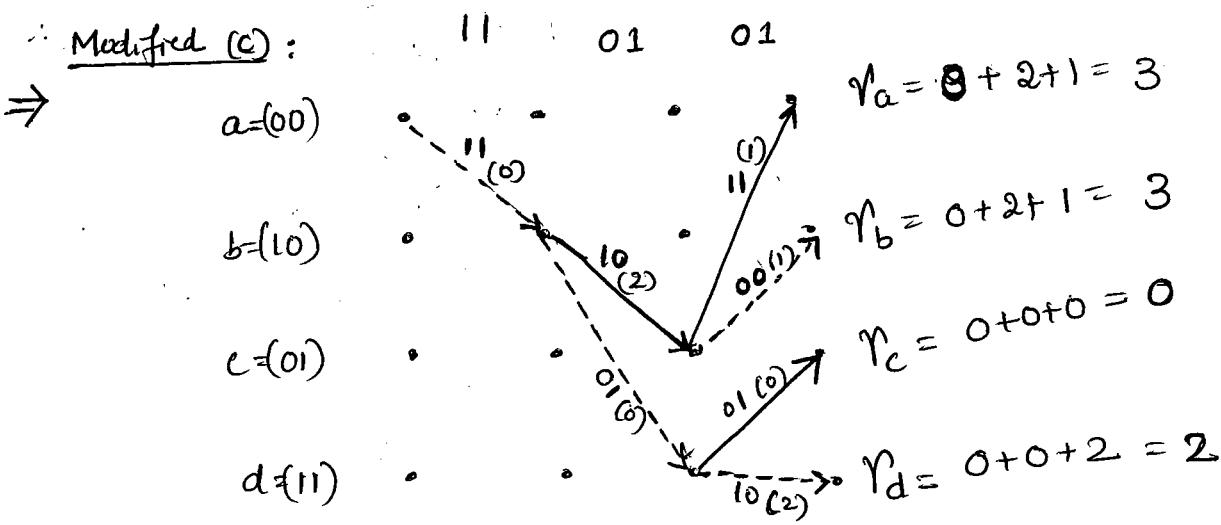


Fig: Survivors at t_4 .

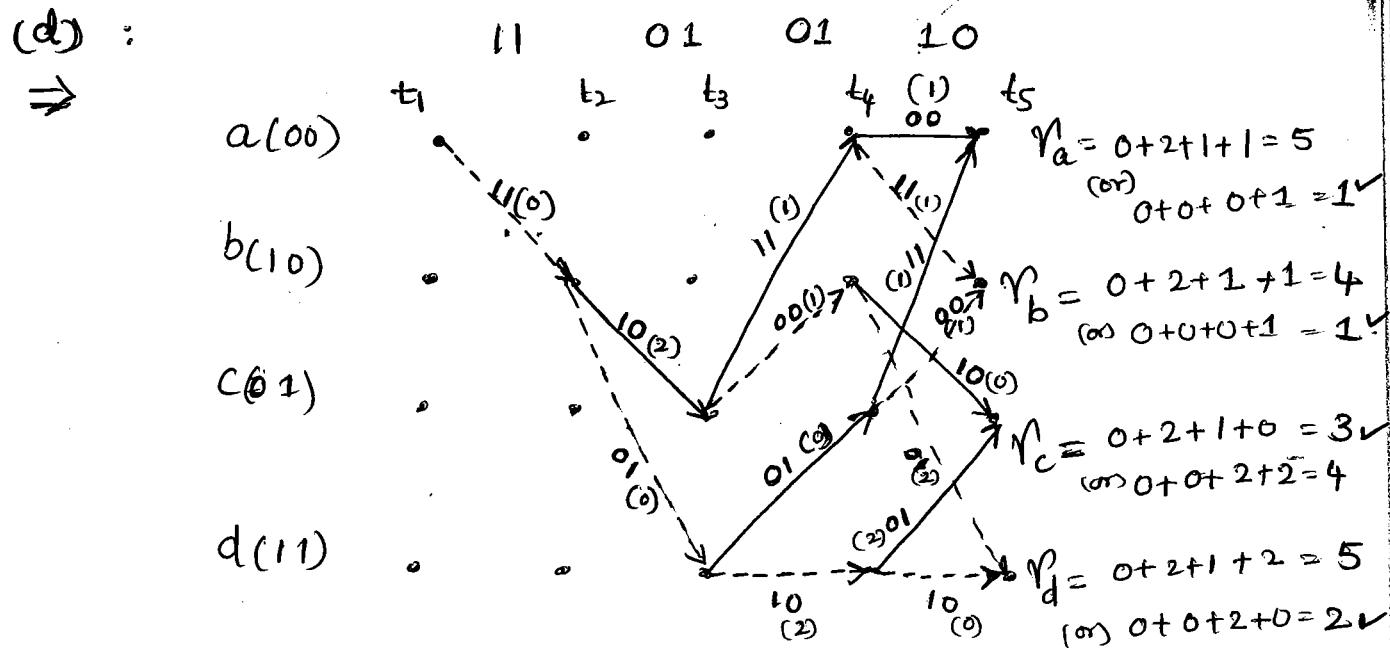


Fig: Metric Components / compression at t_5

Modified (d) :

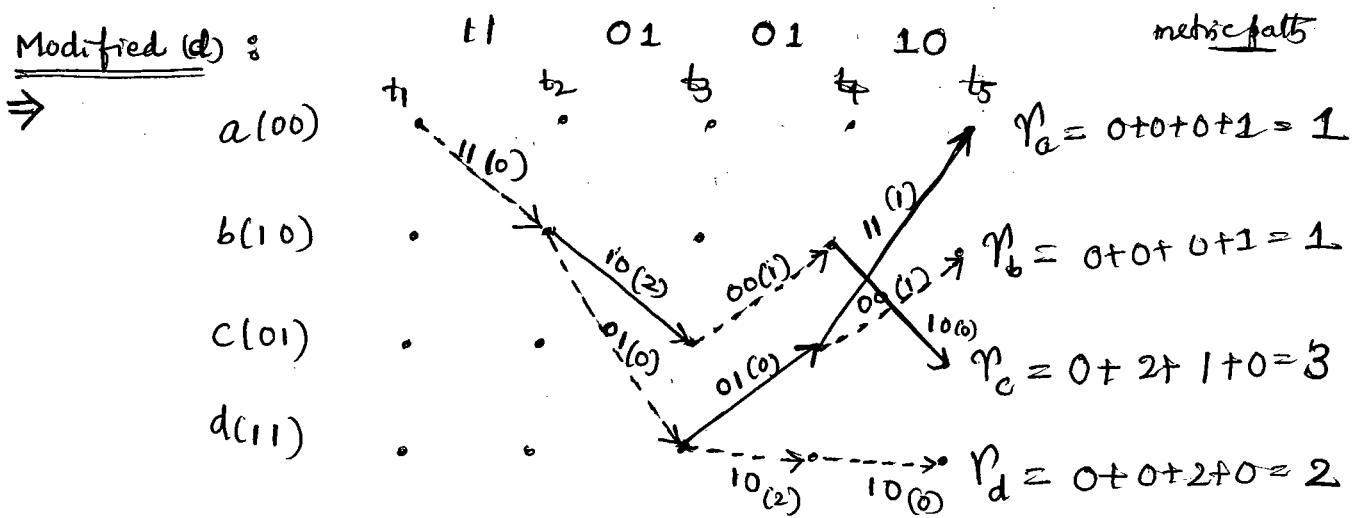


Fig : Survivors at t_5 .

(e).

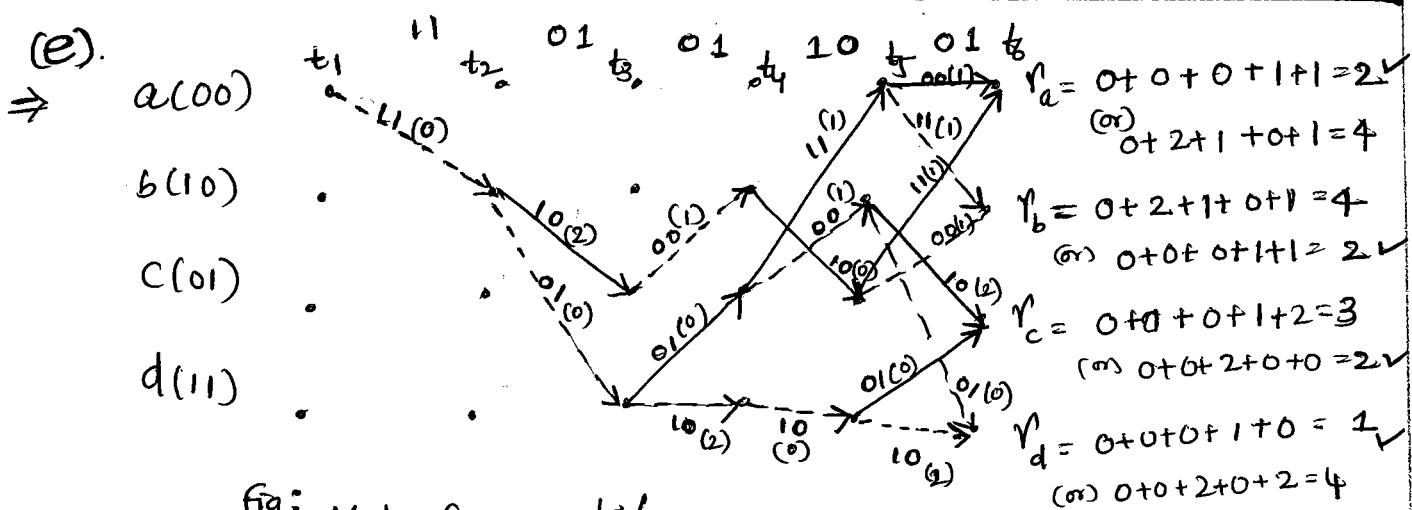


Fig : Metric Components / Comparison at t_6 .

Modified (e) :

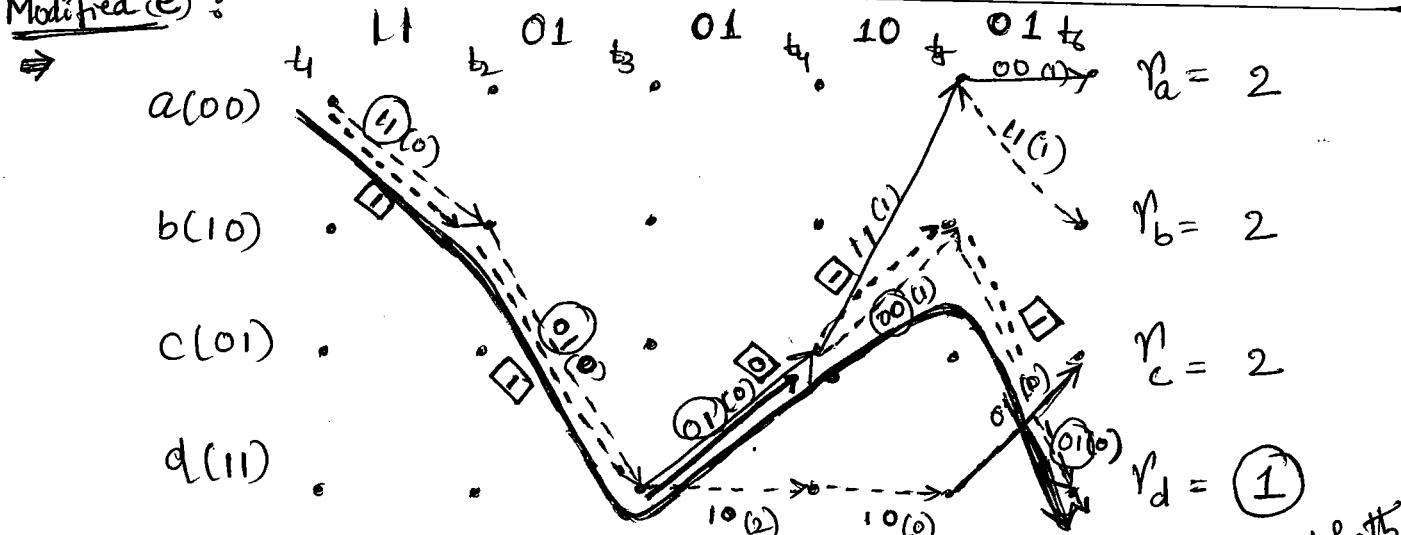


Fig : Survivors at t_6 .

shortest path

- Among four state metric path values $r'_d = 1$ as a minimum length
- So, The path from t_1 to t_6 through r'_d is selected for decoding the message.

Message 11011

∴ Transmitted Data : 1101010001

Received Data ; 1101011001

Message 11011

② Sequential Decoding :

- ✓ Viterbi decoding algorithm for a convolutional code is maximum likelihood, the error performance of the algorithm over a discrete memoryless channel is optimum.
- ✓ The computational complexity of the algorithm is large.
- ✓ The constraint length $N = M+i$ is limited, so error correcting capability of the code is restricted.

Hence a decoding algorithm that avoids computing the likelihood or metric of every path in the trellis, thereby reducing computational complexity and allowing the constraint length 'N' to take on very large values. There is a suboptimum class of such algorithms known as "Sequential decoding algorithm".

- ✓ The complexity of a sequential decoder is essentially independent of the constraint length 'N', so that very large values of 'N' can be employed.
- ✓ Although sequential decoding algorithms are not quite as good as maximum likelihood decoding / Viterbi decoding algorithms.
- * Sequential decoding is an intuitive trial-and-error technique for searching out the correct path in a code tree.
- ✓ During the search, the decoder moves forward or backward in the code tree, one node at a time.
- ✓ The decision whether to move forward or backward is determined by the manner in which the metric of the algorithm varies along the path followed by the decoder.
 - (a) Fano Metric
 - (b) Fano Algorithm.

(a) Fano Metric:

Let us consider constraint length N
Code rate $r = k/n$

Convolutional code over a discrete memory less channel.

let $c_{ij} \rightarrow i^{\text{th}}$ bit of binary label on the j^{th} branch of the code tree.

let $y_{ij} \rightarrow$ The corresponding bit of received sequence at the channel output.

\therefore The bit metric is defined by

$$y_{ij} = \log_2 \left(\frac{p(y_{ij}/c_{ij})}{p(y_{ij})} \right) - r \quad \rightarrow ①$$

where $p(y_{ij}/c_{ij}) \rightarrow$ a transition probability of the channel.

$p(y_{ij}) \rightarrow$ The nominal (nominal) probability of the channel output.

If the binary label on each branch of the code tree has n bits.

Hence bit metric becomes branch metric as

$$\therefore y_j = \sum_{i=1}^n y_{ij}$$

$$y_j = \sum_{i=1}^n \left[\log_2 \left(\frac{p(y_{ij}/c_{ij})}{p(y_{ij})} \right) - r \right] \quad \rightarrow ②$$

The Sequential decoder has followed a path with l branches, starting from the origin of the code tree.

\therefore The path metric (or) Fano metric as

$$P(l) = \sum_{j=1}^l y_j$$

$$P(l) = \sum_{i=1}^n \sum_{j=1}^l \left[\log_2 \left[\frac{p(y_{ij}/c_{ij})}{p(y_{ij})} \right] - r \right] \quad \rightarrow ③$$

(b) Fano Algorithm:

where $l = 1, 2, \dots$

- ✓ In this, the decoder moves forward (or) backward through the code tree always one node at a time.
- ✓ The decision whether or not the decoder favours a node is made by comparing the path metric at a node with a

running threshold maintained by the decoder.

- * The running threshold ' T ' is defined as an integer multiple of the threshold spacing ' Δ ', which is a design parameter.
- When ' Δ ' is too small, the decoder frequently back-tracks even when it is on the correct path.
- When ' Δ ' is too large, the decoder incorrect turns are not identified quickly.

Thus a compromise choice of ' Δ ' is required to minimize computation.

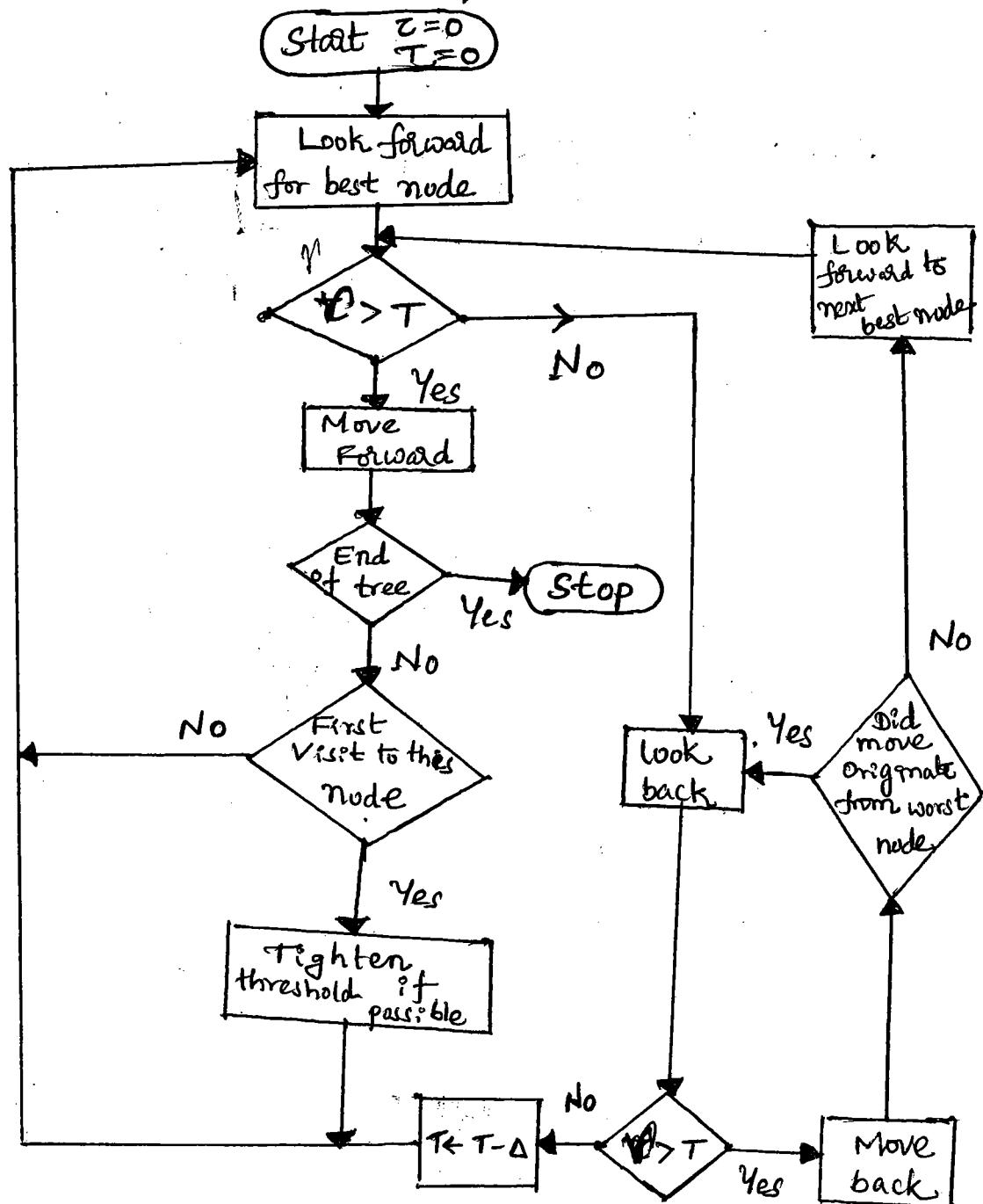


Fig: Flow chart for the Fano Algorithm.

Example:

Let us consider code tree diagram for
Code rate = $\frac{1}{2}$ & Constraint length '7' of
Convolutional code.

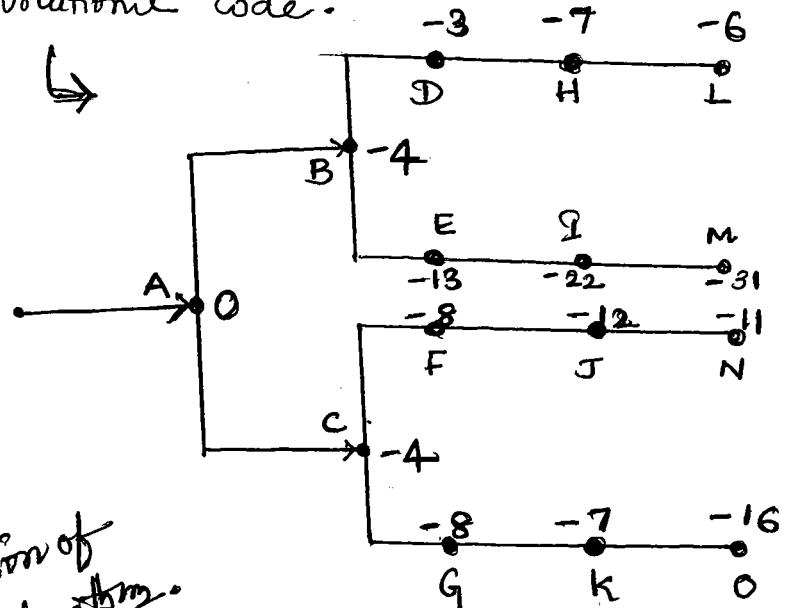


Illustration of
Fano algorithm.

| Step | Node | Fano Metric $\gamma(e)$ | Threshold T |
|------|------|-------------------------|---------------|
| 1 | A | 0 | 0 |
| 2 | A | 0 | -4 |
| 3 | C | -4 | -4 |
| 4 | A | 0 | -4 |
| 5 | B | -4 | -4 |
| 6 | D | -3 | -4 |
| 7 | B | -4 | -4 |
| 8 | A | 0 | -4 |
| 9 | | 0 | -8 |
| 10 | | -4 | -8 |
| 11 | C | -8 | -8 |
| 12 | K | -8 | -8 |
| 13 | G | -8 | -8 |
| 14 | J | -8 | -8 |
| 15 | G | 0 | -8 |
| 16 | U | -4 | -8 |
| 17 | A | -4 | -8 |
| 18 | B | -3 | -8 |
| 19 | D | -7 | -8 |
| 20 | H | -6 | -8 |
| | L | | |
| | STOP | | |