

UNIT-5

Special Semiconductor Devices

Principle of operation, char. & Application of -
Varactor diode, Tunnel Diode, Uni Junction Transistor,
Silicon Controlled Rectifier, Schottky diode, LED, Phototransistor.

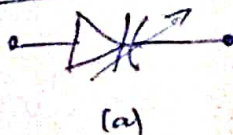
Varactor Diode:-

* As the transition capacitance of a diode varies with the applied voltage, it can be used as a voltage variable capacitance in many applications.

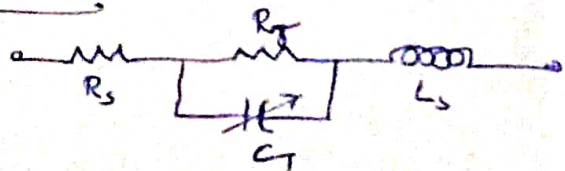
* In practice, special type of diodes are manufactured which shows the transition capacitance property more predominantly as compared to the normal diode, such diodes are called "Varactor diodes", "Varicap", VVC (VOLT Variable Capacitance) and 'tuning diode'.

* It is also a junction diode with a small impurity dose at its junction, which has the useful property that its junction transition capacitance is easily varied electronically.

Symbol:-



Equ. CNJ



where,

R_r - Reverse Resistance, which is very large ($\geq 1 \text{ M}\Omega$)

R_s - The geometric resistance of diode (0.1 - 1 Ω) (i.e., small)

L_s - The Inductance it indicates that there is a high freq. limit associated with the use of Varactor diodes

C_T - Transition Capacitance.

$$* C_T = \frac{k}{(V_j + V_R)^n}$$

where, k - constant depends on semiconductor material and construction technique.

V_j - junction potential.

V_R - magnitude of reverse bias voltage.

$n = \frac{1}{2}$ for the alloy junction, $\frac{1}{3}$ for diffused junction.

$$\lambda W \propto \sqrt{V_0} = V_0^{1/2} \quad (\text{or}) \quad W \propto \sqrt[3]{V_0} = V_0^{1/3}$$

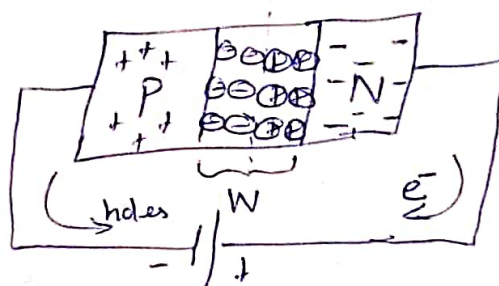
* At the zero bias condition, the capacitance is $C(0)$.

In terms of $C(0)$, the transition capacitance is,

$$C_T = \frac{C(0)}{\left[1 + \frac{V_R}{V_j}\right]^n}$$

Principle of operation of Varactor Diode:-

* When diode is RB, the depletion Region is formed



* The Large Reverse bias

applied across the diode, the width of the depletion region 'W' becomes wider.

* For small Reverse bias, depletion layer width will become narrow.

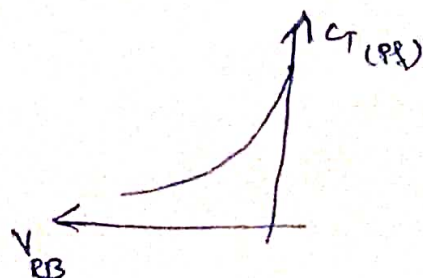
* This depletion Region is devoid of majority carriers and acts like an insulator preventing conduction b/w the N and p regions of the diode, which represents like two plates of a capacitor.

$$C_T \propto \frac{1}{W} \quad \text{or} \quad C_T = \frac{\epsilon A}{W} \quad \text{where } \epsilon - \text{permittivity}$$

A - Area
W - distance b/w plates.

* Increasing RB Volt is increase depletion region width and decreasing transition capacitance C_T .

* At zero Volt, the Varactor depletion region is small and the capacitance is large at approximately 600 Pf.



$V_{RB} = 0$
W - small
 $C_T \uparrow$

Application :

- ① FM radio & TV Receivers.
- ② AFC CKts i.e., Automatic frequency control.
- ③ self Adjusting bridge circuit
- ④ Adjustable band pass filter.
- ⑤ PLL i.e., Phase locked loop
- ⑥ Voltage controlled oscillators.
- ⑦ Electronic tuning devices, mobiles.

→ Determine the transition capacitance of a diffused junction Varactor diode at a reverse bias Volt. of 4.2V of $C(0) = 80 \text{ pF}$. and junction potential of 0.7V. Also calculate constant 'K' for diode.

so Given data: $V_R = 4.2 \text{ V}$, $V_j = 0.7 \text{ V}$, $C(0) = 80 \text{ pF}$; $n = \frac{1}{3}$

$$C_T = \frac{C(0)}{\left[1 + \left(\frac{V_R}{V_j}\right)\right]^n} = \frac{80 \times 10^{-12}}{\left[1 + \left(\frac{4.2}{0.7}\right)\right]^{\frac{1}{3}}} = 41.82 \text{ pF.}$$

and

$$C_T = \frac{K}{[V_R + V_j]^n} \Rightarrow K = C_T [V_R + V_j]^n = 41.82 \times 10^{-12} \times (4.2 + 0.7)^{\frac{1}{3}} \\ = 71.03 \times 10^{-12}$$

Tunnel Diode:-

* A Tunnel diode is a type of semiconductor diode which is capable of very fast operation.

* A tunnel diode is a thin-junction diode which exhibits negative resistance under low forward bias conditions.

* Introduced by Leo Esaki in 1958. So, it is called as 'Esaki diode'. There are high impurity density normal PN Junction diodes.

* Normally an electron or holes must have energy greater than or equal to potential energy barrier, to move to other side of the barrier. For very thin barrier there is a large probability of electron penetrating through the barrier which is called as Tunneling (Quantum-Mechanical behavior).

Symbol



where,

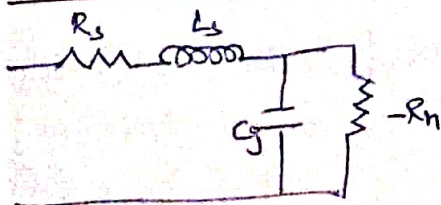
R_s - series Resistance due to ohmic contact resistance.

L_s - Series Inductance due is depends upon the lead length & geometry of the diode package.

C_j - Junction capacitance depends upon the bias represents junction diffusion capacitance.

$-R_n$ - -ve resistance has a minimum at the point of inflection b/w peak & valley of $I-V$ char.

Equivalent Circuit:



Principle of operation:

Under Forward bias:

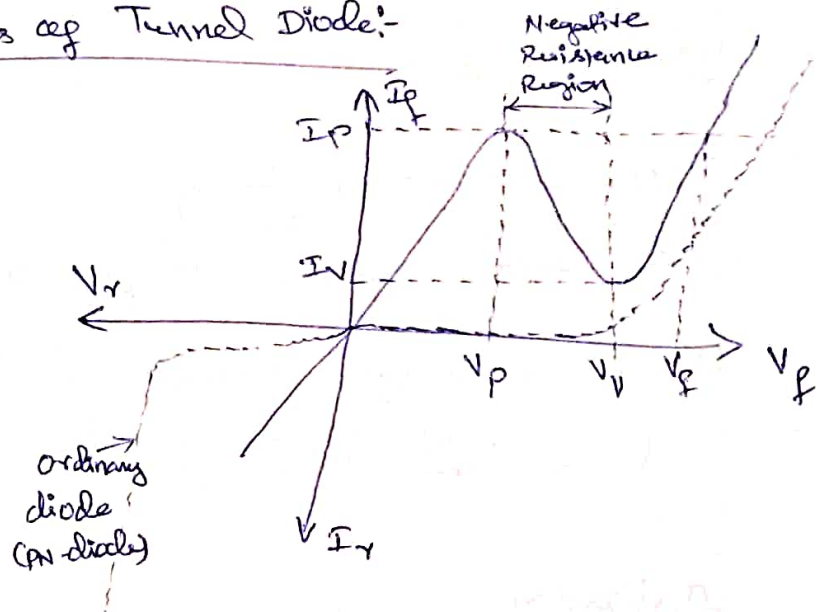
- * As voltage begins to increase, electrons at first tunnel through the very narrow pn-junction barrier ~~become~~ because filled electrons states in the conduction band on the n-side become aligned with empty valance band hole states on the p-side of the pn junction.
- * As voltage increases further these states become more misaligned and the current drops. It is called -ve resistance because current decreases with increasing voltage.
- * As voltage increases yet further, the diode begins to operate as a normal diode where electrons travel by conduction across pn junction.

Under Reverse bias:-

- * When used in the Reverse direction they are called 'back diodes' and can act as fast rectifiers with zero offset voltage and extreme linearity for power signals.
- * Under Reverse bias filled states on the p-side become increasingly aligned with empty state on the n-side and electrons now tunnel through the pn junction barrier in Reverse direction.

V-I Characteristics of Tunnel Diode:-

Where,
 V_p - Peak Voltage
 V_v - Valley Voltage
 V_f - Peak Forward Voltage
 I_p - Peak Current
 I_v - Valley Current



* Thus the most important operating Region for a tunnel diode is the negative resistance Region.

* The value of current b/w I_p and I_v can be obtained with these different Voltage values. So the characteristics is tripe values. This multivalued features makes the tunnel diode useful in the pulse and digital circuits.

Advantage:-

- (1) Environmental Immunity is; Peak point (V_p, I_p) is not a sensitive function of temperature.
- (2) Low cost.
- (3) Low power Consumption.
- (4) High speed is; tunneling takes place at the speed of light.
- (5) Low noise.

Disadvantage :-

- (1) Low o/p Voltage swing, is; even for small voltage while the current goes to large values. so the swing is limited.
- (2) circuit design difficulty. since it is a two terminal device and there is no isolation b/w i/p and o/s.

Application:-

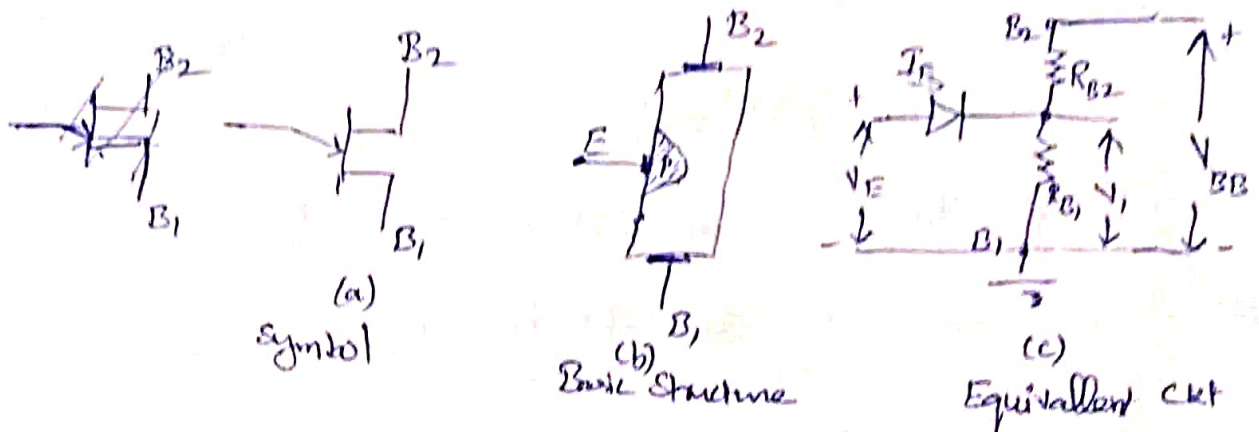
- (1) As a high speed switch
- (2) In pulse and digital circuit.
- (3) In negative resistance and high freq (microwave) oscillator.
- (4) In switching networks.
- (5) In timing and computer logic circuitry.
- (6) Design of pulse generators and amplifiers.

Uni Junction Transistor (UJT):-

* UJT is a 3-terminal semiconductor switching device, It has only one P-n junction and 3-lands.

* P-type emitter alloyed to a lightly doped n-type material.

* Terminals are, Emitter (E), Base-1 (B1) and Base-2 (B2).



* Originally this device was named as 'double base diode' but now it is commercially known as UJT.

Principle of operation & VI characteristics:-

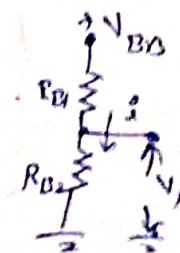
* If emitter $I_E = 0$, due to supply voltage V_{BB} , a current 'i' is,

$$i = \frac{V_{BB}}{R_{B1} + R_{B2}}$$

$$V_1 = i R_{B1} = \frac{V_{BB} \times R_{B1}}{R_{B1} + R_{B2}}$$

$$V_1 = \frac{R_{B1}}{R_{B1} + R_{B2}} \times V_{BB} = \eta V_{BB}$$

where, η = Intrinsic stand off ratio.



* It is evident that the diode cannot conduct unless the emitter voltage

$$V_E = V_Y + V_1 \quad \left[\text{where, } V_Y - \text{cut in volt. of the diode} \right]$$

* The value of the emitter voltage which make the diode conduct is termed as 'peak voltage' and It is denoted as ' V_p '.

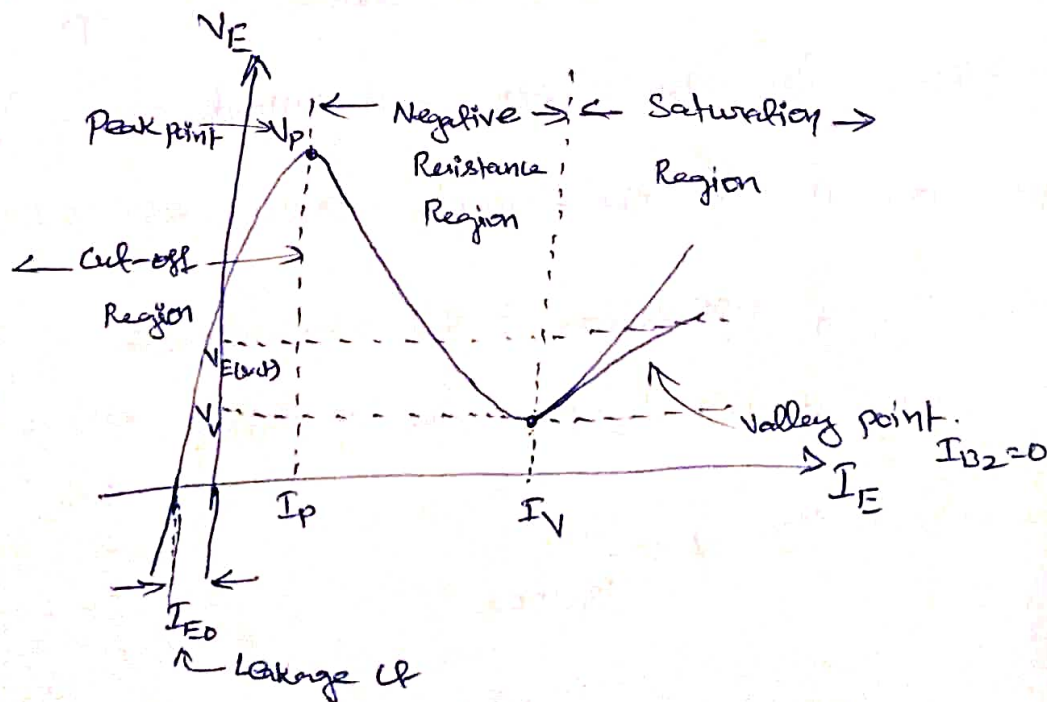
$$V_p = V_Y + V_1 \quad \text{where, } V_1 = \eta V_{BB} = \frac{R_{B1}}{(R_{B1} + R_{B2})} V_{BB}$$

$$V_p = V_Y + \eta V_{BB}$$

$$\eta = 0.56 \text{ to } 0.82$$

i.e., If $V_E < V_p$ then UJT is OFF and

$V_E > V_p$ then UJT is ON.



* If a -ve voltage is applied to the emitter, PN-junction remains reverse biased and the emitter current is cut-off. The device is now in the off state, upto the left of the peak point is called 'cut-off region'.

* The UJT has a stable firing voltage ' V_p ' & small firing current I_{fp} (25 μA). At 'P' the peak voltage $V_p = V_g + \eta V_{BB}$, the diode starts conducting and holes are injected into N-region, hence resistance decreases there by decreasing V_E for the increasing I_E (Negative resistance Region).

* After the Valley point, the device is driven into saturation and behaves like a normal PN junction diode. In Valley point the resistance changes from negative to positive.

* For large I_E , the characteristics asymptotically approaches the curve for $I_{B2} = 0$

Application:-

- ① Sawtooth wave generator
- ② pulse generator
- ③ switching, timing and phase control circuits
- ④ UJT used as Relaxation oscillator.

Schottky Diode:-

* Schottky Diode is a diode of junction lightly doped n-type semiconductor with a metalelectrode.

ie, A metal semiconductor junction is formed b/w a metal and a semiconductor, creating a schottky barrier diode.

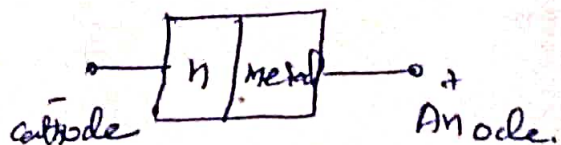
* Typically metal used are molybdenum, Platinum, chromium, tungsten and Aluminium.

* Metal sides acts as the anode and n-type semiconductor acts as the Cathode.

* This Schottky diode results is both very fast switching diode and low forward volt. drops.

* Schottky diode can produce a very fast switching diode which is mainly used in high frequency circuits and high speed digital circuits.

Symbol



Principles of operation:-

It is a unipolar device because it has electrons as majority carriers on both side of the junction. Hence there is no depletion layer formed near the junction.

* Under Forward bias, the electrons move from n-type material to the metal and give up their energy quickly. There are no holes, so the conduction quickly stops upon change to reverse bias.

FB $\rightarrow e^-$ move to semi to metal quickly.
RB, conduction quickly stops because not holes.

i.e; The forward CF is dominated by electrons flow from semiconductor to metal and the reverse CF is mainly due to electrons flow from metal to semiconductor. As there is very little minority carrier injected from semiconductor into metal, so there ~~are~~ are also said to be "Majority Carrier Devices".

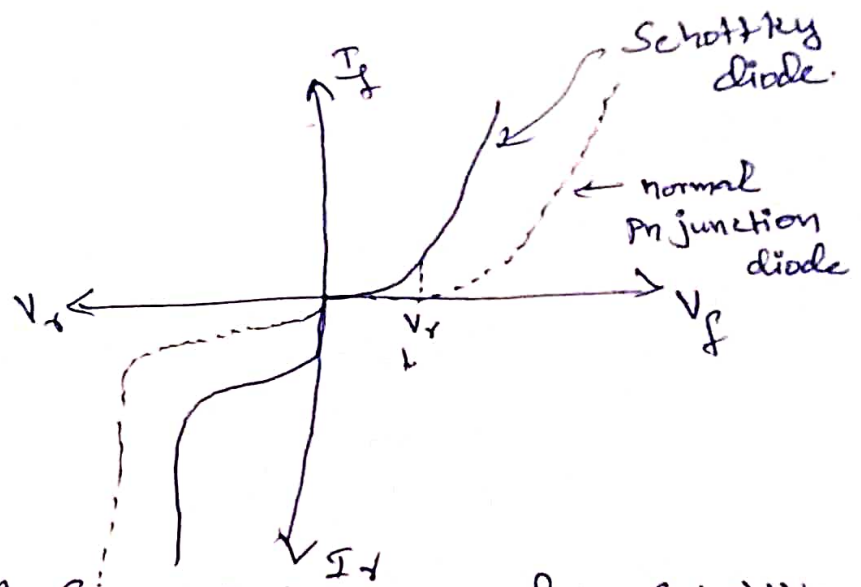
F.D. $\rightarrow I \uparrow$ from semi to metal.
R.D. $\rightarrow I \downarrow$ e^- flow from metal to semi is low.

* The diode is also referred to as "hot carrier diode" because unipolar biased electrons plunge into the metal from semiconductor with large energy. These are commonly called as hot carriers.

V-I Characteristics

* V_{γ} - cut in voltage

is less for Schottky diode.



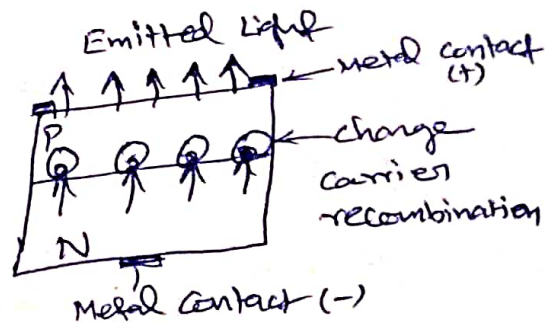
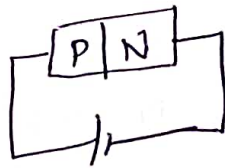
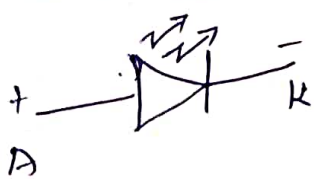
* Reverse saturation current is more for Schottky diode compared normal PN-junction diode for silicon.

Application

- ① Schottky diode can be used for rectification of signals of frequencies even exceeding 30 MHz.
- ② used in switching power supplies at freq. of 20 kHz.
- ③ used in sensitive communication receivers like RADARS.
- ④ used in clipping and clamping circuit and in computer gating.

LED - Light Emitting Diode

* LED is a PN junction device which emits light when forward biased, by a phenomenon called Electroluminescence.



Operation

* In all semiconductor PN Junctions some of the energy will be radiated as heat and some in the form of photons.

* In Si and Ge greater percentage of energy is given out in the form of heat and Emitted Light is in significant.

* In other materials like GaAsP (gallium arsenide phosphide), GaP (gallium phosphide) the no. of photons of light energy emitted is sufficient to create a visible light source.

* When forward bias is applied the barrier potential is reduced and the depletion region is narrowed. As holes are injected into n-region & recombined with free electrons,

like electrons injected into p-region and recombine with hole current results in Excess energy is transferred to an emitted photon and light is generated.

* The brightness of the light is directly proportional to the forward current.

* LED radiate different colours such as Red, Green, Yellow and orange, Blue and white. The wavelength of emitted light depends on the energy gap of the material. Hence the colour of the emitted light depends on the type of Materials used as,

✓ GaAs (gallium arsenide) - Infrared radiation (Invisible)

✓ GaP (gallium phosphide) - Red, Green.

✓ GaAsP (gallium arsenide phosphide) - Red and Yellow.

* In order to protect LEDs, Resistance of 1 k Ω or 1.5 k Ω must be connected in series with the LED.

* LEDs emit no light when reverse biased.

* LEDs operate at Voltage levels from 1.5 to 3.3V

* LEDs can be switched ON and OFF at very fast speed of 1 nsec.

Applications :

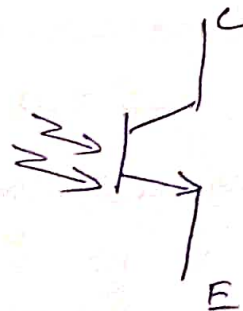
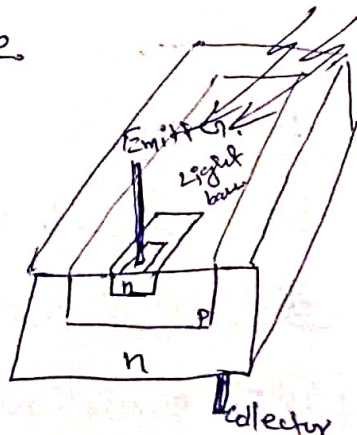
- (1) Burglar alarms Systems
- (2) Picture phones
- (3) Multimeters
- (4) Calculators
- (5) Digital meters
- (6) Microprocessors.

Photo Transistor :

* As transistor providing Internal Current Multiplication when exposed to light is called photo transistor.

* It has light Sensitive Collector to base junction. A lens is used so that the base is exposed to the light.

* The base collector area is kept large as it is light Sensitive.



* The base terminal is generally not brought out hence phototransistor act as a two terminal devices.

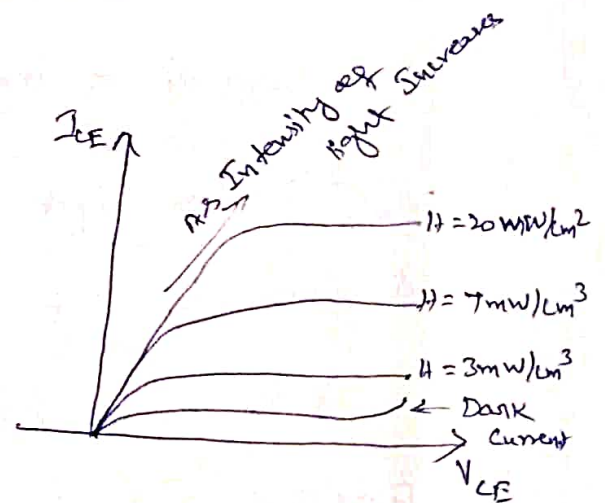
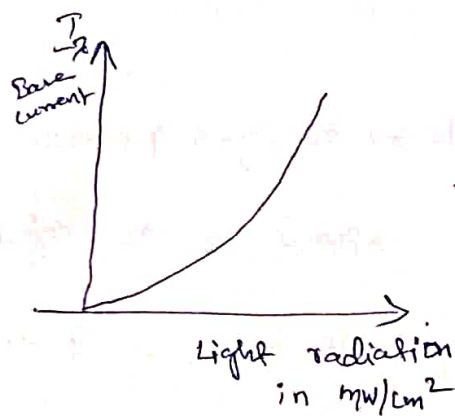
* The construction of a phototransistor is shown in the figure.

When no light is incident small leakage current flow from collector to emitter called I_{CEO} due to small thermal generation. This is called dark current which is very small (10^{-9} A) .

* When base is exposed to light, base current is produced due to generated electron-hole pairs. This is photocurrent. This is denoted as I_B .

* $\therefore$ collector current $I_C = \beta I_B$ ✓

* light Intensity $\uparrow \rightarrow I_B \uparrow \rightarrow$ so collector current also increases (✓)



Characteristics

- * In biasing, base kept open
- * Collector connected to +ve terminal
- * Emitter is ground.
- * light Intensity $\uparrow$ then base $I_B \uparrow$
- * o/p char, Intensity $\uparrow$ then $I_C \uparrow$
- * Speed is good but not fast than photodiode.

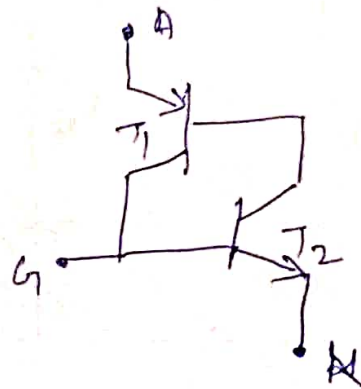
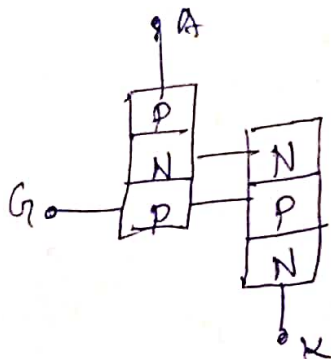
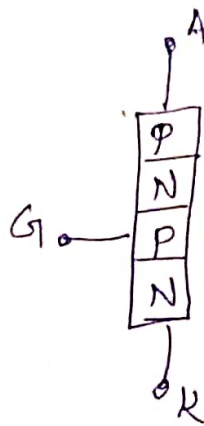
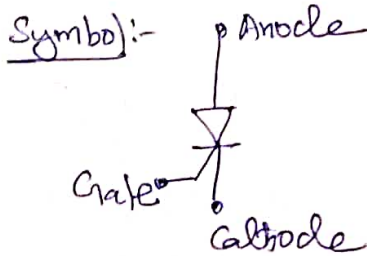
Application

- ① In computer logic circuits
- ② In level indicators and relays
- ③ In punch card readers
- ④ In alarm systems.
- ⑤ In various opto electronic systems like counting systems, smoke detector circuits, intrusion detector circuits, etc.

SCR - Silicon Controlled Rectifier:-

* An SCR consists of 4-layers of Alternating P and N type semiconductor materials.

* SCR have 3-terminals, i.e., Anode - P layer,
Cathode - N-layer,
Gate - P-layer.



T_1 - PNP
 T_2 - NPN

* Silicon is used as the Intrinsic semiconductor to which proper dopants are added.

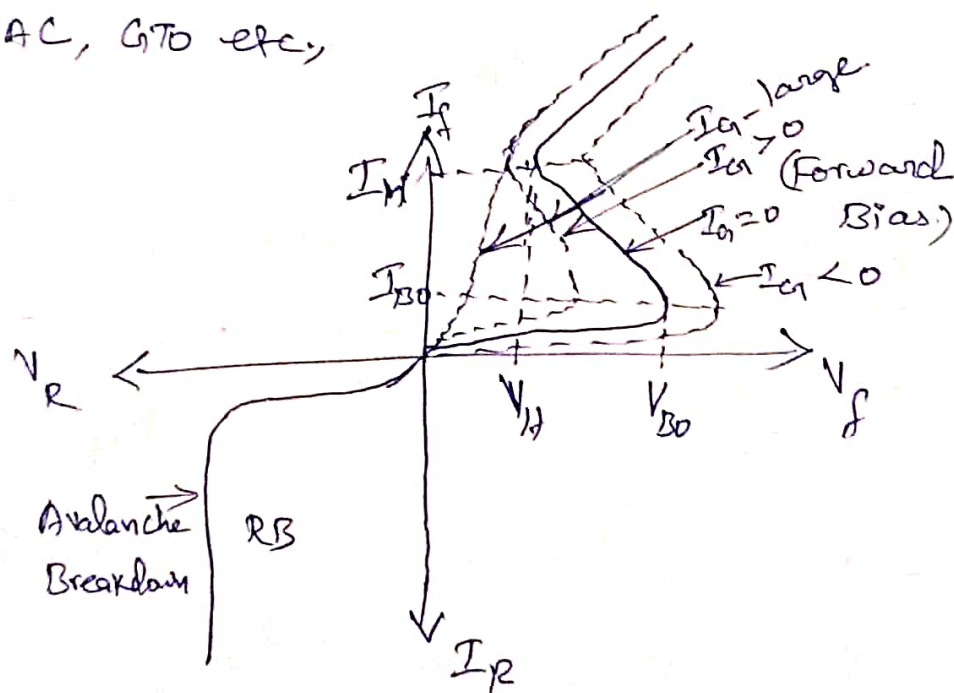
* The junctions are either diffused or alloyed.

* Since the PNPN pellet is required to handle large current.

Operation and Characteristics (V-I) of SCR:-

* Thyristor, It is a semiconductor device have 3-terminal (or) more (junction), such devices acts as a switch without any bias and can be fabricated to have voltage ratings of several hundred volt and rating from a few amperes, almost thousand amperes.

* The family of Thyristors consists of PNPN diode (Schottky diode), SCR, LASER (Light activated SCR), TRIAC, DIAC, GTO etc.,



where,
 I_H - Holding ct
 V_H - Holding Volt.
 V_{BO} - Breakdown Volt
 I_{BO} - Breakdown ct.

* SCR is a 3-terminal 4-layer semiconductor device.

* Leakage ct is very small for SCR

* SCR acts as a switch when it is forward biased

$I_g = 0$ V_g applied
 P_1-N_1 - FB
 P_2-N_2 - FB
 N_1-P_2 - RB
 I_H

* When gate is open i.e. $I_g = 0$ and anode volt. is applied junction P_1-N_1 and P_2-N_2 are forward biased where N_1-P_2 is reverse biased, only small reverse current flows.

* If we increase anode voltage further, at one stage anode current increases suddenly and voltage across the SCR falls to holding voltage V_H .

* One SCR fires (conducts), It will remain in conduction till the current through the device is reduced less than I_H , adding current by reducing applied voltage (to less than holding voltage) close to zero.

conduction
 off at
 $I < I_H$

firing angle
 range by
 I_{cr}

* The firing angle can be varied by varying the gate voltage. with very large positive gate current break over may occur at very low voltage and SCR works as if it is a normal PN diode.

Application:-

- ① SCR is mainly used in device where the control of high power possibly coupled with high volt. is demanded.
- ② In Relay control ③ In Motor control.

- ④ In phase Control
- ⑤ In Heater Control
- ⑥ In Battery Charges
- ⑦ In Inverts.
- ⑧ In Regulated power Suppliers
- ⑨ In Static Switches.
- ⑩ Their operation makes them suitable for use in medium to high voltage AC power control applications such as regulators.

Thyristor Ratings : scr called as Thyristor.

- ① Latching I_L : It is minimum current required to latch or trigger the device from its OFF state to ON state.
- ② Holding Current (I_H) : It is the minimum value of the current to hold the device in ON state (conduction).
- ③ Gate Current (I_G) :- Gate side applied current for control purposes. i.e; It is the mini I_L for trigger the device.
- ④ Voltage safety factor (V_f) :- It is the ratio of PIV and RMS value of the normal operating voltage.

$$V_f = \frac{\text{PIV (Peak Inverse Voltage)}}{\sqrt{2} \times \text{RMS value of operating voltage}} = 2 \text{ to } 2.7$$

For safe operation the normal working voltage is much below its PIV.

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