



SREENIVASA INSTITUTE OF TECHNOLOGY AND MANAGEMENT STUDIES, CHITTOOR

(AUTONOMOUS)

Approved by AICTE, New Delhi, Affiliated to JNTUA, Ananthapuramu.

ESTD: 1998

Date: 20.11.2025

B.Tech (R23) V SEMESTER (III YEAR) HONOR & MINOR - SEMESTER END EXAMINATIONS (Phase-I) – DEC-2025

Honor/Minor	Offered Department	Subject Code & Subject Title	Date and Time of the Exam
B.Tech - Honor - Electronics & Communication Engineering (VLSI)	ECE	23HRECE1 Analog IC Design	01.12.2025 Monday (2.00 PM TO 5.00 PM)
B.Tech – Minor – Internet of Things	CSE	23MRIOT1 Introduction to IOT	
B.Tech – Minor – Artificial Intelligence & Machine Learning	CSE (AIML)	23MRAIM1 Introduction to Artificial Intelligence	
B.Tech – Minor – Embedded Systems and IOT	ECE	23MRESI1 Embedded System Technology	
B.Tech – Minor – Quantum Technologies	Open	23MRQMT1 Foundations of Quantum Technologies	
B.Tech – Minor – Industrial Engineering	MECH	23MRIEM1 Production Planning and Control	


Controller of Examinations

Copy to:

1. The Dean (Academics)
2. HODs with a request to arrange to read out in the respective classrooms, and circulation among the faculty members and display on the notice board.
3. Copy to Computer Centre to upload the same in the college website
4. Copy to Maintenance, Transport and Electrical Departments


Principal