



# SREENIVASA INSTITUTE OF TECHNOLOGY AND MANAGEMENT STUDIES, CHITTOOR

(AUTONOMOUS)

Approved by AICTE, New Delhi, Affiliated to JNTUA, Ananthapuramu.

ESTD: 1998

Date: 27.04.2026

## B.Tech (R23) VI SEMESTER (III YEAR) HONOR & MINOR - SEMESTER END EXAMINATIONS (Phase-II) – MAY-2026

Schedule	B.Tech - Honor Program	B.Tech – Minor Degree Program				
	Electronics & Communication Engineering (VLSI)	Internet of Things	Artificial Intelligence AND Machine Learning	Embedded Systems and IOT	Quantum Technologies	Industrial Engineering
06.05.2026 (Wednesday) 10.00 AM to 01.00 PM	23HRECE2 Digital IC Design	23MRIOT2 Wireless Sensor Networks	23MRAIM2 Machine Learning Fundamentals	23MRESI2 Real Time Embedded Systems Design and Analysis	23MRQMT2 Solid State Physics for Quantum	23MRIEM2 Marketing Management
08.05.2026 (Friday) 10.00 AM to 01.00 PM	23HRECE3 Low Power VLSI	23MRIOT3 Communication Protocols for IoT	23MRAIM3 Deep Learning & Neural Networks	23MRESI3 Principles of IoT	23MRQMT3 Quantum Optics	23MRIEM3 Supply Chain Management

*S. N. S. 28/4/2026*  
**Controller of Examinations**

Copy to:

1. The Chief Executive
2. HODs with a request to arrange to read out in the respective classrooms, and circulation among the faculty members and display on the notice board.
3. Copy to Computer Centre to upload the same in the college website
4. Copy to Maintenance, Transport and Electrical Departments

*[Signature]*  
**Principal** 28/4/26