



SREENIVASA INSTITUTE OF TECHNOLOGY AND MANAGEMENT STUDIES, CHITTOOR

(AUTONOMOUS)

Approved by AICTE, New Delhi, Affiliated to JNTUA, Ananthapuramu.

ESTD: 1998

EXAMINATION SECTION

B.Tech (R23) IV Semester (Second Year) – Semester End Practical Exam Schedule (Regular)

Date: 06.04.2026

Exam Schedule	CIVIL	EEE	MECH	ECE	CSE	CSE (AIML)	CSE (AI)	CSE (DS)
13.04.2026 to 18.04.2026	23CIV244L Concrete Technology Lab	23EEE244L Control Systems Lab	23MEC244L Fluid Mechanics and Hydraulic Machines Lab	23ECE244L Analog and Digital Communications Lab	23CSE245L Database Management Systems Lab	23CSE245L Database Management Systems Lab	23CSE245L Database Management Systems Lab	23CSE245L Database Management Systems Lab
	23CIV245L Engineering Geology Lab	23EEE245L Induction and Synchronous Machines Lab	23MEC245L Manufacturing Processes Lab	23ECE245L Electronic Circuits Analysis Lab	23CSE247L Operating Systems Lab	23CSM243L Machine Learning Lab	23CSM242L Artificial Intelligence and Machine Learning Lab	23CSD243L Data Engineering Lab
	23HSM241L Soft Skills (SOC)	23CSE244L Data Structures and Algorithm (SOC)	23HSM241L Soft Skills (SOC)	23HSM241L Soft Skills (SOC)	23CSE246L Full Stack Development – I (SOC)	23CSE246L Full Stack Development – I (SOC)	23CSE246L Full Stack Development – I (SOC)	23CSD244L Exploratory Data Analysis with Python (SOC)
	23ESC241I* Design Thinking and Innovation	23ESC241I* Design Thinking and Innovation	23ESC241I* Design Thinking and Innovation	23ESC241I* Design Thinking and Innovation	23ESC241I* Design Thinking and Innovation	23ESC241I* Design Thinking and Innovation	23ESC241I* Design Thinking and Innovation	23ESC241I* Design Thinking and Innovation

Note: * Design Thinking and Innovation is an integrated course (blended theory and laboratory). Examiners are requested to refer to the prescribed academic guidelines for evaluation.

Controller of Examinations

Copy to:

1. The Chief Executive
2. Copy to all and a request to arrange to read in the respective classroom Circulation among the faculty members and display on the notice board.
3. Copy to Computer Centre to upload the same in the college website.

Neel
Principal



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EXAMINATION SECTION

Date: 06.04.2026

B.Tech (R20) IV Semester (Second Year) – Semester End Practical Exam Schedule (Supplementary)

Exam Schedule	CIVIL	EEE	MECH	ECE	CSE	CSE (AIML)	CSE (AI)	CSE (DS)
13.04.2026 to 18.04.2026	20CIV244 Advanced Surveying (SOC)	**	**	20ECE245 PCB Design (SOC)	20CSE244 Data Analytics using R (SAC)	20CSE244 Data Analytics using R (SAC)	20CSE244 Data Analytics using R (SAC)	20CSE244 Data Analytics using R (SAC)
	20CIV245 - Engineering Geology Lab	20EEE245 Control System Lab	20MEC245 Computer Aided Machine Drawing Lab	20ECE246 Linear IC applications Lab	20CSE245 Database Management Systems Lab	20CSM242 Artificial Intelligence: Principles and Techniques Lab	20CSM242 Artificial Intelligence: Principles and Techniques Lab	20CSE247 Software Engineering and Case Tools Lab
	20CIV246 Hydraulic Machinery Lab	20EEE246 Electrical Machines -I Lab	20MEC246 Machine Tools Technology Lab	20ECE247 Microprocessor and Interfacing Lab	20CSE246 Operating Systems Lab	20CSE245 Database Management Systems Lab	20CSE245 Database Management Systems Lab	20CSE245 Database Management Systems Lab
	20CIV247 Basic Civil Engineering Lab	20EEE247 Measurements and Instrumentation Lab	20MEC247 Thermal Engineering Lab and Automotive Systems Lab	20ECE248 Pulse and Digital circuits Lab	20CSE247 Software Engineering and Case Tools Lab	20CSE246 Operating Systems Lab	20CSE246 Operating Systems Lab	20CSE246 Operating Systems Lab


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