



**SREENIVASA INSTITUTE OF TECHNOLOGY AND MANAGEMENT STUDIES
(AUTONOMOUS)**

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

11th Board of Studies Meeting held on 14th February 2026 at 2.00 PM

Minutes of Meeting

Members Present:

1	Dr.K.M.Hemambaran	Chairman
2	Dr. S. Aruna Mastani	External Member (JNTUA, Nominee)
3	Dr.M. Bhaskar	External Member (Academics)
4	Dr. G. Umamaheswara Reddy	External Member (Academics)
5	Dr.N.Nandakumar	External Member (Industry)
6	Mr.Sravana Kumar Pulicherla	External Member (Alumni)
7	Dr.S. Vijayakumar	Internal Member
8	Prof.R.M.Sulthani	Internal Member
9	Dr.K. Yogaprasad	Internal Member
10	Dr.S.Sugumaran	Internal Member
11	Dr.C.Kavitha	Internal Member
12	Mrs.V.G.Hamsaveni	Internal Member
13	Dr. K. Varun Kumar	Internal Member
14	Mr.Y.Ravikiran Varma	Internal Member
15	Mr.S.Ashmad	Internal Member
16	Mr.S.Narendra Kumar	Internal Member
17	Mrs.B.R.Usharani	Internal Member
18	Mrs.S.P.Sasirekha	Internal Member
19	Mrs.G.Geethanjali	Internal Member
20	Ms.M.Padma	Internal Member
21	Ms.R.Anitha	Internal Member
22	Mrs. Shaik Reshma	Internal Member
23	Mrs. K. Yasmin	Internal Member

Chairman of BOS, ECE presented the R25 M.Tech VLSI-SD Course structure and syllabus of II, III and IV Semester. It is approved by the BoS members.

The following suggestions are given by the experts which may be included:

1. R25 – M.Tech Credits

The experts have recommended verifying the total credits for the R25 M.Tech program. They suggested ensuring that students are able to earn a total of 80 credits at the end of their course. This suggestion may also be considered for further evaluation and possible implementation across other M.Tech programs offered by the institution.

2. Enhancement of CMOS Mixed Signal Course

The Board suggested strengthening the CMOS Mixed Signal Design course by including digital design components within the syllabus, ensuring better integration between analog and digital domains. This will ensure students gain balanced competency in both analog and digital VLSI design.

3. Inclusion of Timing Constraints

It was recommended that timing constraints analysis be explicitly included in the course Physical Design Automation. This will enhance industry readiness in digital IC design workflows.

4. Memory Cell Design using FinFET Technology

The experts suggested incorporating memory cell design using advanced FinFET technology in the Professional elective III course Semiconductor Memory. This update aligns the syllabus with current semiconductor technology trends.

5. SoC Testing and Verification – Industry Alignment

The Board suggested strengthening the SoC Testing and Verification course by including practical exposure to contemporary testing methodologies. The curriculum should reflect current industry practices in VLSI testing.

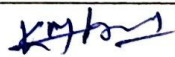
6. Power Gating and Clock Gating Techniques

It was recommended to include power gating techniques and clock gating strategies in the course Low power VLSI design.

7. Inclusion of RISC-V Processor Architecture

The Board suggested including at least one detailed case study on a RISC-V based processor, preferably highlighting an Indian processor (e.g., Shakti processor), in the course SoC architecture.

Attendance of the 11th BoS meeting held on 14.02.2026:

S.No.	Name of the member	Designation/Role	Signature
1	Dr.K.M.Hemambaran	Chairman	
2	Dr. S.Aruna Mastani	External Member (JNTUA, Nominee)	Online

3	Dr.M. Bhaskar	External Member (Academics)	Online
4	Dr. G. Umamaheswara Reddy	External Member (Academics)	Online
5	Dr.N.Nandakumar	External Member (Industry)	Online
6	Mr.Sravana Kumar Pulicherla	External Member (Alumni)	Online
7	Dr.S. Vijayakumar	Internal Member	<i>Mr. S. Vijayakumar</i>
8	Prof.R.M.Sulthani	Internal Member	<i>Prof. R. M. Sulthani</i>
9	Dr.K. Yogaprasad	Internal Member	<i>Dr. K. Yogaprasad</i>
10	Dr.S.Sugumaran	Internal Member	<i>Dr. S. Sugumaran</i>
11	Dr.C.Kavitha	Internal Member	<i>Dr. C. Kavitha</i>
12	Mrs.V.G.Hamsaveni	Internal Member	<i>Mrs. V. G. Hamsaveni</i>
13	Dr. K. Varun Kumar	Internal Member	<i>Dr. K. Varun Kumar</i>
14	Mr.Y.Ravikiran Varma	Internal Member	<i>Mr. Y. Ravikiran Varma</i>
15	Mr.S.Ashmad	Internal Member	<i>Mr. S. Ashmad</i>
16	Mr.S.Narendra Kumar	Internal Member	<i>Mr. S. Narendra Kumar</i>
17	Mrs.B.R.Usharani	Internal Member	<i>Mrs. B. R. Usharani</i>
18	Mrs.S.P.Sasirekha	Internal Member	<i>Mrs. S. P. Sasirekha</i>
19	Mrs.G.Geethanjali	Internal Member	<i>Mrs. G. Geethanjali</i>
20	Ms.M.Padma	Internal Member	<i>Ms. M. Padma</i>
21	Ms.R.Anitha	Internal Member	<i>Ms. R. Anitha</i>
22	Mrs. Shaik Reshma	Internal Member	<i>Mrs. Shaik Reshma</i>
23	Mrs. K. Yasmin	Internal Member	<i>Mrs. K. Yasmin</i>

The meeting is concluded with vote of thanks by the BoS chairman.

K.M. Hemambaran
Dr. K.M.Hemambaran
Chairman, BOS-ECE

HEAD OF THE DEPARTMENT
Dept. of Electronics & Communication Engg.
Sreenivasa Institute of Technology
and Management Studies (SITAMS)
GUNTUR - 517 127 A.P.



**SREENIVASA INSTITUTE OF TECHNOLOGY AND MANAGEMENT STUDIES,
CHITTOOR.**

(AUTONOMOUS)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CIRCULAR

Dtd.6/2/2026

This is to inform the senior members of the department that a Department Meeting will be held today at **3:00 PM** in **Room No. 402** to discuss about the **M.Tech II,III & IV semester syllabus under R25 regulations** for presenting it for **BOS approval**.

All the members are kindly requested to go through the **course structure and syllabus of the M.Tech VLSI System Design course (Second ,Third and Fourth Semester)**, which has already been shared to you for reference, and suggest any modifications are required, if required, intimate to the **Head of the Department** during the meeting.

S.No	Name of the staff	Designation	Remarks
1	Dr.C.Kavitha	Associate Professor	
2.	Dr.S.Vijay Kumar	Professor	
3	Prof.R.M.Sulthani	Professor	
4	Dr.S.Sugumaran	Professor	
5	Dr.K.M.Hemambaran	Associate professor	
6	Mrs.V.G.Hamsaveni	Associate professor	
7	Mr.Y.Ravi Kiran Varma	Assistant Professor	
8	Dr.K.Varun Kumar	Assistant Professor	

HOD/ECE



SREENIVASA INSTITUTE OF TECHNOLOGY AND MANAGEMENT STUDIES, CHITTOOR.
(AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Resolution

Dated: 6/2/2026

The following senior members are attending the today's dept meeting held in room number 402. And they gave the following suggestions regarding **M.Tech II,III & IV semester syllabus under R25 regulations** for presenting it for **BOS approval**.

S.No	Name of the staff	Designation	Remarks
1	Dr.C.Kavitha	Associate Professor	
2.	Dr.S.Vijay Kumar	Professor	
3	Dr.K.Yogaprasad	Professor	
4	Prof.R.M.Sulthani	Professor	
5	Dr.S.Sugumaran	Professor	
6	Dr.K.M.Hemambaran	Associate professor	
7	Mrs.V.G.Hamsaveni	Associate professor	
8	Mr.Y.Ravi Kiran Varma	Assistant Professor	
9	Dr.K.Varun Kumar	Assistant Professor	

COMMON COURSE STRUCTURE & SYLLABI R25 Regulations: SEMESTER-II

S.No	Course code	Name of the subject	Suggestions	Signature
1	25MTEC21T	Quantum Technologies and Applications	No change	
2	25MTVSD21T	CMOS Mixed Signal IC Design	No change	
3	25MTVSD22T	Physical Design Automation	No change	
4	25MTVSD23	Program Elective-III • SoC Testing and Verification • Semiconductor Memory Design and Testing ✓ • Advanced VLSI interconnects	- relevant tools can be added	
5	25MTVSD24	Program Elective-IV • Low Power VLSI Design • Algorithms for VLSI Design • VLSI Signal Processing	- no change	
6	25MTVSD26L	CMOS Mixed Signal IC Design Lab	No change	
7	25MTVSD27L	Physical Design Automation Lab	No change	
8	25MTVSD25P	Comprehensive Viva Voce		
9	25MTMAC21	Audit Course-II • Pedagogy Studies • Personality Development Through Life Enlightenment Skills * Yoga For Stress Management	- No change	

COMMON COURSE STRUCTURE & SYLLABI R25 Regulations: SEMESTER-III

S.No	Course code	Name of the subject	Suggestions	Signature
1	OE-MOOC	Open Elective – I		
	25MTOCS31A	Advanced Data Structures and Algorithms		
	25MTOCS31B	Cloud Computing		
	25MTOPE31A	Photovoltaic Systems		
	25MTOPD31A	Integrated Product Design and Development		
2	25MTVSD31	Professional Core Theory - V		
	25MTVSD31A	Bi-CMOS Technology and Applications	NO change is needed	M.L.
	25MTVSD31B	Optimization Techniques and Applications in VLSI Design		
	25MTVSD31C	SoC Architecture	- Real time RISC processors	
3	25MTVSD32P	Dissertation Phase – I	Preferably industrial project	
4	25MTVSD33P	Industry Internship		
5	25MTVSD34L	Co-curricular Activities		

COMMON COURSE STRUCTURE & SYLLABI R25 Regulations: SEMESTER-IV

S.No	Course code	Name of the subject	Suggestions	Signature
1	25MTVSD41P	Dissertation Phase – II	Cont. of phase - I	

[Signature]
HOD/ECE



hod ece <hodece@sitams.org>

Minutes of 11th BoS -VLSI-SD9R25)-ECE - Approval - Reg

Dr.S.Aruna Mastani <aruna_mastani.ece@jntua.ac.in>
To: hod ece <hodece@sitams.org>

Wed, Feb 18, 2026 at 8:16 PM

Dear Hemambaran

I have gone through the modification made and approve the same
[Quoted text hidden]



hod ece <hodece@sitams.org>

Minutes of 11th BoS -VLSI-SD(R25)-ECE - Approved - Reg

bhaskar@nitt.edu <bhaskar@nitt.edu>
To: hod ece <hodece@sitams.org>

Thu, Feb 19, 2026 at 10:39 AM

Dear Sir/Madam,
The minutes of the 11th Board of Studies meeting is approved
Thanks and regards
Dr. M.Bhaskar
Professor, Dept. of ECE
NIT, Tiruchirappalli
9443314616

Quoting hod ece <hodece@sitams.org>:

Respected Sir,

Greetings!

My sincere thanks to you for attending the *11th Board of Studies meeting* of the Department of Electronics and Communication Engineering, SITAMS, held on 14*-2-2026 at 2:00 PM via Google Meet*.

Please find attached herewith the following documents for your kind perusal:

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Minutes of the 11th BoS Meeting
-

M.Tech R25 Course Structure
-

M.Tech I year IIsem, second year III & IV semester Syllabus

I kindly request you to review the documents and approve the same by replying to this mail.*

Thank you for your valuable time and support.

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Thanks and Regards

Dr.K M Hemambaran,
Associate Professor & Head
Department of Electronics and Communication Engineering
Sreenivasa Institute of Technology and Management Studies
Chittoor - 517127

Mail: hodece@sitams.org



hod ece <hodece@sitams.org>

Minutes of 11th BoS -VLSI-SD(R25)-ECE - Approval - Reg

Umamaheswara Reddy <umaskit@gmail.com>
o: hod ece <hodece@sitams.org>

Wed, Feb 18, 2026 at 9:49 PM

Dear Sir

The Minutes of the **11th Board of Studies meeting** of the Department of Electronics and Communication Engineering, SITAMS, held on **14-2-2026 at 2:00 PM via Google Meet**, M.Tech R25 Course Structure and M.Tech I year II Sem, second year III & IV Semester Syllabuses are approved.

Thank you Sir

With best regards

Dr. G. Umamaheswara Reddy
Professor, Department of ECE
S. V. University College of Engineering
S.V. University, Tirupati - 517502
Andhra Pradesh, India.
Mobile: +91-7013224147, 7097192109.