SREENIVASA INSTITUTE OF TECHNOLOGY AND MANAGEMENT STUDIES, CHITTOOR
(AUTONOMOUS)
Approved by AICTE, New Delhi, Affiliated to JNTUA, Ananthapuramu.
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

12th Board of Studies

AGENDA

Date: 24-08-2026
Time: 3.00 PM

1. **R26 Regulations-UG.**
2. **I Year B.Tech - I & II Semester Course Structure**
3. **Electronics and Electrical Engineering Workshop - Syllabi.**
4. **Panel of External Examiners for Theory Examination Question Paper Setting.**
5. **Any other item.**

25/8/2026

BOS CHAIRMAN
DEPARTMENT OF ECE

HEAD OF THE DEPARTMENT
Dept. of Electronics & Communication Engg.
Sreenivasa Institute of Technology
and Management Studies (SITAMS)
CHITTOOR - 517 127 A.P

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List of 12th BOS Meeting Members

S. No.	Name	Designation / Role	Signature
1	Dr. K. Yogaprasad	Chairman	
2	Dr. S. Aruna Mastani	External Member (JNTUA, Nominee)	ONLINE
3	Dr. M. Bhaskar	External Member (Academics)	ONLINE
4	Dr. G. Umamaheswara Reddy	External Member (Academics)	ONLINE
5	Mr G Hemachandra	External Member (Industry)	ONLINE
6	Mr. Jinka Anilkumar	External Member (Alumni)	ONLINE
7	Dr. S. Vijaya Kumar	Internal Member	
8	Prof. R. M. Sulthani	Internal Member	
9	Dr. S. Sugumaran	Internal Member	
10	Dr.Munipraveena Rela	Internal Member	
11	Dr.D.Bright Anand	Internal Member	
12	Dr. K. M. Hemambaran	Internal Member	
13	Dr.T.Senthil	Internal Member	
14	Dr. K. Varun Kumar	Internal Member	
15	Mrs. V. G. Hamsaveni	Internal Member	
16	Mr. Y. Ravikiran Varma	Internal Member	
17	Mr. S. Ashmad	Internal Member	
18	Mr. S. Narendra Kumar	Internal Member	
19	Mrs.B.R.Usha Rani	Internal Member	
20	Mrs. G. Geethanjali	Internal Member	
21	Mrs.S.P.Sasirekha	Internal Member	
22	Mrs. R. Anitha	Internal Member	
23	Ms.M.Padma	Internal Member	
24	Mr.D.Gowtham	Internal Member	
25	Mrs. Shaik Reshma	Internal Member	
26	Mrs. K. Yasmin	Internal Member	
27	Mr.K.Ramadurai	Internal Member	
28	Mrs.K.Dhanusha	Internal Member	
29	Mrs.V.R.Chandini	Internal Member	
30	Mrs.P.Sreekavya	Internal Member	
31	Mrs. Sai Chandana	Internal Member	
32	Mrs.K.Sowjanya	Internal Member	

Proposed R26 – Academic Regulations – Highlights

Particulars	Guidelines / Requirements
AI Integration	Assignments shall be designed to incorporate AI-integrated elements of the syllabus to promote applied and experiential learning. Assignments shall be in the form of problems, mini projects, design problems, slip tests, quizzes etc., depending on the course content.
Massive Open Online Courses	To promote self-learning and lifelong learning, every student shall mandatorily complete at least one MOOC course before the commencement of the IV Year II Semester. In addition, the Fifth Professional Elective (PE-V) and the Fourth Open Elective (OE-IV) shall be mandatorily completed through the SWAYAM/SWAYAM Plus platforms.
Credit Transfer Policy	The institution shall offer credit mobility for MOOCs and give the equivalent credit weightage to the students for the credits earned through online learning courses.
Academic Bank of Credits	The University has implemented Academic Bank of Credits(ABC) to promote flexibility in curriculum as per NEP 2020 to execute Multiple Entry and Exit system with credit count, credit transfer and credit acceptance from students' account.
Audit Courses	Courses like Environmental Sciences, Indian Knowledge System, Cyber security Awareness for Engineers, Gender Sensitization, Technical Paper Writing & IPR are offered as non-credit mandatory courses for all the undergraduate students.
Skill Oriented Courses	There shall be five skill-oriented courses offered during III to VII semesters.

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Proposed R26 – Academic Regulations – Highlights

Particulars	Guidelines / Requirements
Year/Semester of Study	04 Years of Study & 8 Semesters – Regular Students 03 Years of Study & 6 Semesters – Lateral Students
Award of the Degree	Course of study for not less than 4 academic years and not more than 8 academic years – Regular Admitted Students Course of study for not less than 3 academic years and not more than 6 academic years – Lateral Admitted Students
Credit Requirements	B.Tech – Regular - 171 Credits Required to Award of the Degree B.Tech – Lateral - 131 Credits Required to Award of the Degree Minor Degree - Additionally 18 Credit Required Above 171 Credits Honours Degree - Additionally 18 Credit Required Above 171 Credits
Credit Definition	01 Hr Lecture – 01 Credit 01 Hr Tutorial – 01 Credit 01 Hr Practice/Practical – 0.5 Credit
Instruction Period	Minimum number of instruction days in each semester is 90 days.

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Proposed R26 – Academic Regulations – Highlights

Particulars	Guidelines / Requirements
Mandatory Internships	Two summer internships with a minimum of 06-08 weeks duration (2 credits each), done at the end of second and third years, respectively are mandatory. In the final semester, the student should mandatorily register and undergo internship and in parallel he/she should work on a project with well-defined objectives.
Student Development Program (SDP)	Each Department shall design and conduct a Department-Wise Workshop relevant to its discipline for all B.Tech. students. The programme shall carry Zero Credits and shall be conducted at the end of the V Semester.
Gap Year Concept	Gap year concept for Student Entrepreneur in Residence is introduced and outstanding students who wish to pursue entrepreneurship / become entrepreneur are allowed to take a break of one year at any time after II year to pursue full-time entrepreneurship program / to establish start-ups. This period may be extended to two years at the most and these two years would not be counted for the time for the maximum time for graduation.
Multiple Entry / Exit Option	As per NEP 2020, there shall be multiple entry and exist with suitable certification / degree, However The Universities shall resolve any issues that may arise in the implementation of Multiple Entry and Exit policies from time to time.
Minor/Honors Degree	A student shall earn additional 18 credits (5 theory courses and 2 laboratory / a project courses) for award of B.Tech.(Minors/Honors) degree.

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Proposed R26 – Academic Regulations – Highlights

Particulars	Guidelines / Requirements
Attendance Requirements	A student shall be eligible to appear for the External examinations if he/she acquires a minimum of 40% attendance in each subject and 75% of attendance in aggregate of all the subjects. Condonation of shortage of attendance in aggregate up to 10% (65% and above and below 75%) Shortage of Attendance below 65% in aggregate shall in NOCASE be condoned
Promotion Rules	A student will be promoted from II to III year if he/she fulfills the academic requirement of securing 40% of the credits (any decimal fraction should be rounded off to lower digit) up to in the subjects that have been studied up to III Semester. A student shall be promoted from III year to IV year if he/she fulfills the academicrequirementsofsecuring40% of the credits (any decimal fraction should be rounded off to lower digit) in the subjects that have been studied upto V semester

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Proposed R26 – Academic Regulations – Highlights

As a measure of the student's performance, a 10-point Absolute Grading System using the following Letter Grades and corresponding percentage of marks shall be followed

Range in which the marks in the subject fall	Grade	Grade Points Assigned
90 &above	S (Superior)	10
80-89	A(Excellent)	9
70-79	B(Very Good)	8
60-69	C(Good)	7
50-59	D(Average)	6
40-49	E(Below Average)	5
< 40	F(Fail)	0
Absent	AB (Absent)	0

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Proposed R26 – Academic Regulations – Highlights

After a student has satisfied the requirements prescribed for the completion of the program and is eligible for the award of B. Tech. Degree, he/she shall be placed in one of the following classes

Class Awarded	CGPA Secured
First Class with Distinction	≥7.5
First Class	≥6.5<7.5
Second Class	≥5.5 <6.5
Pass Class	≥4.5 <5.5

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DEPARTMENT OF Electronics and Communication Engineering
B.Tech R26 - COURSE STRUCTURE AND SYLLABI

Semester I (First Year)

S.No	Course Code	Course Title	Scheme of Instructions Hours per Week					Scheme of Examination Maximum Marks		
			L	T	Pr	P	C	I	E	Total
1		Physics for Communication Systems	2	1	0	0	3	30	70	100
2		Linear Algebra and Calculus	3	0	2	0	4	30	70	100
3		Computational Thinking and Problem Solving with Python	2	1	0	0	3	30	70	100
4		Design Thinking	1	0	4	0	3	30	70	100
5		AI Tools and Applications	1	0	4	0	3	30	70	100
6		Physics for Communication Systems Lab	0	0	0	3	1.5	30	70	100
7		Computational Thinking and Problem Solving with Python Lab	0	0	0	3	1.5	30	70	100
8		Engineering Workshop	0	0	0	3	1.5	30	70	100
9		Health and wellness, Yoga and Sports	0	0	0	1	0.5	100	-	100
Contact Hours per week			9	2	10	10	-	-	-	-
Total Hours per week			31				-	-	-	-
Total credits							21	-	-	-
Total Marks								340	560	900

Semester II (First Year)

S.No	Course Code	Course Title	Scheme of Instructions Hours per Week					Scheme of Examination Maximum Marks		
			L	T	Pr	P	C	I	E	Total
1		Communicative English	2	0	0	0	2	30	70	100
2		Engineering Chemistry for Electronics and Communication Systems	2	1	0	0	3	30	70	100
3		Differential Equations and Vector Calculus	3	0	2	0	4	30	70	100
4		Engineering Graphics	1	0	4	0	3	30	70	100
5		Network Analysis	2	1	0	0	3	30	70	100
6		Communicative English Lab	0	0	0	2	1	30	70	100
7		Electronics and Electrical Engineering Workshop	0	0	0	2	1	30	70	100
8		Engineering Chemistry for Electronics and Communication Systems Lab	0	0	0	3	1.5	30	70	100
9		NSS/NCC/Scouts and Guides / Community Service	0	0	0	1	0.5	100	--	100
		Cybersecurity Awareness for Engineers	2	0	0	0	--	30	--	--
Contact Hours per week			12	1	8	8	-	-	-	-
Total Hours per week			29				-	-	-	-
Total credits							19	-	-	-
Total Marks								370	560	900

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IB.Tech.- IISemester

		L	T	P	C
26ECE	Electronics and Electrical Engineering Workshop	-	-	2	1

PRE-REQUISITES:Basic Physics and Mathematics (Class XII Level AP Intermediate): no concurrent prerequisite.

COURSEEDUCATIONALOBJECTIVES:

1. Gain hands-on proficiency in workshop tools, measuring instruments, and electronic components through assembly, testing, and theoretical vs. measured value comparison.
2. Understand electrical systems and implement verified DC and AC circuits on breadboard and PCB experimentally.
3. Measure and interpret electrical parameters — real power, power factor, earth resistance, and energy consumption — using standard instruments such as wattmeter, Megger, and energy meter.
4. Apply Python and AI/LLM tools to automate data analysis, simulate circuit behavior, and validate experimental results through regression and predictive modeling.
5. Design a socially relevant mini project using AI/LLM tools and Python/MATLAB for simulation, analysis, and prediction.

List of Activities:

Part A – Regular Activities

1. Familiarization of commonly used workshop tools like breadboard, soldering iron, relays, switches, connectors, fuses, cutters, pliers, wire stripper, etc.; exercises for proper handling and usage.
2. Familiarization with voltmeters, ammeters, multimeter, LCR meter, power supplies, CRO/DSO, function generator, and frequency counter; perform basic measurement experiments.
3. Identification of components (R, L, C, diodes, transistors, ICs) — type, symbol, colour coding, and package; testing and comparison of theoretical vs. measured values.
4. Study of fundamental electrical concepts — voltage, current, power, AC/DC, single-phase, three-phase; understanding the working principles of transformers, generators, and motors.
5. Calculate energy consumption of domestic appliances and/or a premises using standard formulas; record and tabulate energy usage data for a given set of loads.
6. Measure real power and power factor of a single-phase AC circuit using a wattmeter; tabulate readings for different load conditions.
7. Measure earth resistance of the laboratory grounding electrode using a Megger instrument; tabulate readings and compare with permissible safety limits.

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8. Assemble any two circuits — one with a DC source and one with an AC source — on a breadboard and test their functionality; observe and record output waveforms or measured parameters.
9. Assemble any two circuits — one with a DC source and one with an AC source — on a PCB and test their performance; compare measured results with expected values.
10. Design and implement an electronic or hobby circuit with a societal application (e.g., energy monitoring system, sensor-based automation, fault detection system); demonstrate working functionality.

Part B – AI Integration Lab Exercises

Any **five** activities must be implemented using appropriate AI/LLM tools and/or Python/MATLAB-based simulations.

Activity 1

a) Use AI/LLM tools to query tool datasheets and manufacturer specifications; generate an interactive Q&A to identify tool functions, correct handling postures, and safety hazards for each workshop tool.

b) Use LLM-generated safety checklists to create a Python-based scoring tool that evaluates a student's handling procedure against a standard rubric.

Activity 2

a) Use AI/LLM tools to explain the operating procedures of each instrument (CRO/DSO, LCR meter, function generator) and to interpret measurement results — including scale reading, range selection, and source of error.

b) Use Python (NumPy/Matplotlib) to plot measured voltage and current data; apply linear regression to verify Ohm's Law and estimate percentage error from ideal values.

Activity 3

a) Use AI/LLM tools for AI-assisted component identification — describe a component by its markings or package and receive datasheet summary, pin-out, and typical application; understand colour-code decoding for resistors and capacitors.

b) Use Python (Scikit-learn) regression to estimate device parameters (static resistance, capacitance, inductance) from LCR meter readings and compare with datasheet nominal values.

Activity 4

a) Use AI/LLM tools to relate fundamental electrical concepts (voltage, current, power factor, single/three-phase systems) to real-world applications — household wiring, industrial motors, and transformer ratings; generate concept maps.

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strategy, and final report generation including abstract, block diagram description, and conclusion.

b) Integrate Python/MATLAB for data acquisition, analysis, or prediction within the project (e.g., regression-based energy forecasting, threshold-based fault classifier, sensor signal processing); include model training, validation, and result visualization.

AI Integration: AI integration covers five selected activities:

(i) Python-based tool safety scoring;

(ii) Ohm's Law verification via NumPy / Matplotlib linear regression;

(iii) LCR meter data regression for component parameters;

(iv) Python phase or diagrams and transformer modeling;

(v) linear regression-based energy prediction — each reinforcing the link between physical experimentation and computational analysis.

Free / Open-Source AI Tools:

Python (NumPy, Matplotlib, Scikit-learn) – Google Colab (free); LT spice / Falstad Circuit Simulator (free) – circuit simulation; Virtual Labs – IIT (free) – online electrical experiments.

COURSE OUTCOMES:

On successful completion of the course, students will be able to		**POs**
CO1	Identify, handle, and test workshop tools, instruments, and electronic components; assemble and verify DC and AC circuits on breadboard and PCB using appropriate measuring instruments.	**PO1,PO2, PO3, PO4, PO5, PO6, PO7, PO8, PO9, PO10**
CO2	Calculate electrical energy consumption and power factor; implement and validate circuits through simulation and hardware, comparing experimental results with theoretical values.	**PO1,PO2, PO3, PO4, PO5,PO6, PO8, PO9, PO10**
CO3	Apply AI/LLM and/or Python/MATLAB tools for data analysis, simulation, and report generation; design and demonstrate a mini project with societal application integrating AI and hardware.	**PO1,PO2, PO3, PO4, PO5,PO6, PO7, PO8, PO9, PO10, PO11**
CO4	Measure earth resistance, real power, and power factor using standard instruments (Megger, wattmeter); compare readings against IS/IEEE permissible limits and interpret results.	**PO1,PO2, PO3, PO4, PO5,PO6, PO7, PO8, PO9, PO10**
CO5	Automate measurement data processing, energy prediction, and fault detection using Python (NumPy/Matplotlib/Scikit-learn); evaluate model accuracy using appropriate metrics.	**PO1,PO2, PO3, PO4, PO5,PO6, PO7, PO8, PO9, PO10, PO11**

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b) Use Python to simulate single-phase and three-phase phasor diagrams (Matplotlib); model transformer turns ratio and calculate secondary voltage for given primary inputs.

Activity 5

a) Use AI/LLM tools to explain energy billing calculations, units (kWh), tariff slabs, and the impact of power factor on energy cost; generate a structured energy audit report for a given premises.

b) Use Python (Google Colab/Scikit-learn) to model and predict monthly energy consumption using linear regression; input appliance wattage and daily usage hours as features.

Activity 6

a) Use AI/LLM tools to explain power factor, reactive power compensation, and the effect of capacitive/inductive loads; auto-generate a power quality summary from wattmeter readings.

b) Use Python/MATLAB to plot real power, reactive power, and power factor against load variation; apply regression to model the relationship between load impedance and power factor.

Activity 7

a) Use AI/LLM tools to explain earth resistance standards (IS 3043/IEEE 80), the fall-of-potential method used by Megger, and the significance of low earth resistance for human safety; auto-generate a safety compliance report.

b) Use Python to analyse tabulated Megger readings; flag values exceeding safety thresholds using a rule-based classifier and plot resistance vs. electrode depth.

Activity 8

a) Use AI/LLM tools to interpret the circuit schematic, suggest component values, and debug functional faults based on described symptoms (e.g., no output, distorted waveform, wrong DC level).

b) Use Python/MATLAB to simulate both the DC and AC circuits before hardware implementation; compare simulated waveforms or voltage levels with measured breadboard results.

Activity 9

a) Use AI/LLM tools for PCB layout guidance — component placement rules, trace width calculation for current capacity, and design-rule-check (DRC) criteria; generate a bill of materials (BOM) from the schematic.

b) Validate the PCB design using MATLAB/Python simulation prior to fabrication; compare simulated and measured performance and quantify deviation using percentage error analysis.

Activity 10

a) Use AI/LLM tools throughout the project lifecycle — problem statement framing, circuit design justification, component selection rationale, testing

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CO-PO MAPPING:

CO\PO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PSO1	PSO2
CO1	3	2	2	3	2	2	1	2	2	2	-	3	2
CO2	3	3	3	3	2	1	-	1	2	2	-	3	2
CO3	3	3	3	3	3	2	1	1	2	2	1	3	3
CO4	2	3	2	3	3	2	1	2	2	2	-	2	2
CO5	2	3	3	3	3	1	1	1	2	3	1	2	3
CO*	2.6	2.8	2.6	3	2.6	1.6	1	1.4	2	2.2	1	2.6	2.4

Recommended Free & Open-Source AI / Computational Tools:

1. Python (NumPy, Matplotlib, Scikit-learn) on Google Colab (free) – energy prediction, Ohm's Law regression, component parameter estimation
2. Scikit-learn (free, open-source) – linear regression and rule-based classifiers for energy forecasting and fault detection
3. LT spice / Falstad Circuit Simulator (free) – pre-hardware simulation of breadboard and PCB circuits
4. Virtual Labs – IIT (free): https://vlab.co.in – online simulation of electrical measurement experiments
5. Libre Office Calc (free, open-source) – standardized data recording templates for all activities

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Date: 25-8-2026

12th BOS Meeting – Minutes of Meeting

Held on 24th August 2026 at 3:00 PM

The 12th BOS Meeting of the Department of Electronics and Communication Engineering (ECE) was held on 24th August 2026 at 3:00 PM, chaired by the BOS Chairman. The BOS Chairman presented the **R26 Regulation Course Structure for I and II Semesters** and the **Electronics and Electrical Engineering Workshop** syllabus for **I B.Tech II Semester** discussion and approval. The R26 Regulation course completion rules and procedures for regular and lateral-entry students were also presented and discussed.

The BOS members accepted and approved the proposed course structure, syllabus, revised statements, and course completion procedures with the following suggestions and recommendations:

1. Approval of R26 Regulation Course Structure and Syllabus:
The BOS members reviewed the proposed R26 Regulation, B.Tech I year course structure, and **Electronics and Electrical Engineering Workshop syllabus** framed for the ECE Department. After detailed discussion, the members accepted and approved.

2. Recommendation for Faculty Training in AI:
The External BOS members suggested that faculty members should complete suitable AI-based courses aligned with the R26 Regulation. It was recommended that such training would enhance the faculty members' knowledge and teaching capabilities and enable them to effectively deliver AI-related concepts to students.

3. Recommendation for Prompt Engineering Course:
The Industrial and Alumni BOS members recommended introducing a Prompt Engineering workshops. The workshop was suggested to develop students' skills in effective prompt design and the practical use of AI tools, thereby enhancing their employability, industry readiness, and ability to work with emerging AI technologies.

4. Panel of External Examiners for Theory Examination Question Paper Setting:
The Panel of External Examiners for Theory Examination Question Paper Setting was placed before the Board of Studies. After due consideration, the Board accepted and approved the proposed panel for setting theory examination question papers in accordance with the prescribed syllabus, examination pattern, and academic standards.

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