SREENIVASA INSTITUTE OF TECHNOLOGY & MANAGEMENT STUDIES (AUTONOMOUS) CHITTOOR
I YEAR M.TECH I SEMESTER REGULAR /SUPPLEMENTARY (R20) –FEBRUARY/MARCH-2024 EXAMINATIONS

R20

Time-table   Dt: 16-02-2024

DATE / TIME	Power Electronics (EEE)	Product Design (ME)	VLSI System Design (ECE)	CSE
24-02-2024 (2.00 PM to 5.00 PM) (Saturday)	Analysis of Power Electronic Converter (20 MPE011)	Creativity in Product Design (20 MPDD11)	CMOS VLSI and Digital IC Design (20MVSD11)	Mathematical Foundations of Computer Science (20MCSE11)
26-02-2024 (2.00 PM to 1.00 PM) (Monday)	Power Electronic Control of DC Drives (20 MPE012)	Computer Applications in Product Design (20 MPDD 12)	Analog IC Design (20 MVSD12)	Advanced Data Structures and Algorithms (20 MCSE 12)
28-02-2024 (2.00 PM to 5.00 PM) (Wednesday)	Control System Design for Power Electronics (20 MEPE 13)	Material Selection for Product Development (20 MPDD 13)	FPGA and HDL Programming (20 MVSD13)	Java and Web Technologies (20 MCSE 13)
01-03-2024 (2.00 PM to 5.00 PM (Friday)	Research Methodology and IPR (20 MMAC11)	Research Methodology and IPR (20 MMAC 11)	Research Methodology and IPR (20 MMAC 11)	Research Methodology and IPR (20 MMAC 11)
04-03-2024 (2.00 PM to 5.00 PM (Monday)	Power Quality Improvement (20 MPE 015 D)	Design for Manufacture Assembly and Environments (20 MPDD14 A)	Advanced Digital System Design (20 MVSD14D)	Artificial Intelligence (20 MCSE 14 B)/ Wireless Sensor Network(20MCSE14A)
06-03-2024 (2.00 PM to 5.00 PM (Wednesday)	Solar Energy Conversion System (20 MPE016B)	Design of Hybrid ad Electric Vehicles (20 MPDD15 C)	Design for Testability and Testing (20 MVSD15A)	Advanced Compiler Design (20 MCSE 15 B)

Controller of examinations 15/02/2024   Principal

Copy to
1) The Dean (Academics)
2) The HODs of EEE, MECH, ECE, CSE: with a request to arrange to read out in the respective class rooms and circulation among the faculty members and display on the notice board.
3) Copy to Computer Centre to upload the same in the college website.
4) Copy to Maintenance, Transport and Electrical Departments.

SREENIVASA INSTITUTE OF TECHNOLOGY & MANAGEMENT STUDIES (AUTONOMOUS) CHITTOOR

I YEAR M.TECH II SEM SUPPLEMENTARY END EXAMINATIONS (R20) – FEBRUARY/MARCH , 2024
Time Table   16-02-2024

R20

DATE / TIME	CSE	EEE (PE)	ECE (VLSI Systems Design)	MECH (PD)
27-02-2024 (Tuesday) 2.00 PM t0 5.00 PM	Data Warehousing and Mining (20MCSE21)	Modern Rectifiers And Resonant Converters (20MPE021)	Low Power VLSI Design (20MVSD21)	Integrated Product Design and Development (20MPDD21)
29-02-2024 (Thursday) 2.00 PM t0 5.00 PM	Soft Computing (20MCSE22)	Power Electronic Control Of AC drives (20MPE0 22)	ASIC Design (20MVSD22	Additive Manufacturing Processes (20MPDD22)
02-03-2024 (Saturday) 2.00 PM t0 5.00 PM	Big Data Analytics (20MCSE23)	Digital Control Of Power Electronics And Drives (20MPE0 23)	Scripting Languages for HDL (20MVSD23)	Quality Concepts in Product Design (20MPDD23)
05-03-2024 (Tuesday) 2.00 PM t0 5.00 PM	Optimization Techniques (20MCSE24)	Modern Power Electronics (20MPE024)	CAD for VLSI(20MVSD24)	Industrial Design and Ergonomics (20MPDD24)
07-03-2024 (Thursday) 2.00 PM t0 5.00 PM	Cryptography and Network Security (20MCSE25C) (Core Elective – III)	Advanced Power Semiconductor Devices and Protection (20MPE025A) (Core Elective – III)	Microelectronics (20MVSD25C)	Product Innovation and Management (20MPDD25A) (Core Elective – III)
09-03-2024 (Saturday) 2.00 PM t0 5.00 PM	Smart Sensors and Internet of Things (20MCSE26D) (Core Elective – IV)	Advanced Drives and Control (20MPE026B) (Core Elective – IV)	Advanced Computer Architecture (20MVSD26B)	Marketing Research for New Product (20MPDD26A) Core Elective - IV

ONTROLLER OF EXAMINATIONS   PRINCIPAL

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